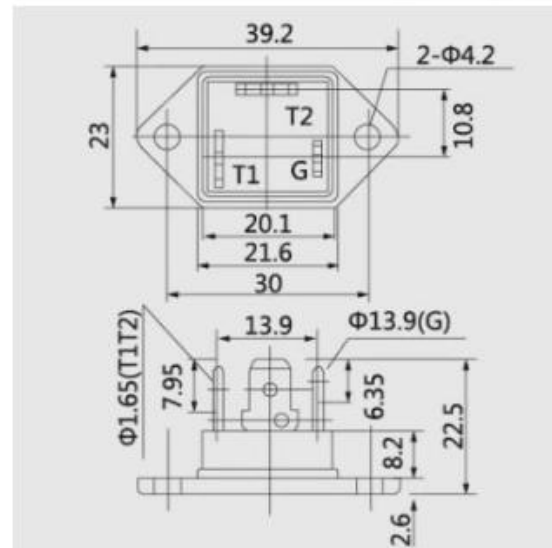


**isc Triacs**
**BTA60-800B**
**FEATURES**

- With TOP3 insulated package
- Suitable for general purpose where high surge current capability is required.  
Application such as phase control and static switching on inductive or resistive load.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

 ABSOLUTE MAXIMUM RATINGS( $T_a=25^{\circ}\text{C}$ )

SYMBOL	PARAMETER	MIN	UNIT
$V_{\text{DRM}}$	Repetitive peak off-state voltage	800	V
$V_{\text{RRM}}$	Repetitive peak reverse voltage	800	V
$I_{\text{T(RMS)}}$	RMS on-state current (full sine wave) $T_j=80^{\circ}\text{C}$	60	A
$I_{\text{TSM}}$	Non-repetitive peak on-state current $t_p=20\text{ms}$	900	A
$T_j$	Operating junction temperature	-40-125	$^{\circ}\text{C}$
$T_{\text{stg}}$	Storage temperature	-40~150	$^{\circ}\text{C}$
$P_{\text{G(AV)}}$	Average gate power dissipation( $T_j=125^{\circ}\text{C}$ )	1	W
$R_{\text{th(j-c)}}$	Thermal resistance, junction to case	0.9	$^{\circ}\text{C}/\text{W}$



**ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C unless otherwise specified)**

SYMBOL	PARAMETER		CONDITIONS	MAX	UNIT
I <sub>RRM</sub>	Repetitive peak reverse current		V <sub>R</sub> =V <sub>RRM</sub> , V <sub>R</sub> =V <sub>RRM</sub> , T <sub>j</sub> =125°C	0.02 2.0	mA
I <sub>DRM</sub>	Repetitive peak off-state current		V <sub>D</sub> =V <sub>DRM</sub> , V <sub>D</sub> =V <sub>DRM</sub> , T <sub>j</sub> =125°C	0.02 2.0	mA
I <sub>GT</sub>	Gate trigger current	I	V <sub>D</sub> =12V; R <sub>L</sub> = 100 Ω	50	mA
		II		50	
		III		50	
		IV		90	
I <sub>H</sub>	Holding current		I <sub>GT</sub> = 0.5A, Gate Open	80	mA
V <sub>GT</sub>	Gate trigger voltage all quadrant		V <sub>D</sub> =12V; R <sub>L</sub> = 100 Ω	1.5	V
V <sub>TM</sub>	On-state voltage		I <sub>TM</sub> = 120A;	1.55	V

**NOTICE:**

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