

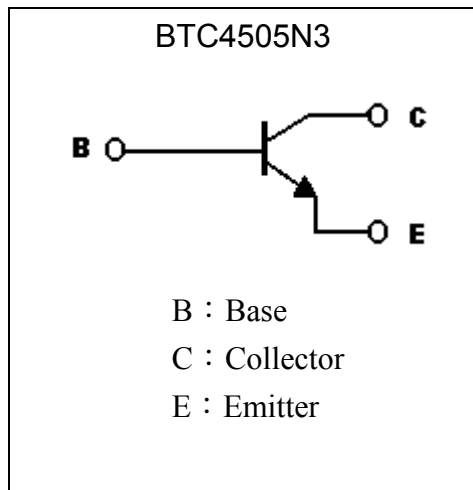
High Voltage NPN Epitaxial Planar Transistor

BTC4505N3

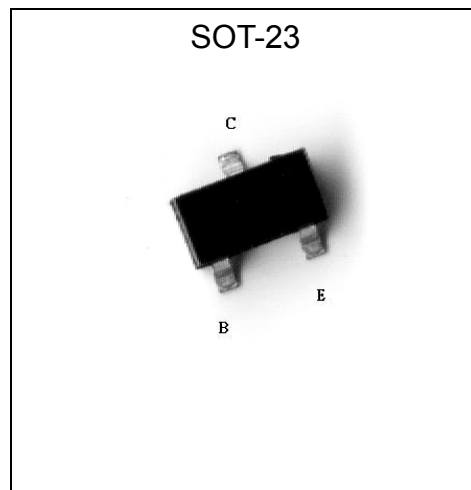
Features

- High breakdown voltage. ($BV_{CEO} \geq 400V$)
- Low saturation voltage
- Complementary to BTA1759N3
- Pb-free lead plating and halogen-free package

Symbol

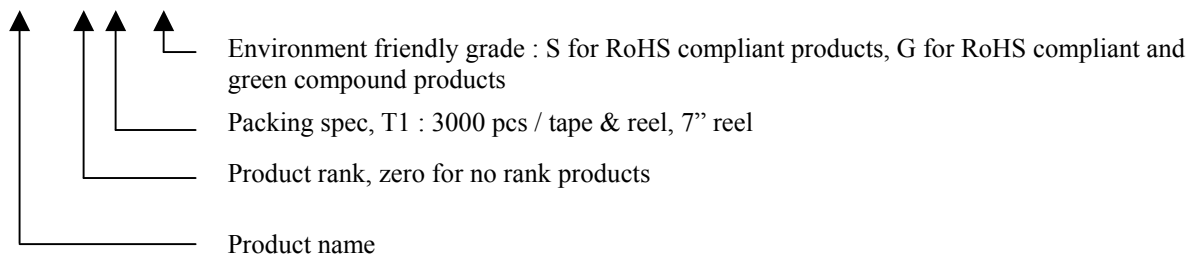


Outline



Ordering Information

Device	Package	Shipping
BTC4505N3-0-T1-G	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel



**Absolute Maximum Ratings** (Ta=25°C)

Parameter	Symbol	Limit	Unit
Collector-Base Voltage	V _{CBO}	400	V
Collector-Emitter Voltage	V _{CEO}	400	V
Emitter-Base Voltage	V _{EBO}	6	V
Collector Current	I _C	300	mA
Power Dissipation	P _d	0.225	W
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	185	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{th,j-a}	556	

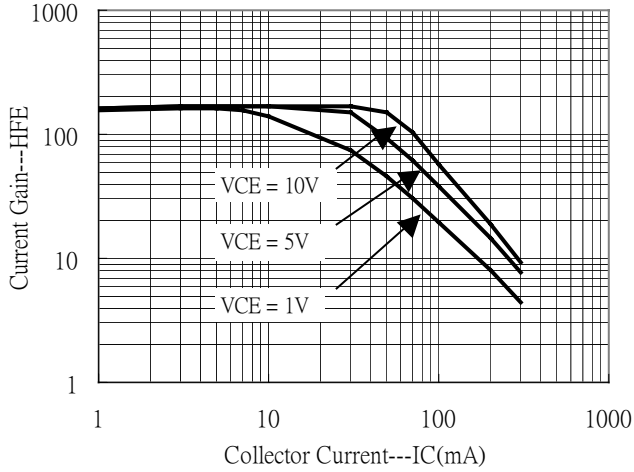
Characteristics (Ta=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CBO}	400	-	-	V	I _C =50μA, I _E =0
BV _{CEO}	400	-	-	V	I _C =1mA, I _B =0
BV _{EBO}	6	-	-	V	I _E =50μA, I _C =0
I _{CBO}	-	-	100	nA	V _{CB} =400V, I _E =0
I _{CER}	-	-	10	nA	V _{CE} =300V, R _{EB} =4kΩ
I _{EBO}	-	-	100	nA	V _{EB} =6V, I _C =0
*V _{CE(sat)}	-	-	0.5	V	I _C =10mA, I _B =1mA
*V _{BE(sat)}	-	-	1.5	V	I _C =10mA, I _B =1mA
h _{FE}	100	-	270	-	V _{CE} =10V, I _C =10mA
f _T	-	20	-	MHz	V _{CE} =10V, I _C =10mA, f=10MHz
Cob	-	7	-	pF	V _{CB} =10V, f=1MHz

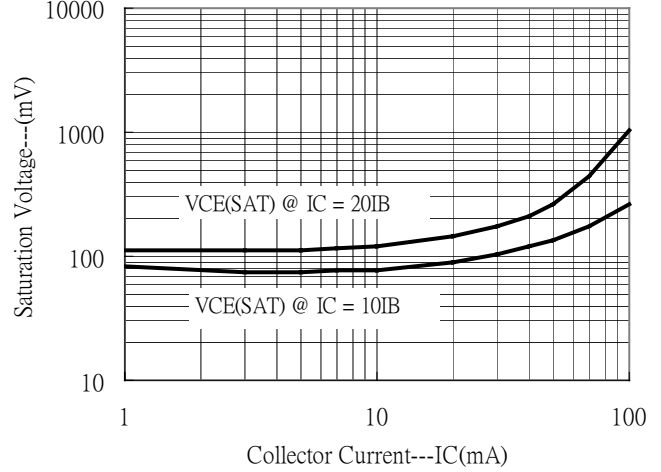
*Pulse Test : Pulse Width ≤380μs, Duty Cycle≤2%

Typical Characteristics

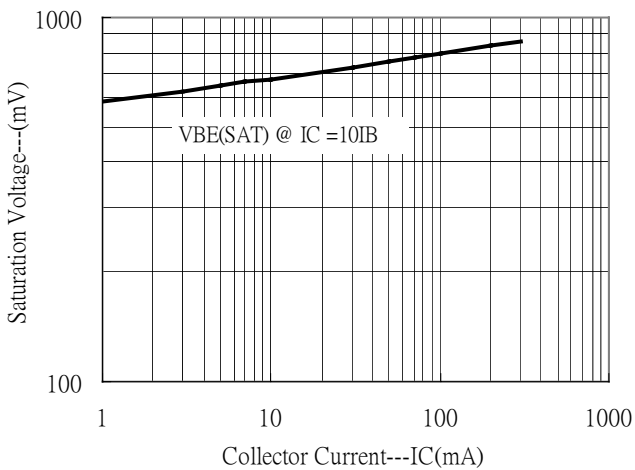
Current Gain vs Collector Current



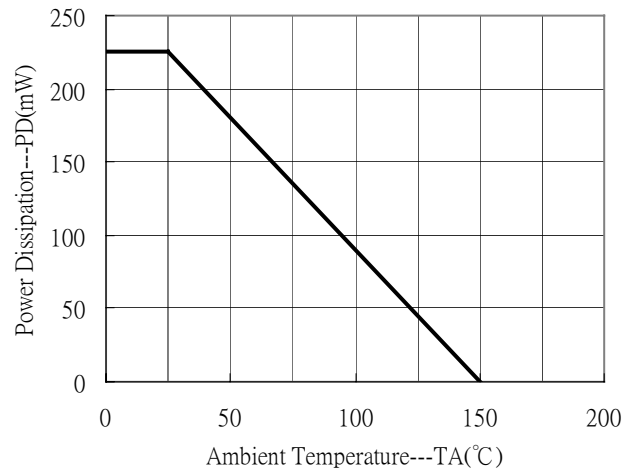
Saturation Voltage vs Collector Current



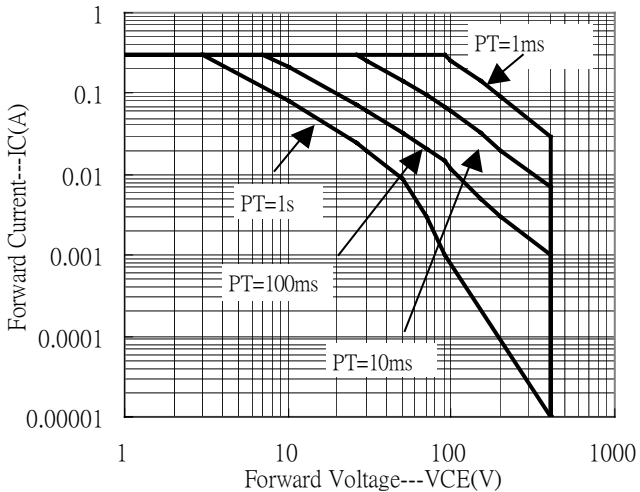
Saturation Voltage vs Collector Current



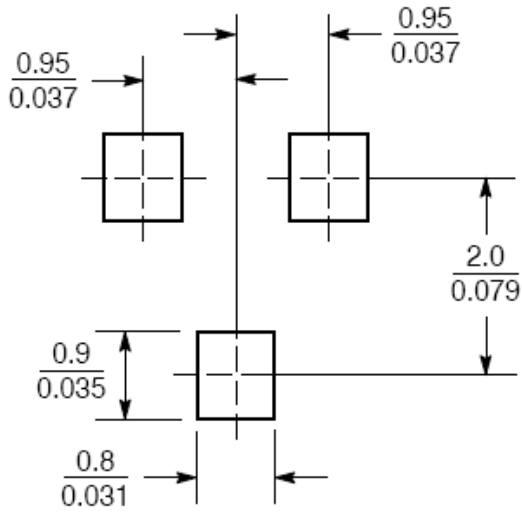
Power Derating Curve



Safe Operating Area

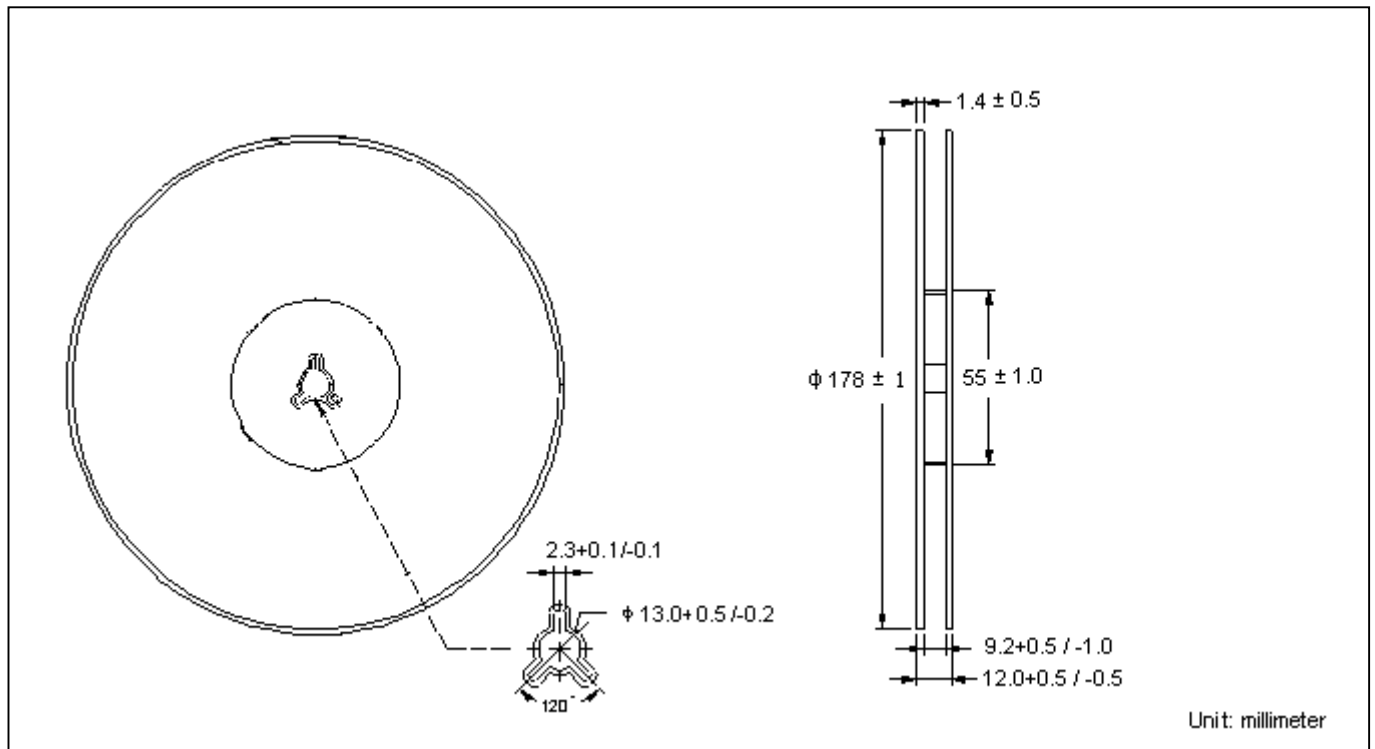


Recommended Soldering Footprint

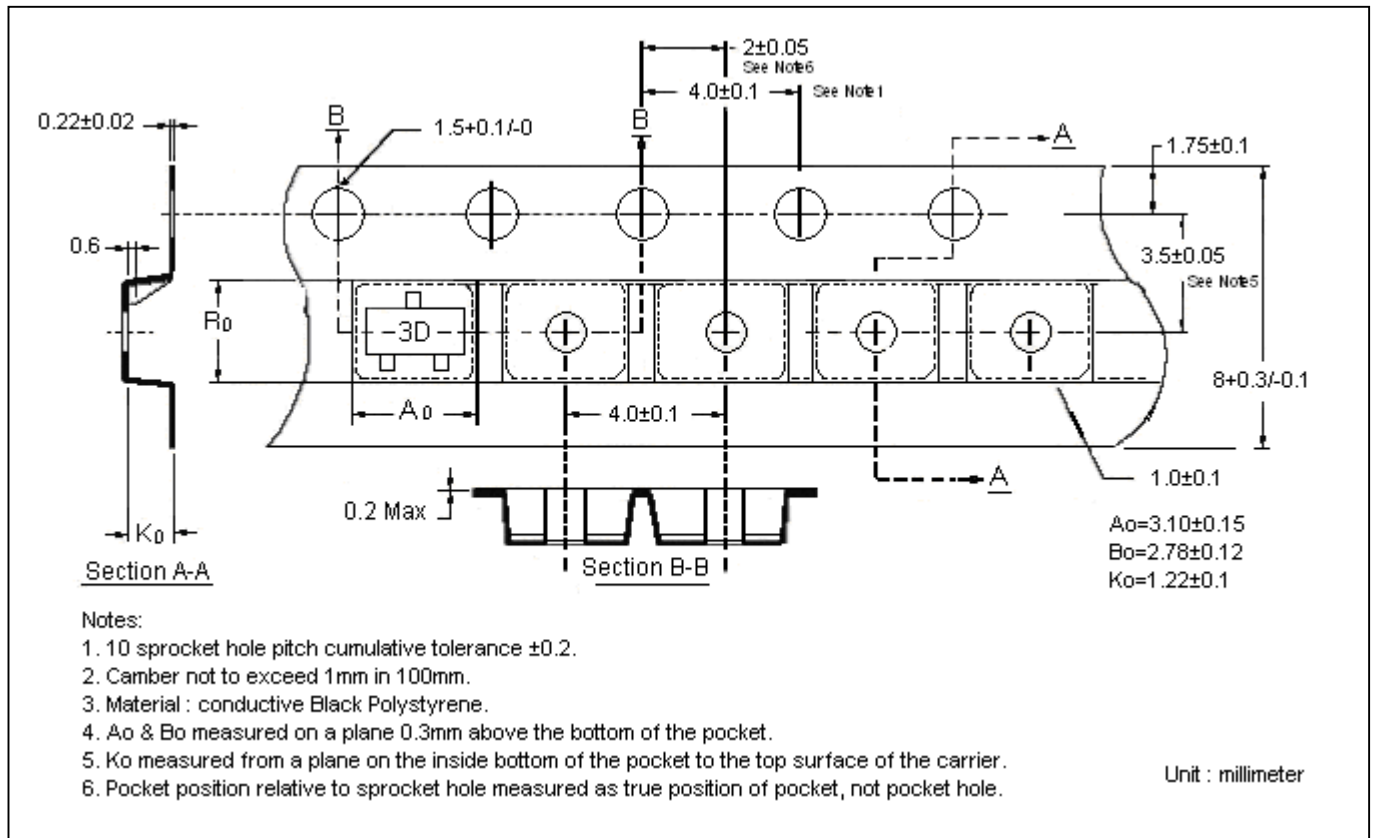


Unit : $\frac{\text{mm}}{\text{inches}}$

Reel Dimension



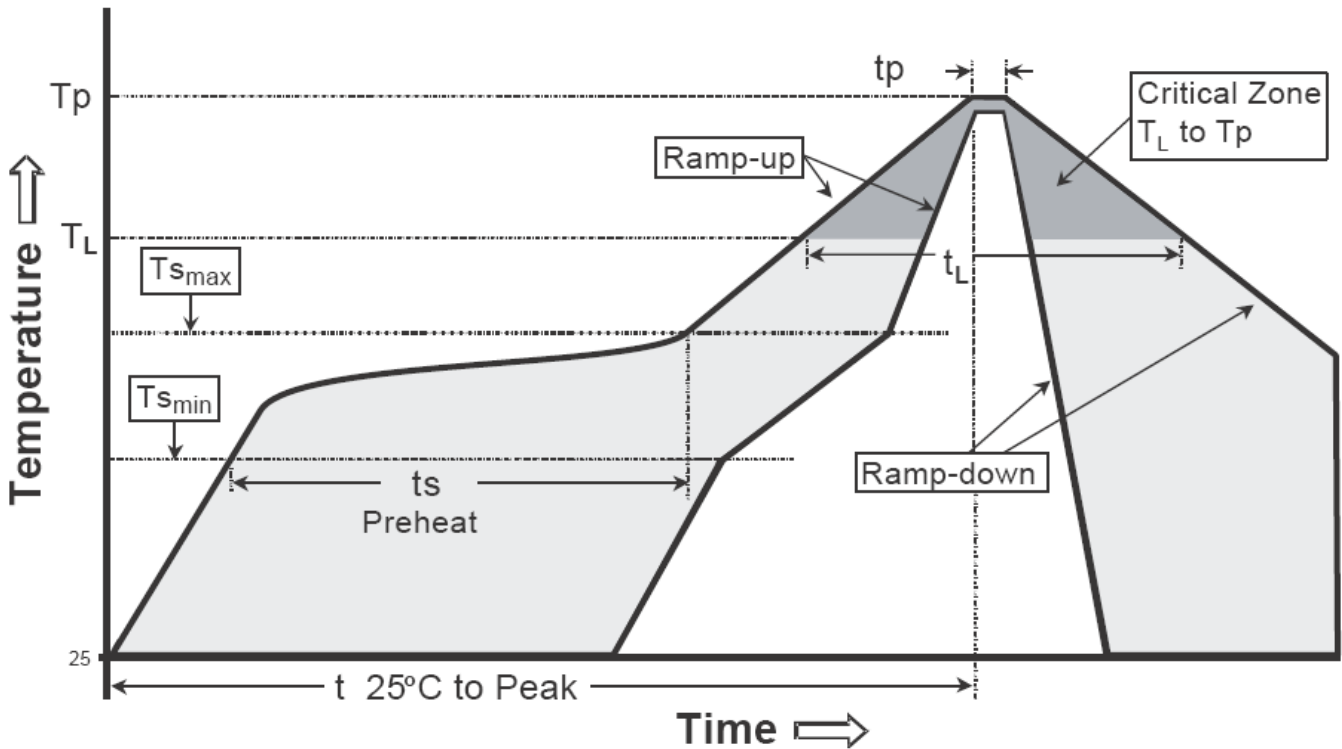
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

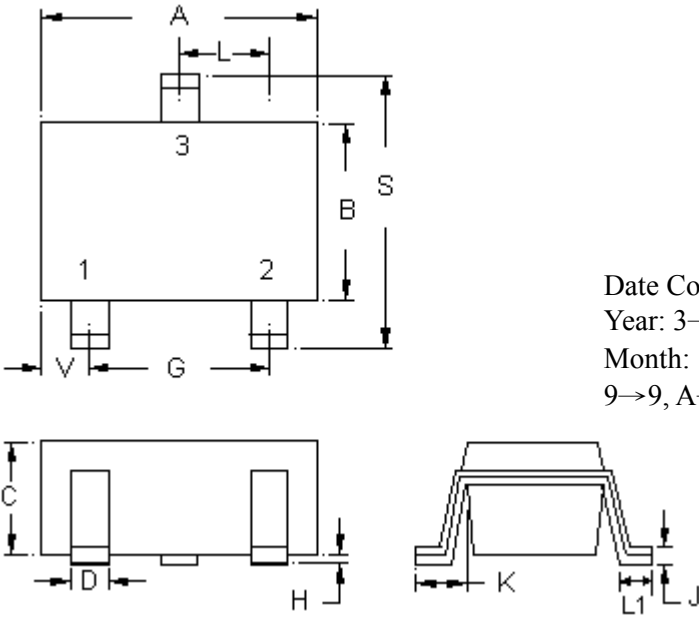
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

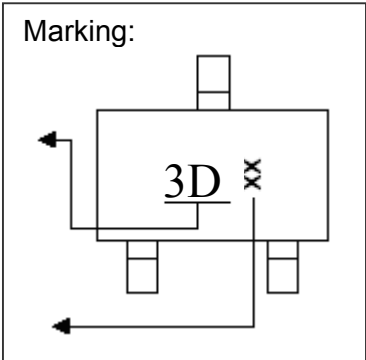
Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-23 Dimension



The diagram shows three views of the SOT-23 package: a top view with dimensions A, L, S, B, V, G, 1, 2, 3; a side view with dimensions C, D, H, J; and a perspective view with dimensions K, L1, L2, J.

Marking:



The marking diagram shows a rectangular package with three leads. The top lead is labeled '3D' and the bottom lead is labeled 'XX'. Arrows indicate the lead positions.

Product Code

Date Code: Year+Month
 Year: 3→2003, 4→2004
 Month: 1→1, 2→2, . . .
 9→9, A→10, B→11, C→12

3-Lead SOT-23 Plastic Surface Mounted Package
 CYStek Package Code: N3

Style : Pin 1.Base 2.Emitter 3.Collector

*:Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1102	0.1204	2.80	3.04	J	0.0032	0.0079	0.08	0.20
B	0.0472	0.0551	1.20	1.40	K	0.0118	0.0266	0.30	0.67
C	0.0335	0.0512	0.89	1.30	L	0.0335	0.0453	0.85	1.15
D	0.0118	0.0197	0.30	0.50	S	0.0830	0.1004	2.10	2.55
G	0.0669	0.0910	1.70	2.30	V	0.0098	0.0256	0.25	0.65
H	0.0000	0.0040	0.00	0.10	L1	0.0118	0.0197	0.30	0.50

Notes : 1.Controlling dimension : millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material :

- Lead : Pure tin plated..
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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