

Silicon NPN Power Transistors

BU508AX

DESCRIPTION

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- With TO-3PML package
- High voltage
- High speed switching

APPLICATIONS

- For use in horizontal deflection circuits of colour TV receivers.

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

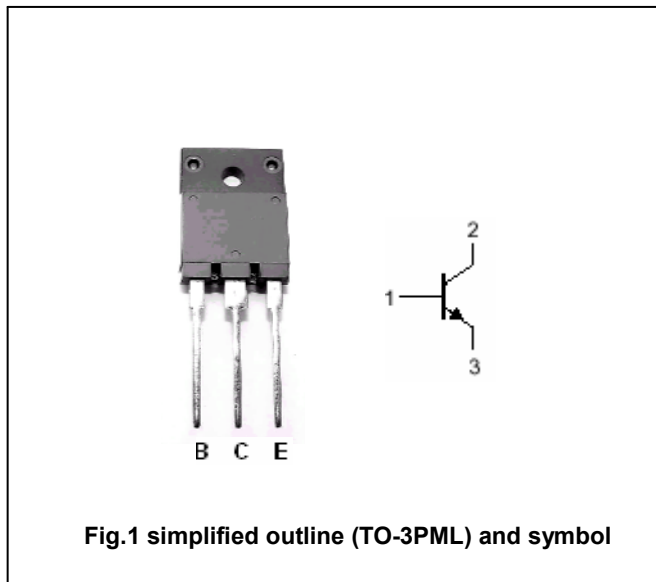


Fig.1 simplified outline (TO-3PML) and symbol

Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	1500	V
V _{CEO}	Collector-emitter voltage	Open base	700	V
I _C	Collector current (DC)		8	A
I _{CM}	Collector current (Pulse)		15	A
I _B	Base current (DC)		4	A
I _{BM}	Base current (Pulse)		6	A
P _{tot}	Total power dissipation	T _C =25°C	45	W
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-65~150	°C

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CHARACTERISTICS

T_j=25°C unless otherwise specified

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SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =100mA ; I _B =0, L=25mH	700			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =4.5A ; I _B =1.6A			1.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =4.5A ; I _B =2A			1.1	V
I _{CES}	Collector cut-off current	V _{CE} =Rated V _{CE} ; V _{BE} =0 T _C =125°C			1.0 2.0	mA
I _{EBO}	Emitter cut-off current	V _{EB} =6.0V; I _C =0			10	mA
h _{FE}	DC current gain	I _C =100mA ; V _{CE} =5V	6	13	30	
f _T	Transition frequency	I _E =0.1A ; V _{CE} =5V		7		MHz
C _{ob}	Output capacitance	V _{CB} =10V; I _E =0; f=1.0MHz		125		pF

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PACKAGE OUTLINE

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Fig.2 Outline dimensions