

(Polarity detection for both S and N features dual outputs)

# **BU52058GWZ**

#### General Description

The BU52058GWZ is omnipolar Hall IC incorporating a polarity determination circuit that enables operation (output) on both the S- and N-poles, with the polarity judgment based on the output processing configuration. This Hall IC product can be in with movie, mobile phone and other applications involving crystal panels to detect the (front-back) location or determine the rotational direction of the panel.

#### Features

- Omnipolar detection (polarity detection for both S and N features dual outputs)
- Micro power operation (small current using intermittent operation method)
- Ultra-compact CSP4 package (UCSP35L1)
- Polarity judgment and output on both poles (OUT1: S-pole output; OUT2: N-pole output)
- High ESD resistance 8kV(HBM)

#### Applications

Mobile phones, notebook computers, digital video camera, digital still camera, etc.

# Block Diagram, Pin Configuration and Pin Description

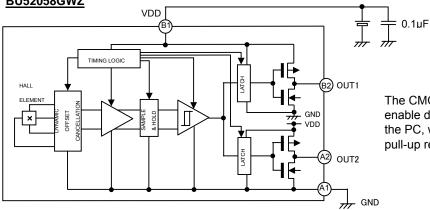
### Key Specifications

- VCC voltage range: Operate point: Hysteresis:
- Period:
- Supply current (AVG):
- Output type:
- Operating temperature range:

#### Package W(Typ.) x D(Typ.) x H(Max.)



# BU52058GWZ



Adjust the bypass capacitor value as necessary, according to voltage noise conditions, etc.

The CMOS output terminals enable direct connection to the PC, with no external pull-up resistor required.

PIN No.	PIN NAME	FUNCTION	COMMENT		
A1	GND	GROUND		A1 A2	A2 A1
A2	OUT2	OUTPUT (respond the north pole)			
B1	VDD	POWER SUPPLY		B1 B2	B2 B1
B2	OUT1	OUTPUT (respond the south pole)		<u>Surface</u>	<u>Reverse</u>

OProduct structure : Silicon monolithic integrated circuit OThis product is not designed protection against radioactive rays



1.65V to 3.6V

+/-3.0mT(Typ.)

0.9mT(Typ.)

50ms(Typ.)

5.0µA (Typ.)

-40°C to +85°C

CMOS

# Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit	Remarks
Power Supply Voltage	V <sub>DD</sub>	-0.1 to +4.5 <sup>*1</sup>	V	
Output Current	lout	±0.5	mA	
Power Dissipation	Pd	100	mW	Reduced by 1.00mW for each increase in Ta of 1°C over 25 (mounted on 24mm × 20mm Glass-epoxy PCB)
Operating Temperature Range	T <sub>opr</sub>	-40 to +85	°C	
Storage Temperature Range	T <sub>stg</sub>	-40 to +125	°C	

\*1. Not to exceed Pd

### Magnetic, Electrical characteristics

BU52058GWZ (Unless otherwise specified, VDD=1.80V, Ta=25°C)

PARAMETERS	SYMBOL	LIMIT		UNIT	CONDITIONS		
FARAIVIETERS	STMBOL	MIN	TYP	MAX	UNIT	CONDITIONS	
Power Supply Voltage	V <sub>DD</sub>	1.65	1.80	3.60	V		
Operate Deint	B <sub>opS</sub>	-	3.0	5.0	mT	OUTPUT : OUT1 (respond the south pole)	
Operate Point	B <sub>opN</sub>	-5.0	-3.0	-	(I) I	OUTPUT : OUT2 (respond the north pole)	
Release Point	B <sub>rpS</sub>	0.6	2.1	-	mT	OUTPUT : OUT1 (respond the south pole)	
	B <sub>rpN</sub>	-	-2.1	-0.6		OUTPUT : OUT2 (respond the north pole)	
Unstaracia	B <sub>hysS</sub>	-	0.9	-	mT		
Hysteresis	B <sub>hysN</sub>	-	0.9	-			
Period	Tp	-	50	100	ms		
Output High Voltage	V <sub>OH</sub>	V <sub>DD</sub> -0.2	-	-	V	$B_{rpN}$ <b<b_{rps} <math="">*^2 I<sub>OUT</sub> =-0.5mA</b<b_{rps}>	
Output Low Voltage	V <sub>OL</sub>	-	-	0.2	V	B <b<sub>opN, B<sub>opS</sub><b <sup="">*2 I<sub>OUT</sub> =+0.5mA</b></b<sub>	
Supply Current	I <sub>DD(AVG)</sub>	-	5	8	μΑ	Average	
Supply Current During Startup Time	I <sub>DD(EN)</sub>	-	2.8	-	mA	During Startup Time Value	
Supply Current During Standby Time	I <sub>DD(DIS)</sub>	-	1.8	-	μA	During Standby Time Value	

%2. B = Magnetic flux density

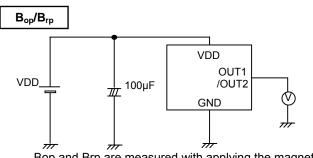
1mT=10Gauss

Positive ("+") polarity flux is defined as the magnetic flux from south pole which is direct toward to

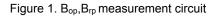
the branded face of the sensor.

After applying power supply, it takes one cycle of period  $(T_P)$  to become definite output.

# •Figure of measurement circuit



Bop and Brp are measured with applying the magnetic field from the outside.



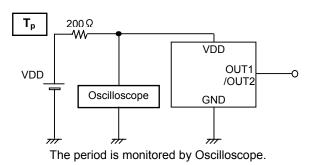


Figure 2. Tp measurement circuit

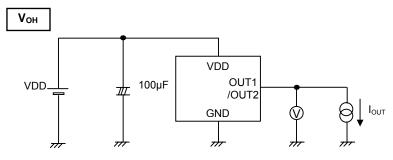


Figure 3. V<sub>OH</sub> measurement circuit

Product Name	I <sub>OUT</sub>
BU52058GWZ	0.5mA

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BU52058GWZ	0.5mA

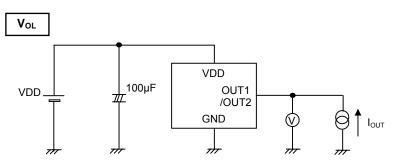


Figure 4.  $V_{OL}$  measurement circuit

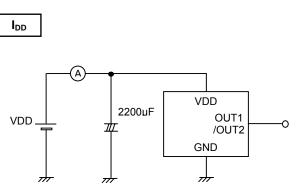


Figure 5. I<sub>DD</sub> measurement circuit

# Typical Performance Curves

# BU52058GWZ

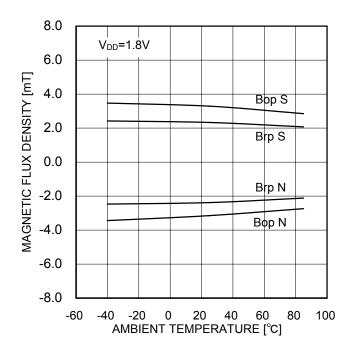


Figure 6. Bop,Brp – Ambient temperature

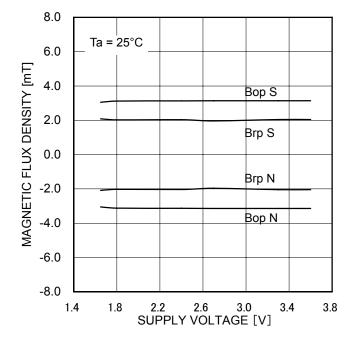


Figure 7. Bop,Brp – Supply voltage

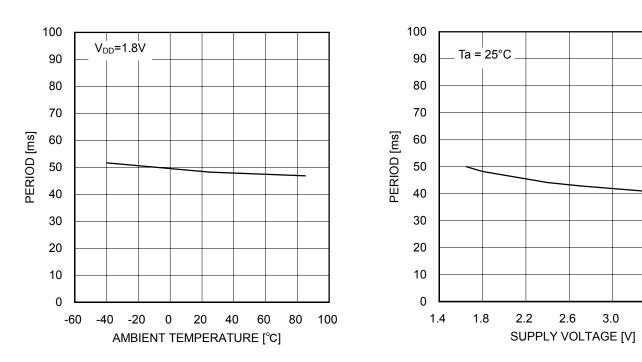


Figure 8. T<sub>P</sub>– Ambient temperature

Figure 9. T<sub>P</sub>– Supply voltage

Datasheet

3.4

3.8

# ●Typical Performance Curves -continued

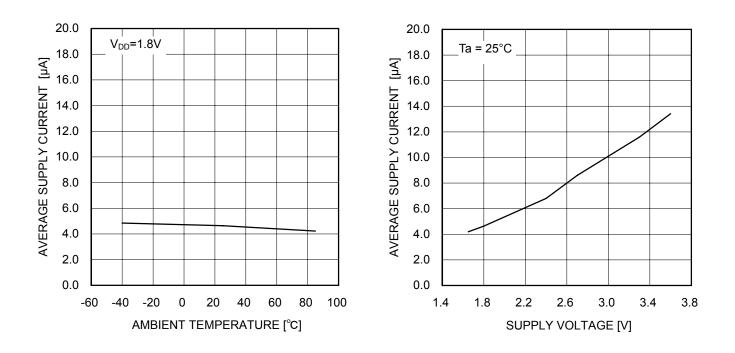
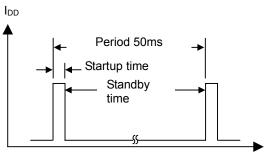


Figure 10. I<sub>DD</sub> – Ambient temperature

Figure 11.  $I_{DD}$  – Supply

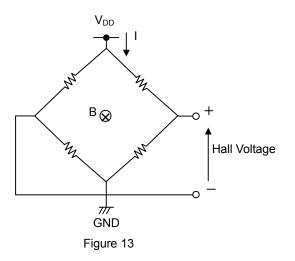
# Description of Operations

Micropower Operation (Small current using intermittent action)





(Offset Cancellation)



The dual output omnipolar detection Hall IC adopts an intermittent operation method to save energy. At startup, the Hall elements, amp, comparator and other detection circuits power ON and magnetic detection begins. During standby, the detection circuits power OFF, thereby reducing current consumption. The detection results are held while standby is active, and then output.

Reference period: 50ms (MAX100ms) Reference startup time: 48µs

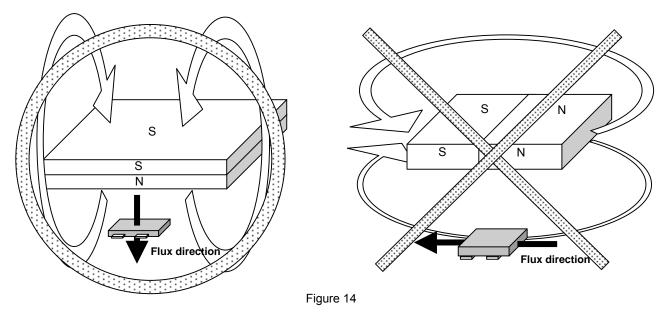
The Hall elements form an equivalent Wheatstone (resistor) bridge circuit. Offset voltage may be generated by a differential in this bridge resistance, or can arise from changes in resistance due to package or bonding stress. A dynamic offset cancellation circuit is employed to cancel this offset voltage.

When Hall elements are connected as shown in Figure 13 and a magnetic field is applied perpendicular to the Hall elements, voltage is generated at the mid-point terminal of the bridge. This is known as Hall voltage.

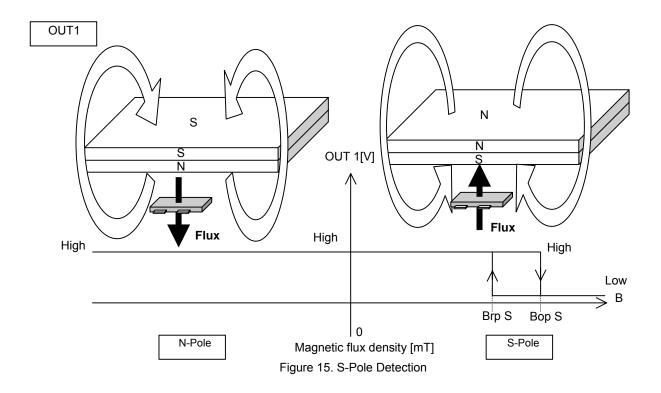
Dynamic cancellation switches the wiring (shown in the figure) to redirect the current flow to a 90° angle from its original path, and thereby cancels the Hall voltage.

The magnetic signal (only) is maintained in the sample/hold circuit during the offset cancellation process and then released.

(Magnetic Field Detection Mechanism)

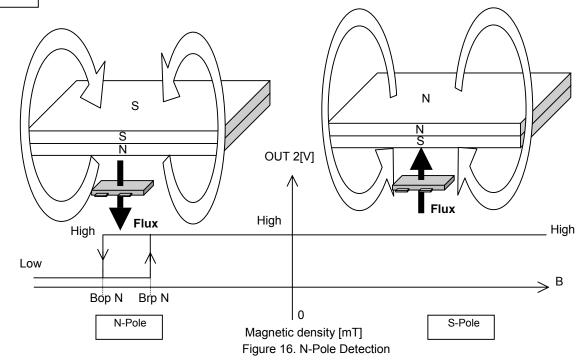


The Hall IC cannot detect magnetic fields that run horizontal to the package top layer. Be certain to configure the Hall IC so that the magnetic field is perpendicular to the top layer.



The OUT1 pin detects and outputs for the S-pole only. Since it is unipolar, it does not recognize the N-pole.

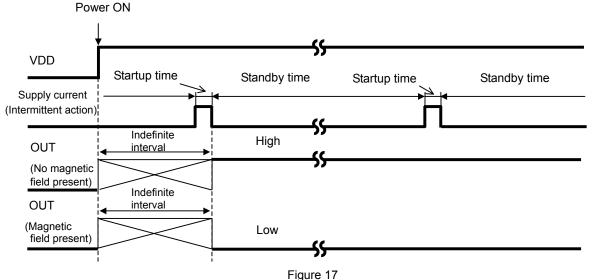
# OUT2



The OUT2 pin detects and outputs for the N-pole only. Since it is unipolar, it does not recognize the S-pole.

The dual output Omnipolar detection Hall IC detects magnetic fields running perpendicular to the top surface of the package. There is an inverse relationship between magnetic flux density and the distance separating the magnet and the Hall IC: when distance increases magnetic density falls. When it drops below the operate point (Bop), output goes HIGH. When the magnet gets closer to the IC and magnetic density rises, to the operate point, the output switches LOW. In LOW output mode, the distance from the magnet to the IC increases again until the magnetic density falls to a point just below Bop, and output returns HIGH. (This point, where magnetic flux density restores HIGH output, is known as the release point, Brp.) This detection and adjustment mechanism is designed to prevent noise, oscillation and other erratic system operation.

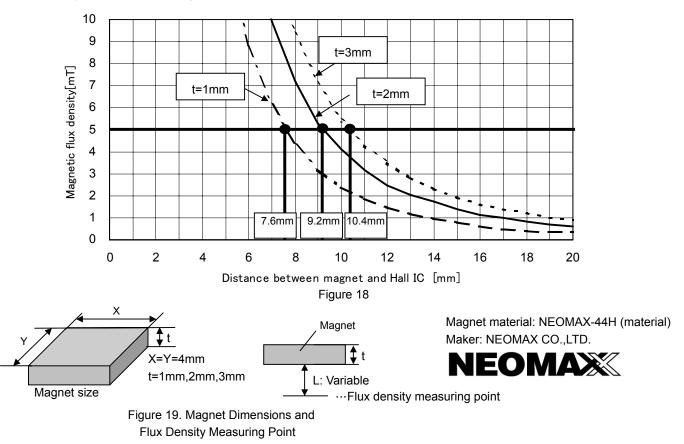
# Intermittent Operation at Power ON



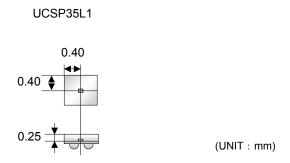
The dual output Omnipolar detection Hall IC adopts an intermittent operation method in detecting the magnetic field during startup, as shown in Figure 17. It outputs to the appropriate terminal based on the detection result and maintains the output condition during the standby period. The time from power ON until the end of the initial startup period is an indefinite interval, but it cannot exceed the maximum period, 100ms. To accommodate the system design, the Hall IC output read should be programmed within 100ms of power ON, but after the time allowed for the period ambient temperature and supply voltage.

### Magnet Selection

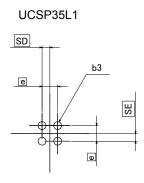
Of the two representative varieties of permanent magnet, neodymium generally offers greater magnetic power per volume than ferrite, thereby enabling the highest degree of miniaturization, thus, neodymium is best suited for small equipment applications. Figure 18 shows the relation between the size (volume) of a neodymium magnet and magnetic flux density. The graph plots the correlation between the distance (L) from three versions of a 4mm X 4mm cross-section neodymium magnet (1mm, 2mm, and 3mm thick) and magnetic flux density. Figure 19 shows Hall IC detection distance – a good guide for determining the proper size and detection distance of the magnet. Based on the BU52058GWZ operating point max 5.0 mT, the minimum detection distance for the 1mm, 2mm and 3mm magnets would be 7.6mm, 9.2mm, and 10.4mm, respectively. To increase the magnet's detection distance, either increases its thickness or sectional area.



### Position of the Hall Element (Reference)



# •Footprint dimensions (Optimize footprint dimensions to the board design and soldering condition)



Symbol	Reference value
е	0.40
b3	φ0.20
SD	0.20
SE	0.20

(UNIT : mm)

# Terminal Equivalent Circuit Diagram

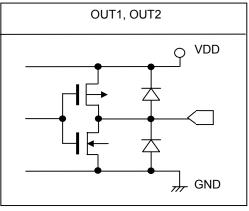


Figure 20

Because they are configured for CMOS (inverter) output, the output pins require no external resistance and allow direct connection to the PC. This, in turn, enables reduction of the current that would otherwise flow to the external resistor during magnetic field detection, and supports overall low current (micropower) operation.

# Operational Notes

#### 1) Absolute maximum ratings

Exceeding the absolute maximum ratings for supply voltage, operating conditions, etc. may result in damage to or destruction of the IC. Because the source (short mode or open mode) cannot be identified if the device is damaged in this way, it is important to take physical safety measures such as fusing when implementing any special mode that operates in excess of absolute rating limits.

2) GND voltage

Make sure that the GND terminal potential is maintained at the minimum in any operating state, and is always kept lower than the potential of all other pins.

3) Thermal design

Use a thermal design that allows for sufficient margin in light of the power dissipation (Pd) in actual operating conditions.

4) Pin shorts and mounting errors

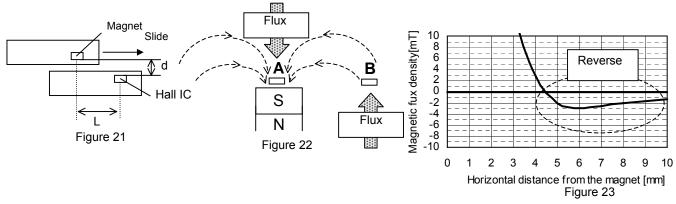
Use caution when positioning the IC for mounting on printed circuit boards. Mounting errors, such as improper positioning or orientation, may damage or destroy the device. The IC may also be damaged or destroyed if output pins are shorted together, or if shorts occur between the output pin and supply pin or GND.

5) Positioning components in proximity to the Hall IC and magnet

Positioning magnetic components in close proximity to the Hall IC or magnet may alter the magnetic field, and therefore the magnetic detection operation. Thus, placing magnetic components near the Hall IC and magnet should be avoided in the design if possible. However, where there is no alternative to employing such a design, be sure to thoroughly test and evaluate performance with the magnetic component(s) in place to verify normal operation before implementing the design.

6) Slide-by position sensing

Figure 21 depicts the slide-by configuration employed for position sensing. Note that when the gap (d) between the magnet and the Hall IC is narrowed, the reverse magnetic field generated by the magnet can cause the IC to malfunction. As seen in Figure 22, the magnetic field runs in opposite directions at Point A and Point B. Since the dual output Omnipolar detection Hall IC can detect the S-pole at Point A and the N-pole at Point B, it can wind up switching output ON as the magnet slides by in the process of position detection. Figure 23 plots magnetic flux density during the magnet slide-by. Although a reverse magnetic field was generated in the process, the magnetic flux density decreased compared with the center of the magnet. This demonstrates that slightly widening the gap (d) between the magnet and Hall IC reduces the reverse magnetic field and prevents malfunctions.



#### 7) Operation in strong electromagnetic fields

Exercise extreme caution about using the device in the presence of a strong electromagnetic field, as such use may cause the IC to malfunction.

8) Common impedance

Make sure that the power supply and GND wiring limits common impedance to the extent possible by, for example, employing short, thick supply and ground lines. Also, take measures to minimize ripple such as using an inductor or capacitor.

9) GND wiring pattern

When both a small-signal GND and high-current GND are provided, single-point grounding at the reference point of the set PCB is recommended, in order to separate the small-signal and high-current patterns, and to ensure that voltage changes due to the wiring resistance and high current do not cause any voltage fluctuation in the small-signal GND. In the same way, care must also be taken to avoid wiring pattern fluctuations in the GND wiring pattern of external components.

10) Exposure to strong light

Exposure to halogen lamps, UV and other strong light sources may cause the IC to malfunction. If the IC is subject to such exposure, provide a shield or take other measures to protect it from the light. In testing, exposure to white LED and fluorescent light sources was shown to have no significant effect on the IC.

#### 11) Power source design

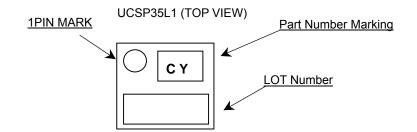
Since the IC performs intermittent operation, it has peak current when it's ON. Please taking that into account and under examine adequate evaluations when designing the power source.

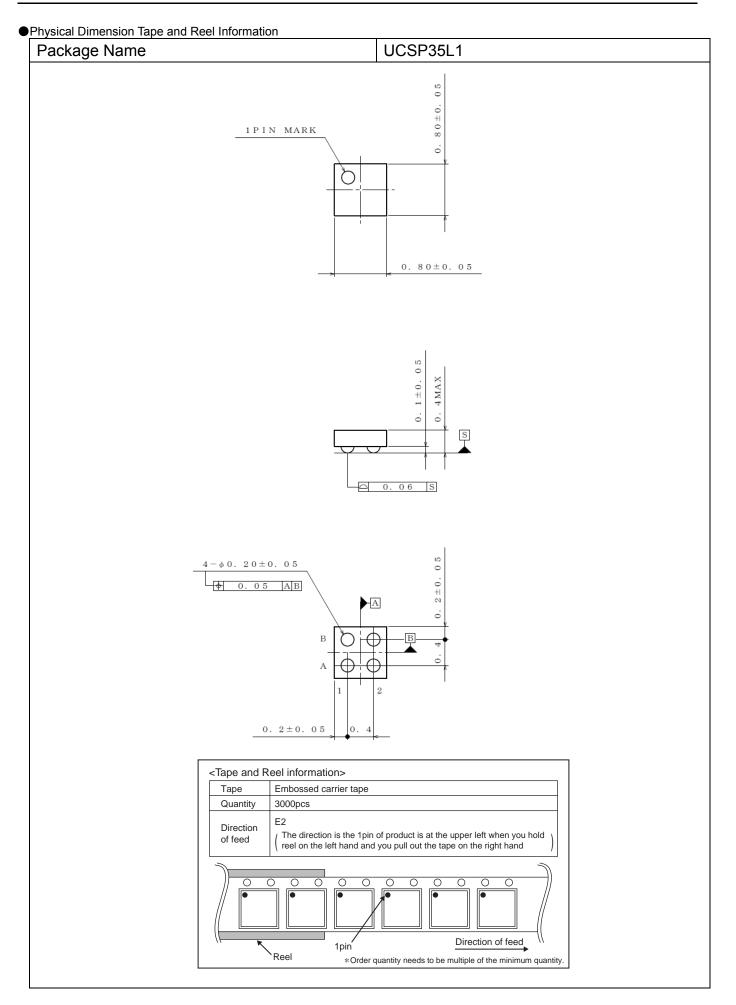
#### Ordering Information В U 5 2 0 5 8 G W Ζ E 2 \_ Part Number Package Packaging and forming specification GWZ: UCSP35L1 E2: Embossed tape and reel (UCSP35L1)

# ●Lineup

Marking	Package		Orderable Part Number
CY	UCSP35L1	Reel of 3000	BU52058GWZ-E2

# Marking Diagrams





#### Revision History

Date	Revision	Changes				
14.Feb.2013	001	New Release				

Status of this document

The English version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document formal version takes priority

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CLASSⅢ	CLASSⅢ	CLASS II b	CLASSⅢ	
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# BU52058GWZ - Web Page

Part Number	BU52058GWZ
Package	UCSP35L1
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes