

## **ISC Silicon NPN Power Transistor**

# **BU608D**

## **DESCRIPTION**

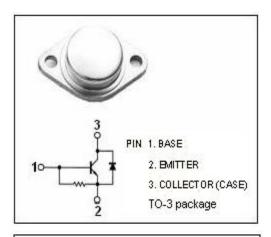
- High Voltage: V<sub>CEV</sub>= 400V(Min)
- · Fast Switching Speed-
  - :  $t_f = 0.5 \,\mu \,s(Max)$
- · Low Saturation Voltage-
- : V<sub>CE(sat)</sub>= 1.0V(Max)@ I<sub>C</sub>= 6A
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

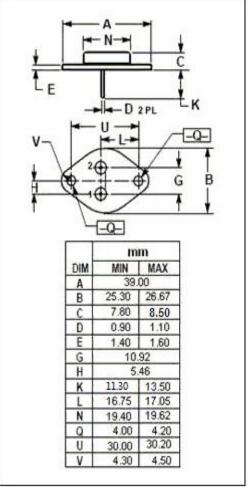


 Designed for use in horizontal deflection output stages of TV's and CRT's

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base Voltage	400	V
V <sub>CEV</sub>	Collector-Emitter Voltage	400	V
V <sub>CEO</sub>	Collector-Emitter Voltage	200	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
Ic	Collector Current-Continuous	7	А
Ісм	Collector Current-Peak	10	А
I <sub>B</sub>	Base Current	4	Α
Pc	Collector Power Dissipation @ Tc=25°C	90	W
TJ	Junction Temperature	150	$^{\circ}$
T <sub>stg</sub>	Storage Temperature Range	-65~150	$^{\circ}$







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#### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = 50mA ;I <sub>B</sub> = 0	200			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 6A; I <sub>B</sub> = 1.2A			1.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 6A; I <sub>B</sub> = 1.2A			1.5	V
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 7A; V <sub>CE</sub> = 2V;	4.3			
I <sub>CEV</sub>	Collector Cutoff Current	V <sub>CE</sub> = 400V; V <sub>BE</sub> = -1.5V			15	mA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 6V; I <sub>C</sub> = 0			400	mA
f⊤	Current-Gain—Bandwidth Product	I <sub>C</sub> = 0.5A ; V <sub>CE</sub> = 10V, f <sub>test</sub> = 1MHz	10			MHz
V <sub>ECF</sub>	C-E Diode Forward Voltage	I <sub>F</sub> = 5A			1.5	V
t <sub>f</sub>	Fall Time	I <sub>C</sub> = 6A; I <sub>B1</sub> = -I <sub>B2</sub> = 1.2A, V <sub>CC</sub> = 40V			0.5	μS

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