BUK7907-40ATC



N-channel TrenchPLUS standard level FET

Rev. 02 — 10 February 2009

Product data sheet

1. Product profile

1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. The devices include TrenchPLUS diodes for clamping and temperature sensing. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

1.2 Features and benefits

- Allows responsive temperature monitoring due to integrated temperature sensor
- Low conduction losses due to low on-state resistance
- Q101 compliant

1.3 Applications

Electrical Power Assisted Steering (EPAS) Variable Valve Timing for engines

1.4 Quick reference data

Table 1. Quick reference

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DS}	drain-source voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C};$	[1]	-	-	40	V
I_D	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ °C};$ see <u>Figure 2</u> ; see <u>Figure 3</u>	[2]	-	-	75	Α
Static cha	racteristics						
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 50 \text{ A};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 7}}{\text{see } \frac{\text{Figure 8}}{\text{Figure 8}}}$		-	5.8	7	mΩ
S _{F(TSD)}	temperature sense diode temperature coefficient	$I_F = 250 \mu A; T_j > -55 °C;$ $T_j < 175 °C$		-1.4	-1.54	-1.68	mV/K
V _{F(TSD)}	temperature sense diode forward voltage	$I_F = 250 \mu A; T_j = 25 °C$		648	658	668	mV
V _{F(TSD)hys}	temperature sense diode forward voltage hysteresis	I _F < 250 μA; T _j = 25 °C; I _F > 125 μA		25	32	50	mV

^[1] Voltage is limited by clamping.



^[2] Continuous current is limited by package.

2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		d a
2	Α	anode	mb	
3	D	drain		
4	K	cathode		$g \leftarrow \downarrow \qquad \downarrow \qquad \downarrow$
5	S	source		
mb	D	mounting base; connected to drain	1 2 3 4 5 SOT263B (TO-220)	MBL306 S k

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BUK7907-40ATC	TO-220	plastic single-ended package; heatsink mounted; 1 mounting hole; 5-lead TO-220	SOT263B

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	[1]	-	40	V
V _{DGS}	drain-gate voltage	$I_{DG} = 250 \mu\text{A}$		-	40	V
V _{GS}	gate-source voltage			-20	20	V
I_D	drain current	T _{mb} = 25 °C; V _{GS} = 10 V; see <u>Figure 2</u> ;	[2]	-	140	Α
		see Figure 3	[3]	-	75	Α
		T _{mb} = 100 °C; V _{GS} = 10 V; see <u>Figure 2</u>	[3]	-	75	Α
I _{DM}	peak drain current	T_{mb} = 25 °C; $t_p \le 10 \mu s$; pulsed; see Figure 3		-	560	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 1</u>		-	272	W
I _{DG(CL)}	drain-gate clamping current	pulsed; $t_p = 5$ ms; $\delta = 0.01$		-	50	mA
I _{GS(CL)}	gate-source clamping	continuous		-	10	mA
	current	pulsed; $t_p = 5$ ms; $\delta = 0.01$		-	50	mA
V _{isol(FET-TSD)}	FET to temperature sense diode isolation voltage			-100	100	V
T _{stg}	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
Source-drain	n diode					
Is	source current	T _{mb} = 25 °C	[2]	-	140	Α
			[3]	-	75	Α
I _{SM}	peak source current	$t_p \le 10 \ \mu s$; pulsed; $T_{mb} = 25 \ ^{\circ}C$		-	560	Α
Clamping						
E _{DS(CL)S}	non-repetitive drain-source clamping energy	I_D = 75 A; V_{DS} ≤ 40 V; V_{GS} = 10 V; R_{GS} = 10 kΩ; unclamped; $T_{j(init)}$ = 25 °C		-	1.4	J
Electrostation	Discharge					
V _{esd}	electrostatic discharge voltage	HBM; C = 100 pF; R = 1.5 kΩ		-	6	kV

^[1] Voltage is limited by clamping.

^[2] Current is limited by power dissipation chip rating.

^[3] Continuous current is limited by package.

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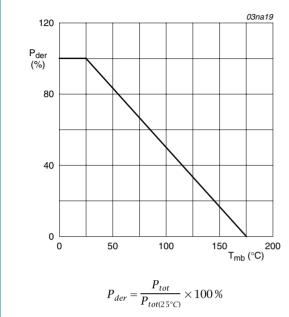


Fig 1. Normalized total power dissipation as a function of mounting base temperature

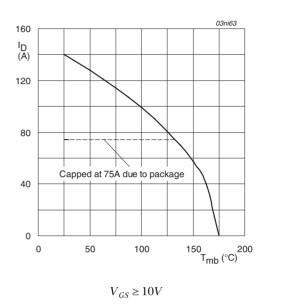


Fig 2. Normalized continuous drain current as a function of mounting base temperature

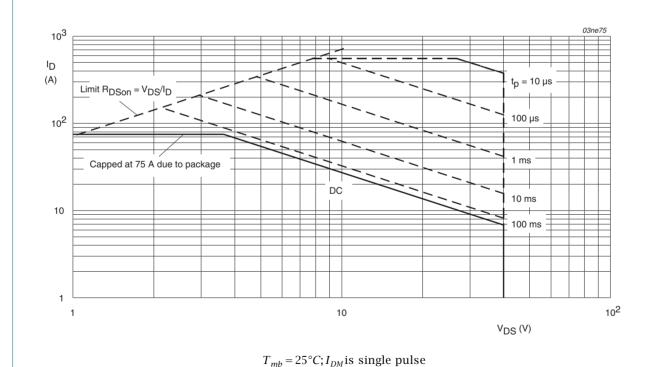


Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	vertical in still air	-	60	-	K/W
R _{th(j-mb)}	thermal resistance from junction to mounting base	see Figure 4	-	-	0.55	K/W

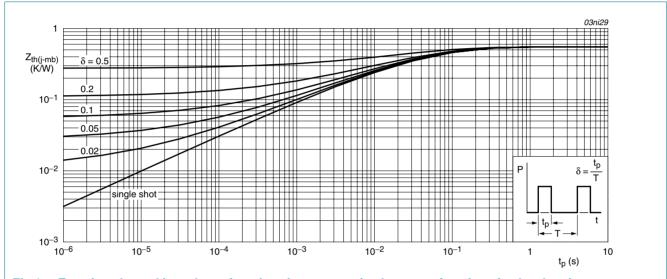


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

6. Characteristics

Table 6. Characteristics

Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
$V_{(BR)DG}$	drain-gate (Zener	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	40	-	-	V
	diode) breakdown voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$	40	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = 25 \text{ °C}$; see Figure 9	2	3	4	V
		$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = 175$ °C; see Figure 9	1	-	-	V
		$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = -55 \text{ °C}$; see Figure 9	-	-	4.4	V
I _{DSS}	drain leakage current	$V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.1	10	μΑ
		$V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$	-	-	250	μΑ
$V_{(BR)GSS}$	gate-source breakdown voltage	$I_G = 1 \text{ mA}; V_{DS} = 0 \text{ V}; T_j > -55 \text{ °C};$ $T_j < 175 \text{ °C}$	20	22	-	V
		$I_G = -1 \text{ mA}; V_{DS} = 0 \text{ V}; T_j > -55 \text{ °C};$ $T_j < 175 \text{ °C}$	20	22	-	V
I _{GSS}	GSS gate leakage current	$V_{DS} = 0 \text{ V}; V_{GS} = 10 \text{ V}; T_j = 25 \text{ °C}$	-	5	1000	nA
		$V_{DS} = 0 \text{ V}; V_{GS} = -10 \text{ V}; T_j = 25 \text{ °C}$	-	5	1000	nA
	$V_{DS} = 0 \text{ V}; V_{GS} = 10 \text{ V}; T_j = 175 \text{ °C}$	-	-	10	μΑ	
		V _{DS} = 0 V; V _{GS} = -10 V; T _j = 175 °C	-	-	10	μΑ
R_{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 50 \text{ A}; T_j = 25 \text{ °C};$ see Figure 7; see Figure 8	-	5.8	7	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 50 \text{ A}; T_j = 175 ^{\circ}\text{C};$ see Figure 7; see Figure 8	-	-	14	mΩ
V _{F(TSD)}	temperature sense diode forward voltage	$I_F = 250 \mu A; T_j = 25 \text{ °C}$	648	658	668	mV
S _{F(TSD)}	temperature sense diode temperature coefficient	$I_F = 250 \mu A; T_j > -55 \text{ °C}; T_j < 175 \text{ °C}$	-1.4	-1.54	-1.68	mV/K
V _{F(TSD)hys}	temperature sense diode forward voltage hysteresis	$I_F < 250 \mu A; I_F > 125 \mu A; T_j = 25 °C$	25	32	50	mV
Dynamic o	characteristics					
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 32 \text{ V}; V_{GS} = 10 \text{ V};$	-	108	-	nC
Q_{GS}	gate-source charge	T _j = 25 °C; see <u>Figure 14</u>	-	21	-	nC
Q_{GD}	gate-drain charge		-	42	-	nC
C _{iss}	input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz};$	-	4500	-	pF
C _{oss}	output capacitance	T _j = 25 °C; see <u>Figure 12</u>	-	960	-	pF
C _{rss}	reverse transfer capacitance		-	510	-	pF

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Table 6. Characteristics ... continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
t _{d(on)}	turn-on delay time	V_{DS} = 30 V; R_L = 1.2 Ω ; V_{GS} = 10 V;	-	2	-	μs
t _r	rise time	$R_{G(ext)} = 1 \text{ k}\Omega; T_j = 25 \text{ °C}$	-	5.7	-	μs
t _{d(off)}	turn-off delay time		-	8.9	-	μs
t _f	fall time		-	6.8	-	μs
L _D	internal drain inductance	from upper edge of drain mounting base to centre of die; $T_j = 25$ °C	-	2.5	-	nΗ
L _S	internal source inductance	from source lead to source bond pad; $T_j = 25 ^{\circ}\text{C}$	-	7.5	-	nΗ
Source-d	rain diode					
V_{SD}	source-drain voltage	$I_S = 25 \text{ A}$; $V_{GS} = 0 \text{ V}$; $T_j = 25 \text{ °C}$; see Figure 19	-	0.85	1.2	V
t _{rr}	reverse recovery time	$I_S = 20 \text{ A}$; $dI_S/dt = -100 \text{ A/}\mu\text{s}$; $V_{GS} = -10 \text{ V}$;	-	80	-	ns
Q _r	recovered charge	$V_{DS} = 30 \text{ V; } T_j = 25 ^{\circ}\text{C}$	-	200	-	nC

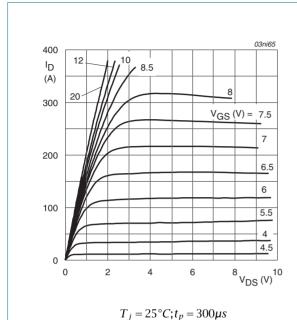
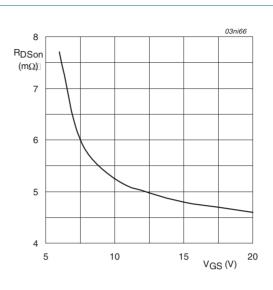


Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values



$$T_j = 25^{\circ}C; I_D = 50A$$

Fig 6. Drain-source on-state resistance as a function of gate-source voltage; typical values

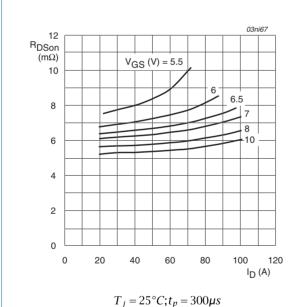


Fig 7. Drain-source on-state resistance as a function of drain current; typical values

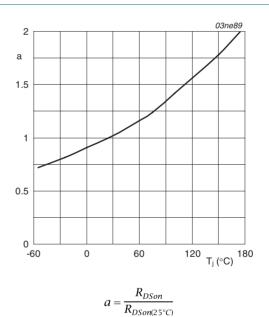
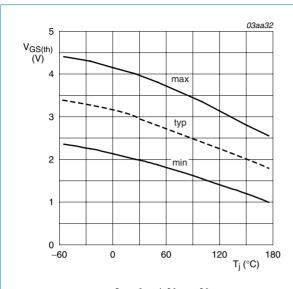
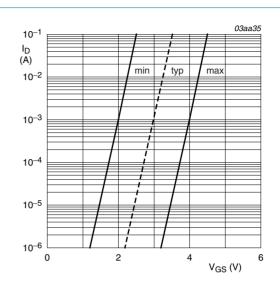


Fig 8. Normalized drain-source on-state resistance factor as a function of junction temperature



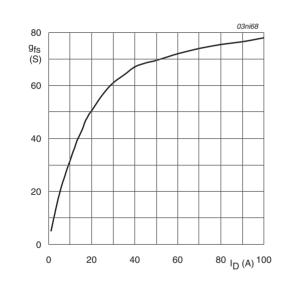
 $I_D=1\,mA; V_{DS}=V_{GS}$

Fig 9. Gate-source threshold voltage as a function of junction temperature



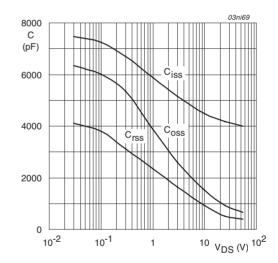
$$T_{j} = 25 \,^{\circ}C; V_{DS} = 5V$$

Fig 10. Sub-threshold drain current as a function of gate-source voltage



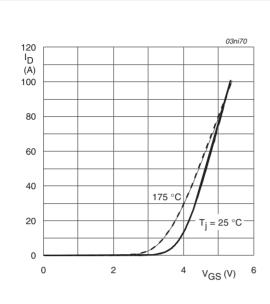
 $T_j = 25$ °C; $V_{DS} = 25V$

Fig 11. Forward transconductance as a function of drain current; typical values



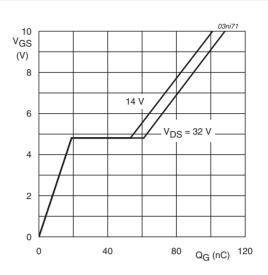
$$V_{GS} = 0V; f = 1MHz$$

Fig 12. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



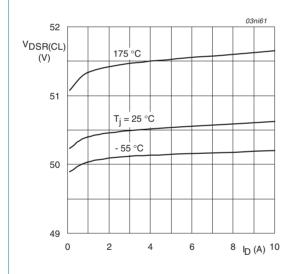
 $V_{DS} = 25V$

Fig 13. Transfer characteristics: drain current as a function of gate-source voltage; typical values



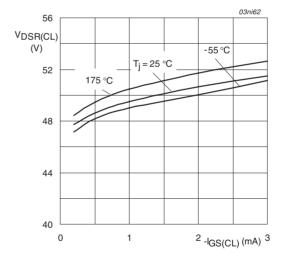
$$T_j = 25 \,{}^{\circ}C; I_D = 25A$$

Fig 14. Gate-source voltage as a function of turn-on gate charge; typical values



 $I_{GS(CL)} = -2mA$

Fig 15. Drain-source clamping voltage as a function of drain current; typical values



$$I_D = 10A$$

Fig 16. Drain-source clamping voltage as a function of gate-source current; typical values

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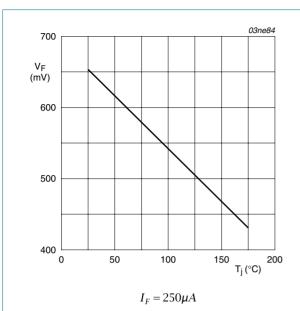
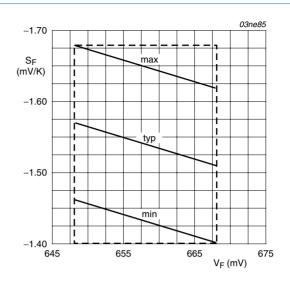


Fig 17. Forward voltage of temperature sense diode as a function of junction temperature; typical values



 V_F at $T_j = 25^{\circ}C$; $I_F = 250 \mu A$

Fig 18. Temperature coefficient of temperature sense diode as a function of forward voltage; typical values

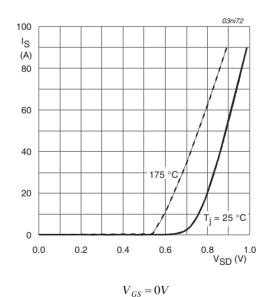
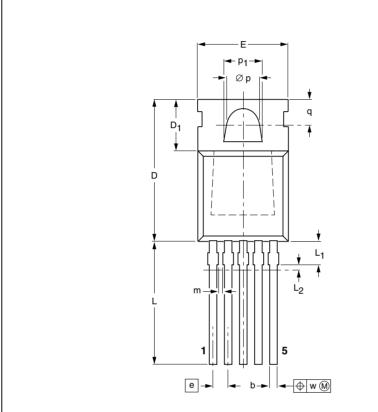


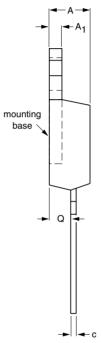
Fig 19. Reverse diode current as a function of reverse diode voltage; typical values

7. Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 5-lead TO-220

SOT263B





0 5 10 mm

DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁	b	С	D	D ₁	E	е	L	L ₁ ⁽¹⁾	L ₂ ⁽²⁾	m	∅p	P ₁	q	Q	w
mm	4.5 4.1	1.39 1.27	0.85 0.70	0.7 0.4	15.8 15.2	6.4 5.9	10.3 9.7	1.7	15.0 13.5	2.4 1.6	0.5	0.8 0.6	3.8 3.6	4.3 4.1	3.0 2.7	2.6 2.2	0.4

Notes

- 1. Terminal dimensions are uncontrolled in this zone.
- 2. Positional accuracy of the terminals is controlled in this zone.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT263B		5-lead TO-220			01-01-11

Fig 20. Package outline SOT263B (TO-220)

8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK7907-40ATC_2	20090210	Product data sheet	-	BUK71_7907_40ATC-01
Modifications:		of this data sheet has been of NXP Semiconductors.	n redesigned to comply w	ith the new identity
	 Legal texts 	have been adapted to the	new company name whe	re appropriate.
	 Type number 	er BUK7907-40ATC separa	ated from data sheet BUK	71_7907_40ATC-01.
BUK71_7907_40ATC-01 (9397 750 09874)	20020809	Product data sheet	-	-

9. Legal information

9.1 Data sheet status

Document status [1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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