

isc Silicon NPN Power Transistor

BUL118D

DESCRIPTION

- Collector-Emitter Sustaining Voltage : $V_{CEO(SUS)} = 400V$ (Min.)
- Low Collector Saturation Voltage : $V_{CE(sat)} = 0.5V$ (Max) @ $I_C = 0.5A$
- Very High Switching Speed

APPLICATIONS

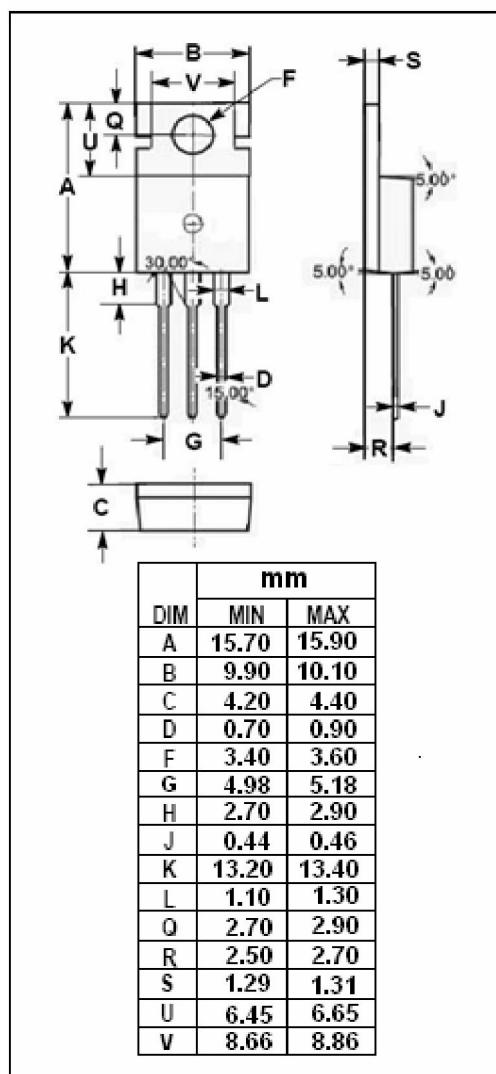
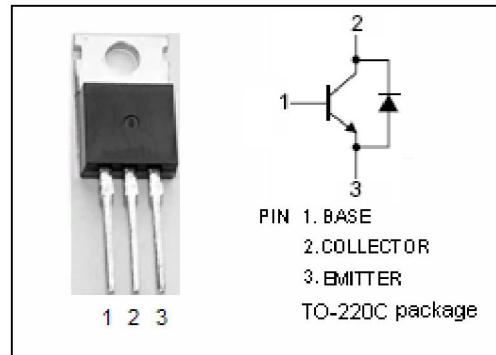
- Designed for use in lighting applications and low cost switch-mode power supplies.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CES}	Collector-Emitter Voltage	700	V
V_{CEO}	Collector-Emitter Voltage	400	V
V_{EBO}	Emitter-Base Voltage	9	V
I_c	Collector Current-Continuous	3	A
I_{CM}	Collector Current-peak $t_p < 5ms$	6	A
I_B	Base Current-Continuous	1.5	A
I_{BM}	Base Current-peak $t_p < 5ms$	3	A
P_c	Collector Power Dissipation $T_c=25^\circ C$	60	W
T_j	Junction Temperature	150	°C
T_{stg}	Storage Temperature Range	-65~150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th j-c}$	Thermal Resistance,Junction to Case	2.08	°C/W
$R_{th j-A}$	Thermal Resistance,Junction to Ambient	62.5	°C/W



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ELECTRICAL CHARACTERISTICS

 $T_c = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CEO(\text{sus})}$	Collector-Emitter Sustaining Voltage	$I_C = 100\text{mA}; L = 25\text{mH}$	400			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E = 10\text{mA}; I_C = 0$	9			V
$V_{CE(\text{sat})-1}$	Collector-Emitter Saturation Voltage	$I_C = 0.5\text{A}; I_B = 0.1\text{A}$			0.5	V
$V_{CE(\text{sat})-2}$	Collector-Emitter Saturation Voltage	$I_C = 1\text{A}; I_B = 0.2\text{A}$			1.0	V
$V_{CE(\text{sat})-3}$	Collector-Emitter Saturation Voltage	$I_C = 2\text{A}; I_B = 0.4\text{A}$			1.5	V
$V_{BE(\text{sat})-1}$	Base-Emitter Saturation Voltage	$I_C = 0.5\text{A}; I_B = 0.1\text{A}$			1.0	V
$V_{BE(\text{sat})-2}$	Base-Emitter Saturation Voltage	$I_C = 1\text{A}; I_B = 0.2\text{A}$			1.2	V
$V_{BE(\text{sat})-3}$	Base-Emitter Saturation Voltage	$I_C = 2\text{A}; I_B = 0.4\text{A}$			1.3	V
I_{CES}	Collector Cutoff Current	$V_{CE} = 700\text{V}; V_{BE} = 0$ $V_{CE} = 700\text{V}; V_{BE} = 0, T_c = 125^\circ\text{C}$			0.1 0.5	mA
I_{CEO}	Collector Cutoff Current	$V_{CE} = 400\text{V}; I_B = 0$			0.25	mA
h_{FE-1}	DC Current Gain	$I_C = 10\text{mA}; V_{CE} = 5\text{V}$	10			
h_{FE-2}	DC Current Gain	$I_C = 0.5\text{A}; V_{CE} = 5\text{V}$	10		50	
h_{FE-3}	DC Current Gain	$I_C = 2\text{A}; V_{CE} = 5\text{V}$	8			
V_F	Diode Forward Voltage	$I_C = 1\text{A}$			2.5	V

Switching Times, Resistive Load

t_r	Rise Time	$I_C = 1\text{A}; V_{CC} = 125\text{V};$ $I_{B1} = -I_{B2} = 0.2\text{A}; t_p = 20\ \mu\text{s}$			0.7	μs
t_s	Storage Time				4.5	μs
t_f	Fall Time				0.4	μs