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CONTROLLED AVALANCHE RECTIFIER DIODES

Glass passivated rectifier diode in hermetically sealed axial-leaded ID* envelope capable of absorbing reverse transients, intended for rectifier applications in colour television circuits as well as general purpose applications in telephony equipment.

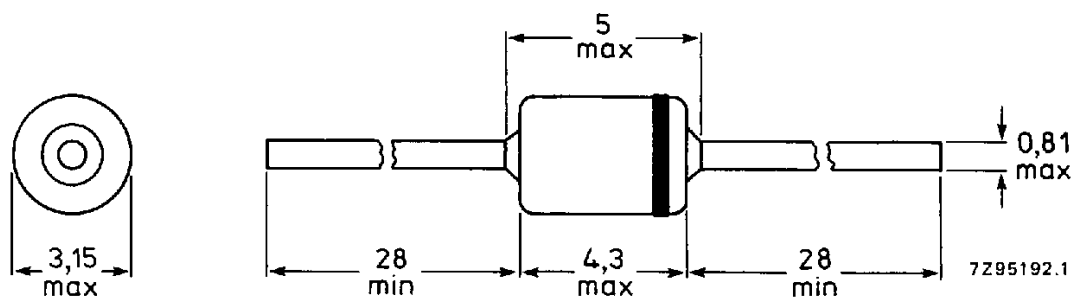
QUICK REFERENCE DATA

Crest working voltage	V_{RWM}	max.	800 V
Repetitive peak reverse voltage	V_{RRM}	max.	1250 V
Average forward current	$I_{F(AV)}$	max.	2 A
Non-repetitive peak forward current	I_{FSM}	max.	50 A
Non-repetitive peak reverse avalanche energy	E_{RSM}	max.	40 mJ
Junction temperature	T_j	max.	175 °C

MECHANICAL DATA

Dimensions in mm

Fig. 1 SOD-84.



The marking band indicates the cathode.

RATINGS

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Limiting values in accordance with the Absolute Maximum System (IEC 134)

Crest working voltage	V_{RWM}	max.	800 V
Repetitive peak reverse voltage ($\delta \leq 1\%$)	V_{RRM}	max.	1250 V
Continuous reverse voltage	V_R	max.	800 V
Average forward current (averaged over any 20 ms period) $T_{tp} = 45\text{ }^\circ\text{C}$; lead length 10 mm $T_{amb} = 60\text{ }^\circ\text{C}$; see Fig. 2	$I_{F(AV)}$	max.	2 A max. 1 A
Repetitive peak forward current $T_{tp} = 45\text{ }^\circ\text{C}$; $f = 50\text{ Hz}$; $a = 4,5$ (inclusive derating for $T_{j\text{ max}}$ at $V_{RRM} = 1250\text{ V}$)	I_{FRM}	max.	20 A
Non-repetitive peak forward current $t = 10\text{ ms}$, half-sine wave (see Fig. 10)	I_{FSM}	max.	50 A
Non-repetitive peak reverse avalanche pulse energy; $I_R = 0,8\text{ A}$; $T_j = 25\text{ }^\circ\text{C}$ prior to surge; with inductive load switched off	E_{RSM}	max.	40 mJ
Storage temperature	T_{stg}		-65 to + 175 $^\circ\text{C}$
Junction temperature	T_j	max.	175 $^\circ\text{C}$

THERMAL RESISTANCE

Influence of mounting method

1. Thermal resistance from junction to tie-point at a lead length of 10 mm
 $R_{th\ j\text{-}tp} = 50\text{ K/W}$
2. Thermal resistance from junction to ambient; device mounted on a 1,5 mm thick epoxy-glass printed-circuit board; Cu-thickness $\geq 40\text{ }\mu\text{m}$; Fig. 2
(See "Thermal model")
 $R_{th\ j\text{-}a} = 105\text{ K/W}$

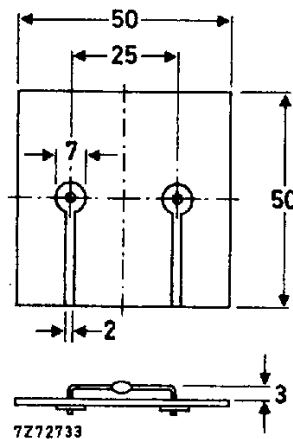


Fig. 2 Mounted on a printed-circuit board.

CHARACTERISTICS

$T_j = 25\text{ }^\circ\text{C}$ unless otherwise specified

Forward voltage*

$$I_F = 3\text{ A}$$

$$I_F = 3\text{ A}; T_j = T_{j\text{ max}}$$

$$V_F < 1,15\text{ V}$$

$$V_F < 1,05\text{ V}$$

Reverse avalanche breakdown voltage

$$I_R = 0,1\text{ mA}$$

$$V_{(BR)R} > 1250\text{ V}$$

Reverse current

$$V_R = V_{RWM\text{ max}}^{**}$$

$$V_R = V_{RWM\text{ max}}; T_j = 100\text{ }^\circ\text{C}$$

$$I_R < 1,0\text{ }\mu\text{A}$$

$$I_R < 10\text{ }\mu\text{A}$$

Reverse recovery when switched from

$$I_F = 1\text{ A to } V_R \geq 30\text{ V with}$$

$$-dI_F/dt = 5\text{ A}/\mu\text{s}$$

recovery charge

recovery time

$$Q_s \text{ typ. } 3\text{ }\mu\text{C}$$

$$t_{rr} \text{ typ. } 2,5\text{ }\mu\text{s}$$

Diode capacitance at $f = 1\text{ MHz}$

$$V_R = 0$$

$$C_d \text{ typ. } 50\text{ pF}$$

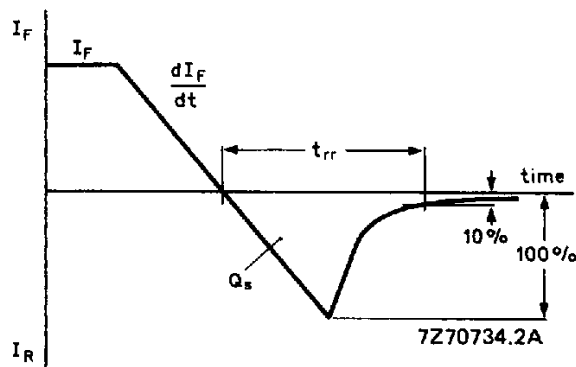


Fig. 3 Definitions of t_{rr} , Q_s and dI_F/dt .

* Measured under pulse conditions to avoid excessive dissipation.

** Illuminance $\leq 500\text{ lux}$ (daylight); relative humidity $< 65\%$.

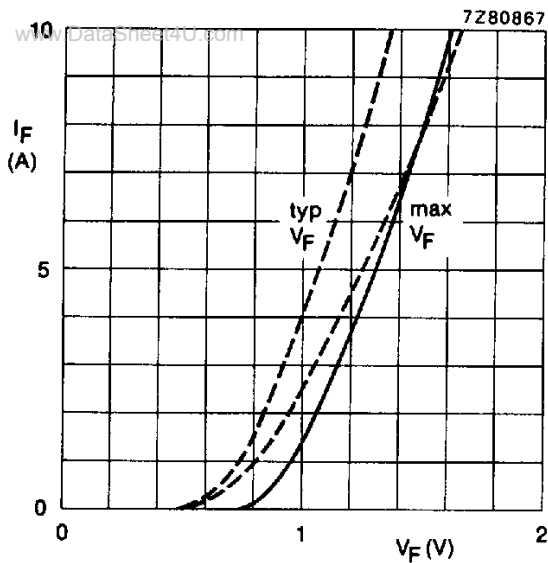


Fig. 4 Forward voltage;
 ——— $T_j = 25\text{ }^\circ\text{C}$;
 - - - $T_j = 175\text{ }^\circ\text{C}$.

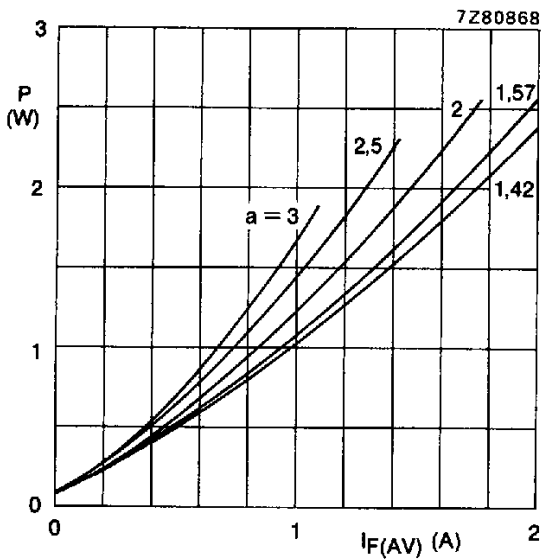


Fig. 5 Maximum values steady state power dissipation (forward plus leakage current) as a function of the average forward current.

$$a = I_{F(RMS)}/I_{F(AV)}; V_R = V_{RWM\text{ max}}$$

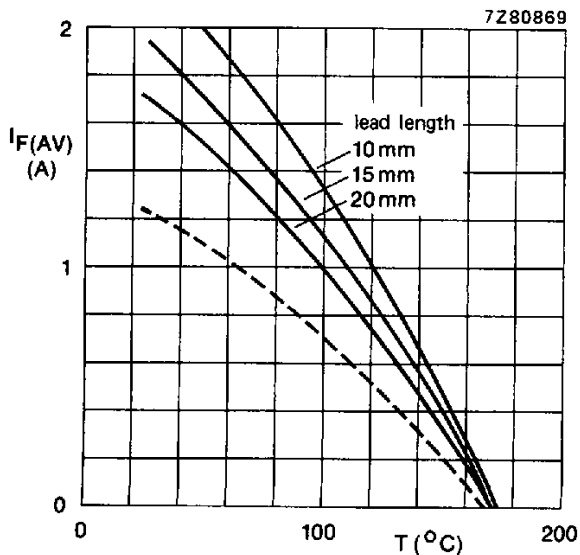


Fig. 6 Maximum average forward current as a function of temperature; the curves include losses due to reverse leakage.

$$V_R = V_{RWM\text{ max}}, \delta = 0,5; a = 1,57.$$

- - - = ambient temperature and device mounted as shown in Fig. 2.
- = tie-point temperature.

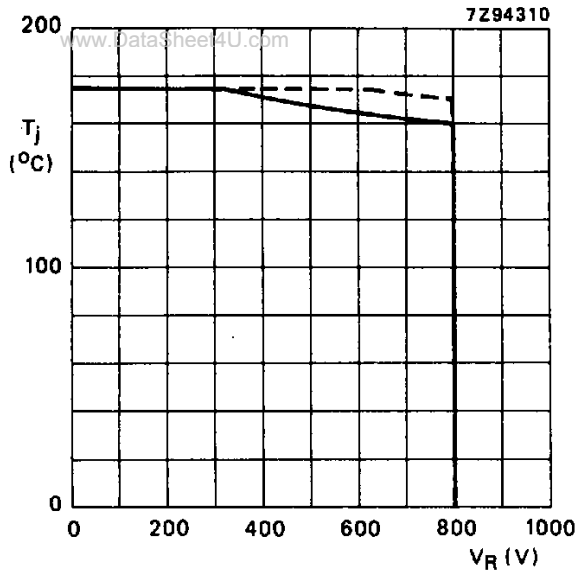


Fig. 7 Maximum permissible junction temperature as a function of reverse voltage; — = V_R ; - - - = V_{RWM} ; $\delta = 0,5$.
Device mounted as shown in Fig. 2.

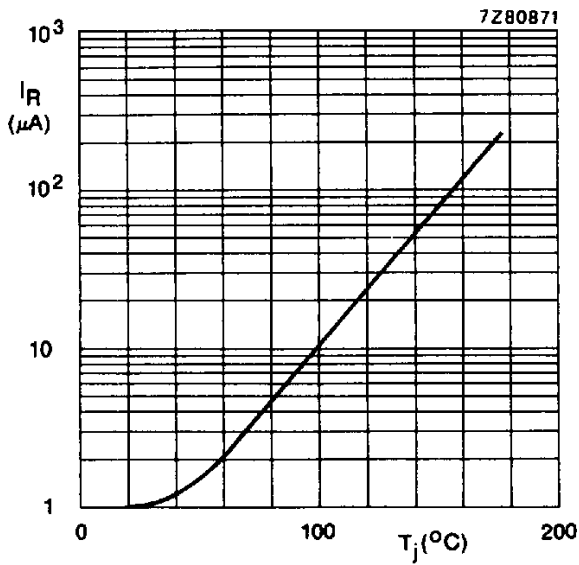


Fig. 8 Maximum values reverse current as a function of junction temperature; $V_R = V_{RWM \text{ max}}$.

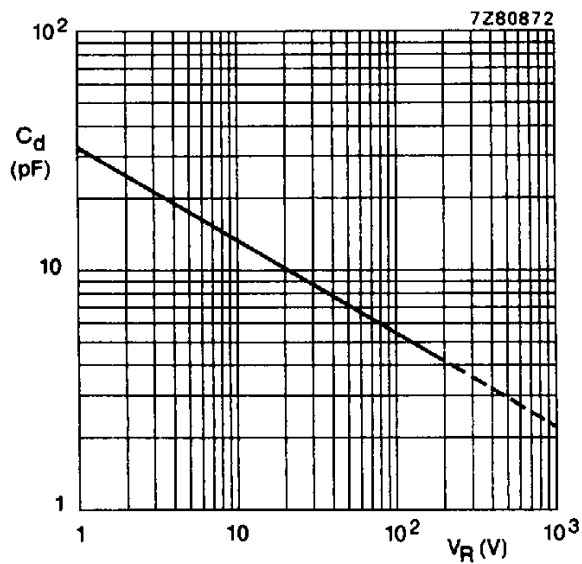


Fig. 9 Capacitance as a function of reverse voltage; $f = 1 \text{ MHz}$; $T_j = 25 \text{ }^{\circ}\text{C}$; typical values.

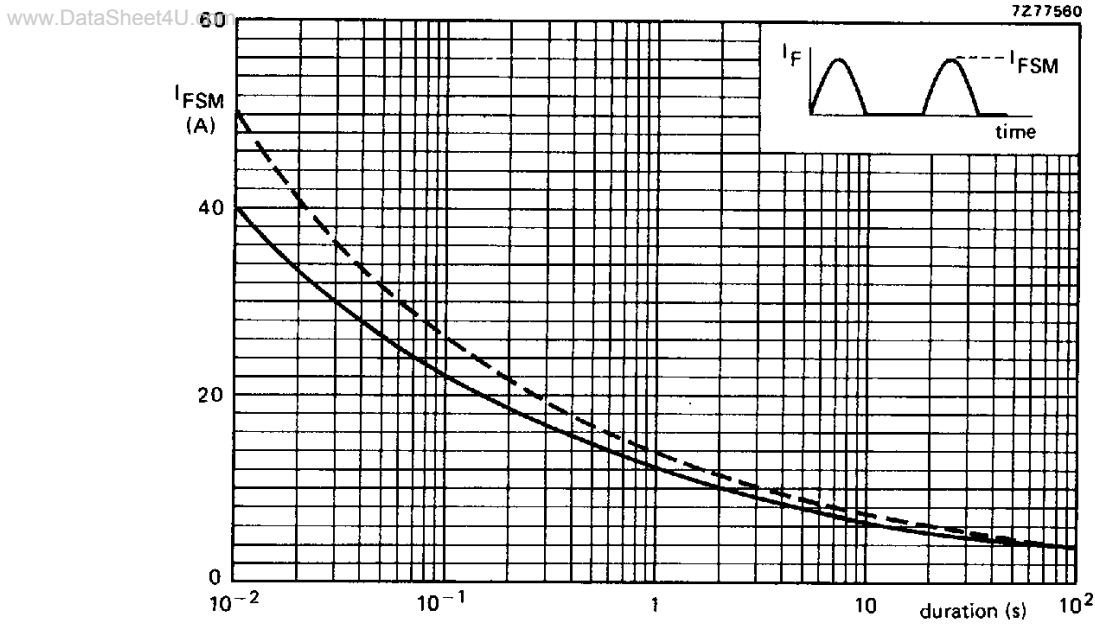


Fig. 10 Maximum permissible non-repetitive peak forward current based on sinusoidal currents; $f = 50 \text{ Hz}$.

- $T_j = 25 \text{ }^\circ\text{C}$ prior to surge; $V_R = 0$.
- $T_j = T_{j \text{ max}}$ prior to surge; $V_R = V_{RWM \text{ max}}$.

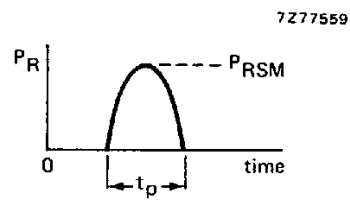
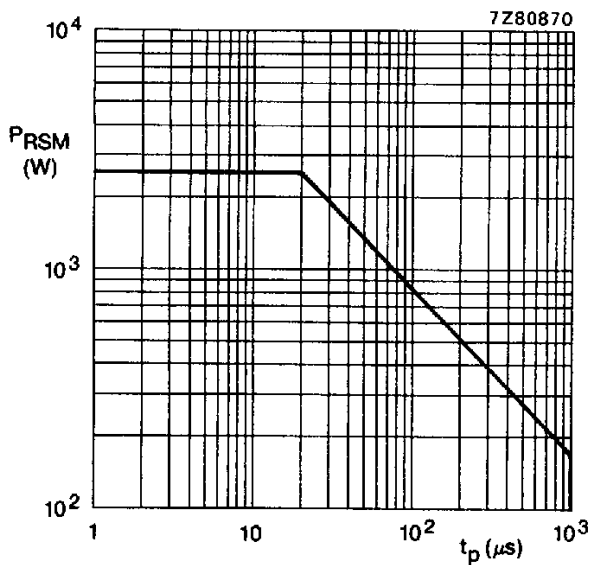


Fig. 11 Non-repetitive peak reverse power in the avalanche region; $T_j = 25 \text{ }^\circ\text{C}$ prior to surge; typical values.