

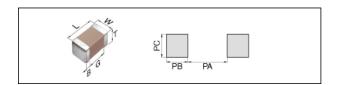
☆TDK Multilayer Ceramic Chip Capacitors

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Creation Date: September 04, 2016

C0603X5R0J105M030BC (TDK item description : C0603X5R0J105MT****)

Applications Commercial Grade Feature General General (Up to 50V) C0603 [EIA CC0201] Series











Size	
Length(L)	0.60mm +/-0.05mm
Width(W)	0.30mm +/-0.05mm
Thickness(T)	0.30mm +/-0.05mm
Terminal Width(B)	0.10mm Min.
Terminal Spacing(G)	0.20mm Min.
Recommended Land Pattern(PA)	0.25 to 0.35mm
Recommended Land Pattern(PB)	0.20 to 0.30mm
Recommended Land Pattern(PC)	0.25 to 0.35mm
Recommended Slit Pattern(SD)	

Electrical Characteristics	
Capacitance	1uF +/-20%
Rated Voltage	6.3Vdc
Temperature Characteristic	X5R(+/-15%)
Dissipation Factor	10% Max.
Insulation Resistance	100MΩ Min.

Other	
Soldering Method	Reflow
AEC Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	15000Pcs Min.

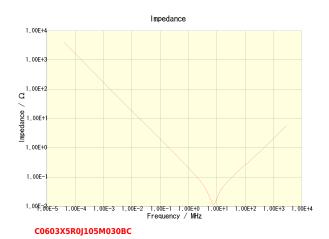
[•]This PDF document was created based on the data listed on the TDK Corporation website.

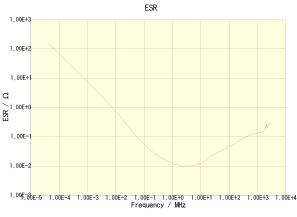
[•]All specifications are subject to change without notice.

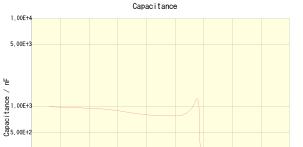
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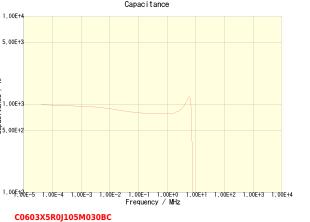
Characteristic Graphs (This is reference data, and does not guarantee the product's characteristics.)







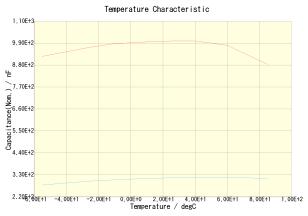


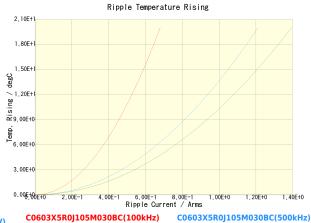












C0603X5R0J105M030BC(DC Bias = 0V\00603X5R0J105M030BC(DC Bias = 3.15V)

C0603X5R0J105M030BC(1MHz)

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