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Product Specification

6.5″ color TFT-LCD module

MODEL NAME: C065GW01 V0

( ) Preliminary Specification
(◆) Final Specification

Note: The content of this specification is subject to change.

Record of Revision

Version	Revise Date	Page	Content
0	22/Feb./2006	0	First draft.
1	30/Aug./2005	4	Added note
		5	Added note
		6	Added note
2	27/Feb./2006	3	Modified weight specification
		7	Modified backlight driving conditions
		9	Modified brightness level of low temperature
		13	Modified mechanical drawing
3	1/Mar./2006	7	Modified backlight driving conditions
		13	Modified mechanical drawing
4	22/May/2006	6	Modified Typical operating conditions: power supply range
		8	Modified Rising & Falling time(Tr/Tf)
		8	Modified high and low level pulse width
		9	Add power sequence description
		12	Modified description of vibration testing
		12	Added Note3
5	13/Jul/2006	11	Added Note8
6	18/Sep/2006	5	Modified Vcom absolute maximum value
7	11/Oct/2006	9	Modified power on sequence
		9	Added Note1
8	07/Dec/2006	6	Added Note4
Final	17/Dec/2007		

Contents:

Appendix:

A. Physical specifications

NO.	Item	Specification	Remark
1	Display resolution (dot)	400RGB(W)×234(H)	
2	Active area (mm)	143.4W)×79.326(H)	
3	Screen size (inch)	6.5(Diagonal)	
4	Dot pitch (mm)	0.1195(W)×0.339 (H)	
5	Color configuration	R. G. B. stripe	
6	Overall dimension (mm)	157.2(W)×89.8(H)×7.7(D)	Note 1
7	Weight (g)	155±5%	
8	Surface treatment	AG(25%) & with WV film	
9	Backlight unit	L type lamp	

Note 1: Refer to Fig. 1

B. Electrical specifications

1.Pin assignment

a. TFT-LCD panel driving section

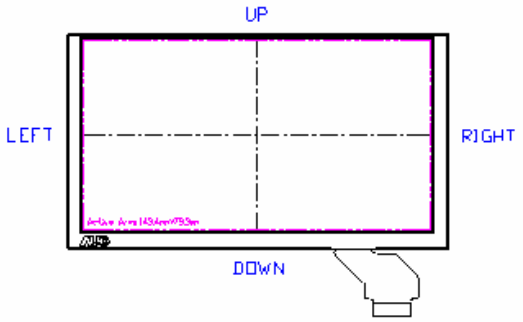
Pin no	Symbol	I/O	Description	Remark
1	V_{GH}	I	Positive power for scan driver	
2	NC	-		
3	V_{CC}	I	Supply voltage of logic control circuit for scan driver	Note 4
4	GND	-	Ground for logic circuit	
5	V_{GL}	I	Negative power for scan driver	
6	NC	-		
7	STVD	I/O	Vertical start pulse	Note 1
8	UDC	I	UP/DOWN scan control input	Note 1,2
9	CKV	I	Shift clock input for scan driver	
10	OEV	I	Output enable input for scan driver	
11	NC	-		
12	NC	-		
13	STVU	I/O	Vertical start pulse	Note 1
14	VCOM	I	Common electrode driving signal	
15	AGND	-	Ground for analog circuit	
16	AV_{DD}	I	Supply voltage for analog circuit	
17	STHR	I/O	Start pulse for horizontal scan line	Note 1
18	LRC	I	LEFT/RIGHT scan control input	Note 1,2
19	CPH3	I	Sampling and shifting clock pulse for data driver	
20	CPH2	I	Sampling and shifting clock pulse for data driver	
21	CPH1	I	Sampling and shifting clock pulse for data driver	
22	MOD	I	Sequential sampling and simultaneous sampling setting	Note 3
23	DV_{DD}	I	Supply voltage of logic control circuit for data driver	Note 4
24	OEH	I	Output enable input for data driver	
25	VB	I	Alternated video signal input(Blue)	
26	VG	I	Alternated video signal input(Green)	
27	VR	I	Alternated video signal input(Red)	
28	NC	-		
29	STHL	I/O	Start pulse for horizontal scan line	Note 1
30	VCOM	I	Common electrode driving signal	

Note 1: Selection of scanning mode (please refer to the following table)

Setting of scan control input		IN/OUT state for start pulse				Scanning direction
UDC	LRC	STVD	STVU	STHR	STHL	
GND	V_{CC}	OUT	IN	OUT	IN	From up to down, and from left to right.
V_{CC}	GND	IN	OUT	IN	OUT	From down to up, and from right to left.
GND	GND	OUT	IN	IN	OUT	From up to down, and from right to left.
V_{CC}	V_{CC}	IN	OUT	OUT	IN	From down to up, and from left to right.

IN: Input; OUT: Output.

Note 2: Definition of scanning direction. Refer to figure as below:



Note 3: MOD = H: Simultaneous sampling. MOD = L: Sequential sampling.
Please set CPH2 and CPH3 to GND when MOD = H.

Note 4: Electrical characteristics of V_{CC} and DV_{DD} are the same.

b. Backlight driving section (Refer to Fig. 1)

No.	Symbol	I/O	Description	Remark
1	HI	I	Power supply for backlight unit (High voltage)	
2	GND	-	Ground for backlight unit	

2. Absolute maximum ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	V_{CC}	GND=0	-0.3	7	V	
	AV_{DD}	AGND=0	-0.3	7	V	
	V_{GH}	GND=0	-0.3	18	V	
	V_{GL}		-15	0.3	V	
	$V_{GH}-V_{GL}$		-	33	V	
Input signal voltage	V_i		-0.3	AV_{DD}+0.3	V	Note 1
	V_I		-0.3	V_{CC}+0.3	V	Note 2
	VCOM		-2.9	6	V	

Note 1: VR, VG, VB.

Note 2: STHL, STHR, OEH, LRC, CPH1~CPH3, STVD, STVU, OEV, CKV, UDC.

3. Environment condition

Item		Condition	Spec.	Remark
Operating temperature	Panel with lighting BLU	Ambient (panel surface)	-30 ~85℃	Note 1,2,3

Storage temperature	Panel with lighting BLU	Surface temperature	-40 ~95℃	Note 4
	Panel	Surface temperature	-40 ~95℃	

Note 1:Under the condition of the operating temperature, the panel would be function normal for the visual display only. For contrast, response time, and other factors related to display quality, determine temperature using the formula Ta=25℃

Note 2: Panel surface temperature is defined as the maximum temperature of panel surface. In general, the lamp side temperature is higher than non-lamp side around 10 ℃

Note 3: The backlight has been turned on before running about condition.

Note 4: BLU is off

4. Electrical characteristics

a. Typical operating conditions (GND=AGND=0V, Note 3)

Item		Symbol	Min.	Typ.	Max.	Unit	Remark
Power supply		V_{CC}	3	5	5.5	V	Note4
		AV_{DD}	4.5	5	5.5	V	Note4
		V_{GH}	14.3	15	15.7	V	
		V_{GL}	-10.5	-10	-9.5	V	
Video signal amplitude (VR,VG,VB)		V_{iA}	0.4	-	AV_{DD}-0.4	V	Note 1
		V_{iAC}	-	3	-	V	AC component
		V_{iDC}	-	AV_{DD}/2	-	V	DC component
VCOM		V_{CAC}	3.5	5.6	6.5	Vp-p	AC component
		V_{CDC}	(1.4)	(1.7)	(2.0)	V	DC component
Input signal voltage	H Level	V_{IH}	0.8V_{CC}	-	V_{CC}	V	Note 2
	L Level	V_{IL}	0	-	0.2V_{CC}	V	

Note 1: Refer to Fig.4-(a).

Note 2: STHL, STHR, OEH, LRC, CPH1~CPH3, STVD, STVU, OEV, CKV, UDC, MOD

Note 3: Be sure to apply GND, V_{CC} and V_{GL} to the LCD first, and then apply V_{GH}.

Note 4: If input signal amplitude is 3.3V, recommend value for Vcc is 3.3V

If input signal amplitude is 5V, recommend value for Vcc is 5V

b. Current consumption (GND=AGND=0V)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Current for driver	I_{GH}	V_{GH}=15V	-	0.20	0.5	mA	
	I_{GL}	V_{GL}=-10V	-	0.80	1.5	mA	
	I_{CC}	DV_{DD}=5V	-	3.0	6.0	mA	Note 1
	I_{DD}	AV_{DD}=5V	-	17.0	30	mA	

Note 1: I_{CC} is current consumption of DV_{DD} plus V_{CC}.

c. Backlight driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Lamp voltage	V_L	-	530	580	Vrms	Note 7
Lamp current	I_L	-	6	6.5	mArms	
Frequency	F_L	-	60	80	kHz	Note 4
Lamp starting voltage	V_S	-	820	1020	Vrms	Note 1,5
		-	1060	1330	Vrms	Note 2,5
		-	1270	1590	Vrms	Note 3,5
Lamp life time		(10000)	(15000)	-	Hr	Note 6

Note 1: Ta = 25℃.

Note 2: Ta = 0℃.

Note 3: Ta = -30℃.

Note 4: The lamp frequency should be selected as different as possible from display horizontal synchronous signal to avoid interference.

Note 5: For starting the backlight unit, the output voltage of DC/AC's transformer should be larger than the maximum lamp starting voltage.

Note 6: The" Lamp life time" is defined as the module brightness decrease to 50% original brightness at Ta=20~35℃ (With no wind), I_L=6.5mA MAX (Continuously operation).

Note 7: The Lamp voltage shall be lighted at constant lamp current (IL:6.0mA)and shall be measured after 20 minutes.

5. AC Timing

a. Timing conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit.	Remark
Rising time	t_r	-	10	25	ns	Note 1
Falling time	t_f	-	10	25	ns	Note 1
High and low level pulse width	t_{CPH}	121	126	131	ns	CPH1~CPH3
CPH pulse duty	t_{CWH}	40	50	60	%	CPH1~CPH3
CPH pulse delay	t_{C12} t_{C23} t_{C31}	30	$t_{CPH}/3$	$t_{CPH}/2$	ns	CPH1~CPH3
STH setup time	t_{SUH}	20	-	-	ns	STHR, STHL
STH hold time	t_{HDH}	20	-	-	Ns	STHR, STHL
STH pulse width	t_{STH}	-	1	-	t_{CPH}	STHR, STHL
STH period	t_H	61.5	63.5	65.5	μs	STHR, STHL
OEH pulse width	t_{OEH}	-	1.22	-	μs	OEH
Sample and hold disable	t_{DIS1}	-	8.28	-	μs	
OEV pulse width	t_{OEV}	-	5.40	-	μs	OEV
CKV pulse width	t_{CKV}	-	4.18	-	μs	CKV
Clean enable time	t_{DIS2}	-	3.74	-	μs	
Horizontal display start	t_{SH}	-	0	-	$T_{CPH}/3$	
Horizontal display timing range	t_{DH}	-	1200	-	$T_{CPH}/3$	
STV setup time	t_{SUV}	400	-	-	ns	STVU, STVD
STV hold time	t_{HDV}	400	-	-	ns	STVU, STVD
STV pulse width	t_{STV}	-	-	1	t_H	STVU, STVD
Horizontal lines per field	t_V	256	262	268	t_H	Note 2
Vertical display start	t_{SV}		3	-	t_H	
Vertical display timing range	t_{DV}		234	-	t_H	
VCOM rising time	t_{rCOM}		-	5	μs	
VCOM falling time	t_{fCOM}		-	5	μs	
VCOM delay time	t_{DCOM}		-	3	μs	
RGB delay time	t_{DRGB}		-	1	μs	

Note 1: For all of the logic signals.

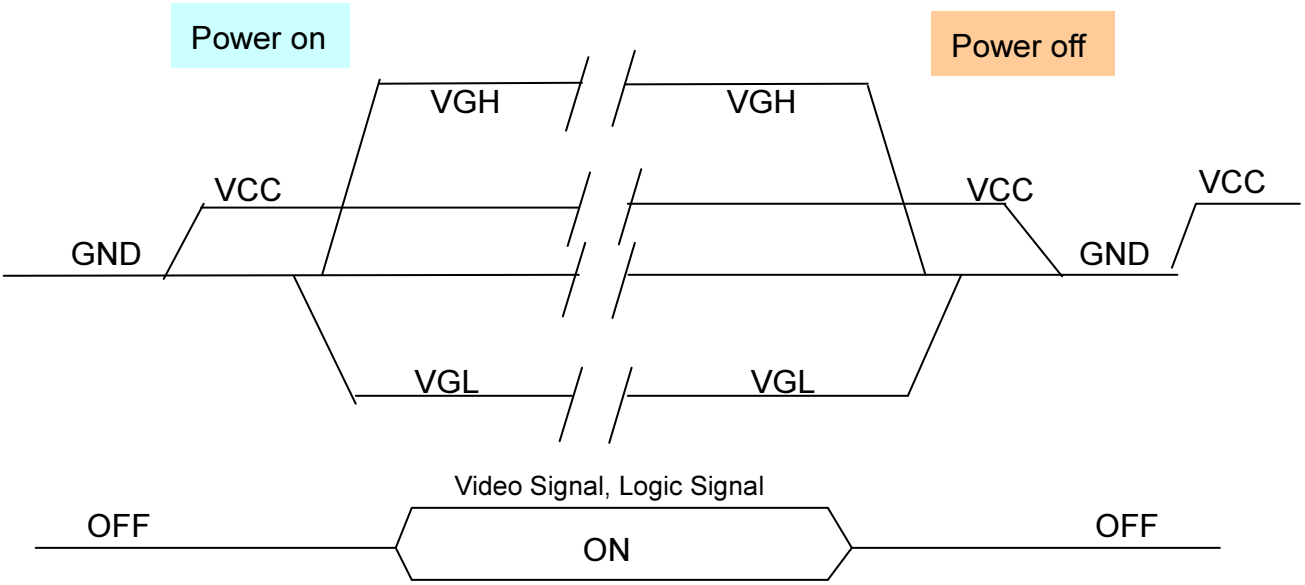
Note 2: Please don't use odd horizontal lines to drive LCD panel for both odd and even field simultaneously.

b. Timing diagram

Please refer to the attached drawing, from Fig.2 to Fig.6.

6. Power Sequence (Suggestion)

Sequence for power on/off and Signal on/off (Note1)



Note 1 : The power sequence is only for reference.

C. Optical specification (Note 1,8)

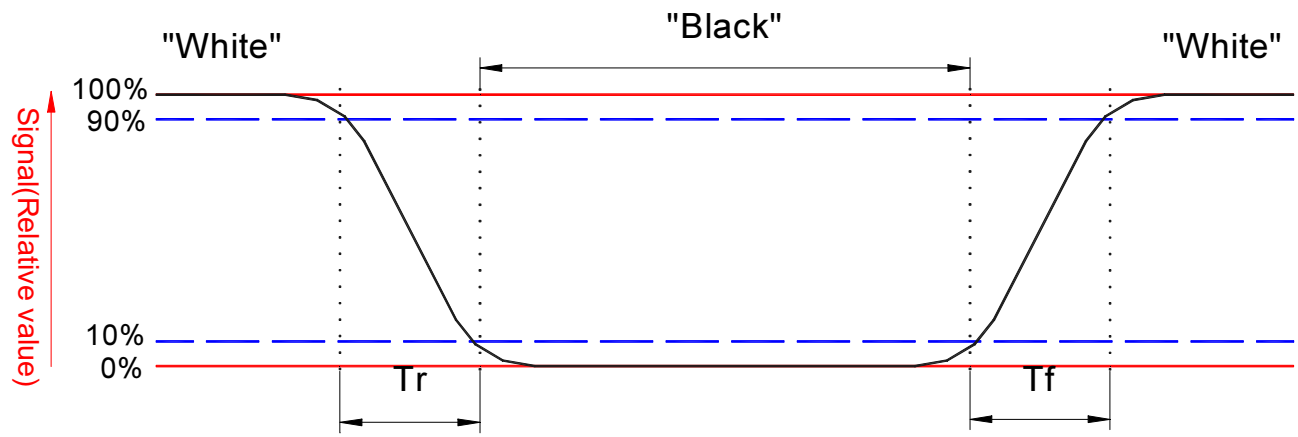
Item		Symb	Condition	Min.	Typ.	Max.	Unit	Remark
Response time	Rise Fall	Tr Tf	θ =0°	- -	15 20	50 60	ms ms	Note 2,4
Contrast ratio		CR	At optimized Viewing angle	100	300	-		Note 3,4
Viewing angle	Top Bottom Left Right		CR≧10	20 40 45 45	40 55 60 60	- - - -	deg.	Note 4,6
	Top Bottom Left Right		CR≧5	30 60 60 60	50 70 70 70	- - - -	deg.	Note 4,5
Brightness		Y_L	I_L=6mA, 25℃	400	500	-	nit	Note 6
			I_L=9.5mA, -10℃, 1min	(140)	(190)		nit	
			I_L=9.5mA, -20℃, 1min	(70)	(120)	-	nit	
White chromaticity		X	θ =0°	0.26	0.31	0.36		Note 6
		Y	θ =0°	0.28	0.33	0.38		

Note 1 : Ambient temperature =25℃. And lamp current I_L = 6 mArms. To be measured in the dark room and to be measured on the center area of panel with a viewing cone of 1°by Topcon luminance meter BM-5, after 20 minutes operation.

Note 2. Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively.

The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 3. Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at “White” state}}{\text{Photo detector output when LCD is at “Black” state}}$$

Note 4. White $Vi=V_{i50} + 1.5V$

Black $Vi=V_{i50} \pm 2.0V$

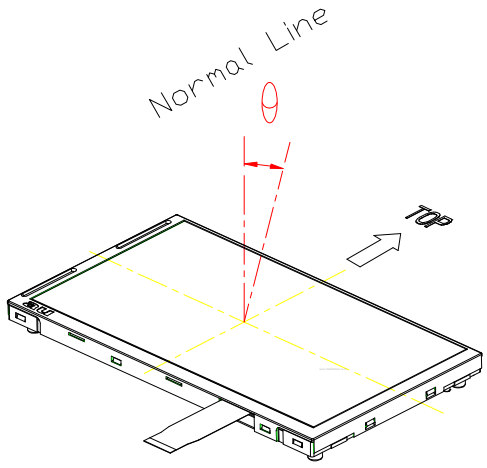
“±” means that the analog input signal swings in phase with V_{COM} signal.

“$\overline{+}$” means that the analog input signal swings out of phase with V_{COM} signal.

V_{i50} : The analog input voltage when transmission is 50%

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 5. Definition of viewing angle, refer to figure as below.



Note 6. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Note 7. It is combine with application circuit which AU Optronics recommendation.

Note 8. All the cosmetic, appearance and optical performance are guaranteed before reliability testing.

D. Reliability test items(Note 2):

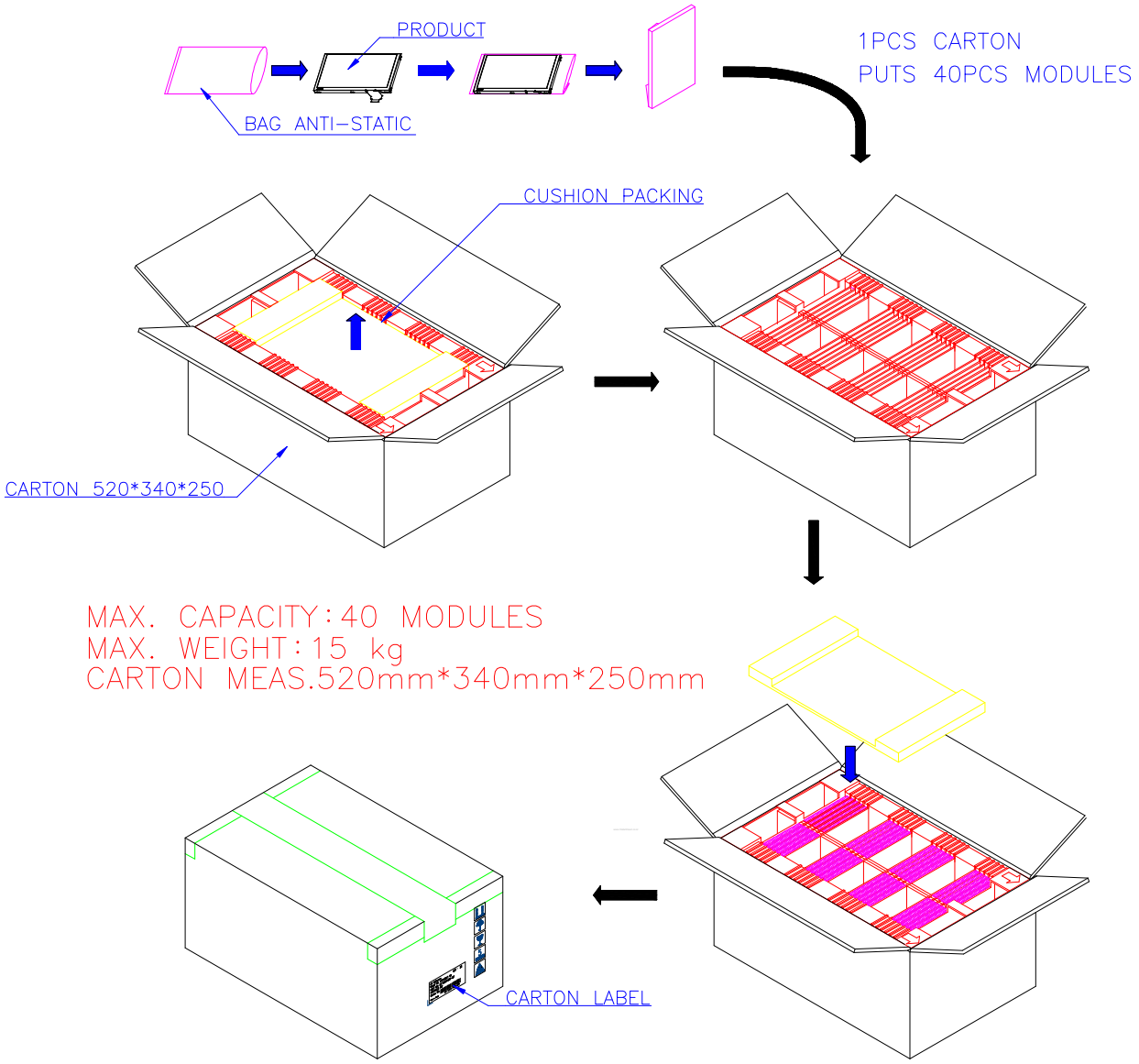
No.	Test items	Conditions	Remark
1	High temperature storage	Ta= 95℃ 240Hrs	
2	Low temperature storage	Ta= -40℃ 240Hrs	
3	High temperature operation	Ta= 85℃ 240Hrs	
4	Low temperature operation	Ta= -30℃ 240Hrs	
5	High temperature and high humidity	Ta= 60℃, 90% RH 240Hrs	Operation
6	Heat shock	-30℃~85℃/200 cycles 1Hrs/cycle	Non-operatio
7	Electrostatic discharge	±200V,200pF(0Ω), once for each terminal	Non-operatio n
8	Vibration	Frequency range : 8~33.3Hz Stoke : 1.3mm Sweep : 2.9G,33.3~400Hz 2 hours for each direction of X, Z 4 hours for direction of Y	JIS D1601, A-10 condition A Note3
9	Mechanical shock	100G, 6ms, ±X,±Y,±Z 3 times for each direction	JIS C7021, A-7 condition C
10	Vibration (with carton)	Random vibration: 0.015G^2/Hz from 5~200Hz –6dB/octave from 200~500Hz	IEC 68-34
11	Drop (with carton)	Height: 60cm 1 corner, 3 edges, 6 surfaces	JIS Z0202

Note 1: Ta: Ambient temperature.

Note 2: In the standard conditions, there is not display function NG issue occurred. All the cosmetic specification is judged before the reliability stress.

Note 3: Cycle time for vibration test is 15 minutes.

E. Packing form



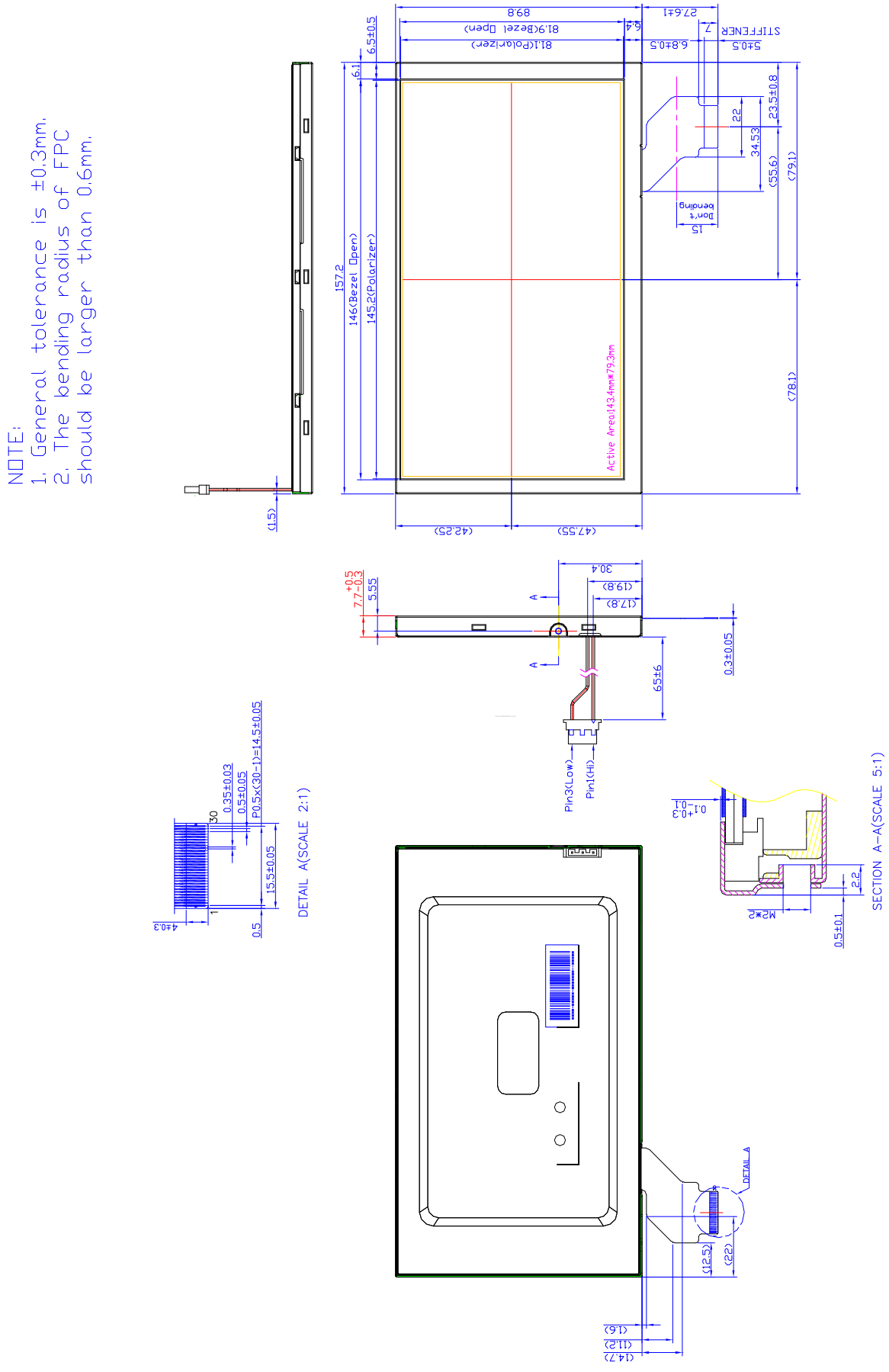


Fig.1-(a) Outline dimension of TFT-LCD module

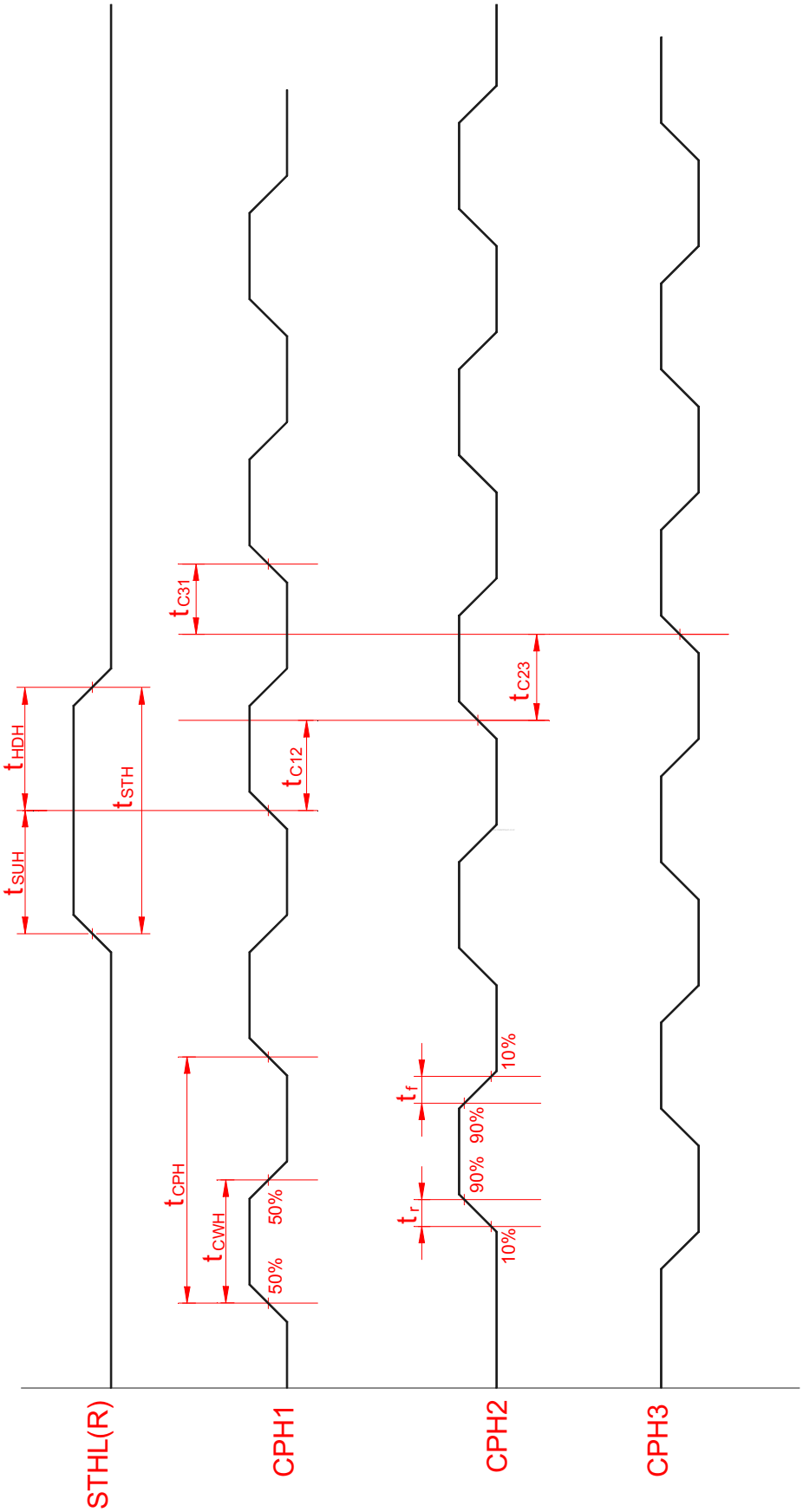


Fig.2 Sampling clock timing

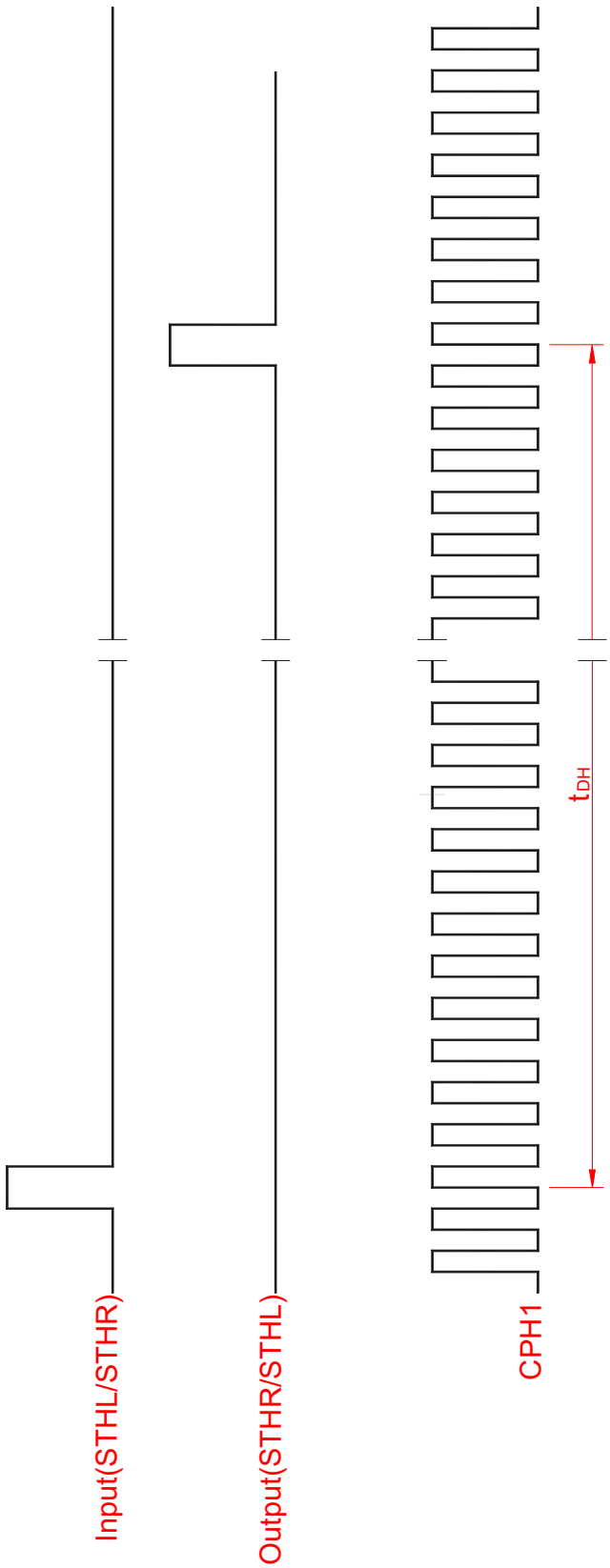


Fig.3 Horizontal display timing range

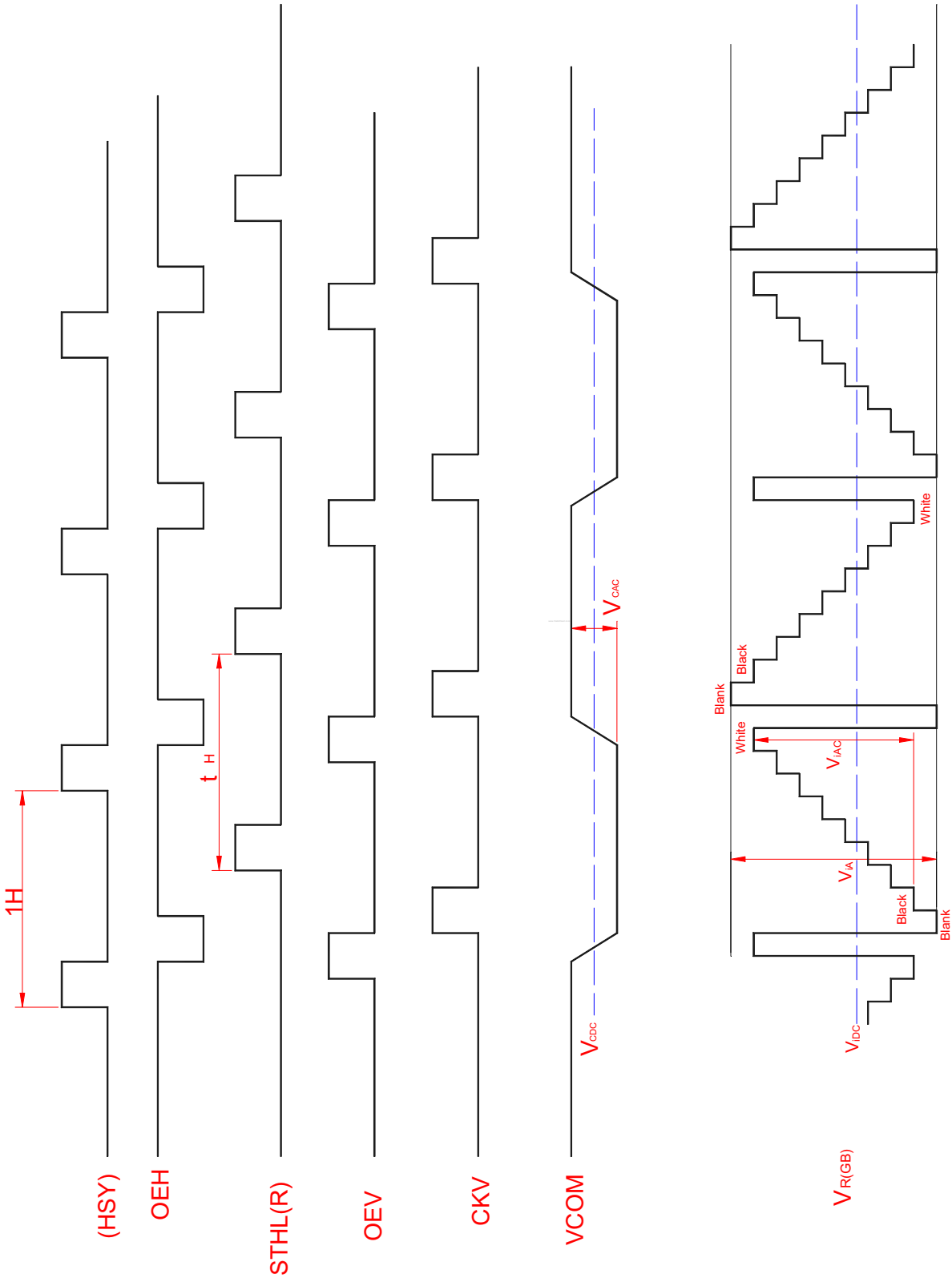
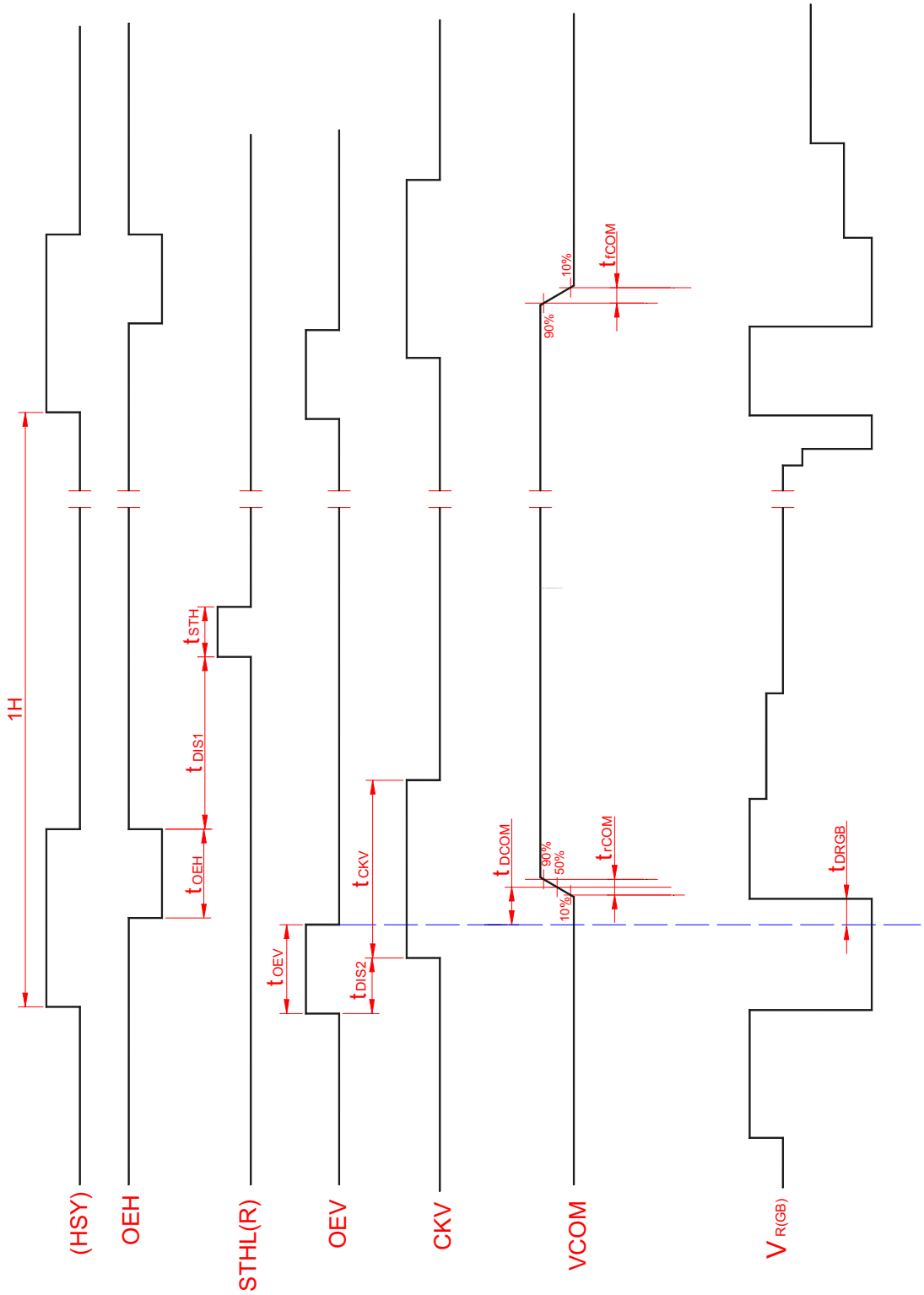


Fig.4-(a) Horizontal timing



Note: The falling edge of OEV should be synchronized with the falling edge of OEH

Fig.4-(b) Detail horizontal timing

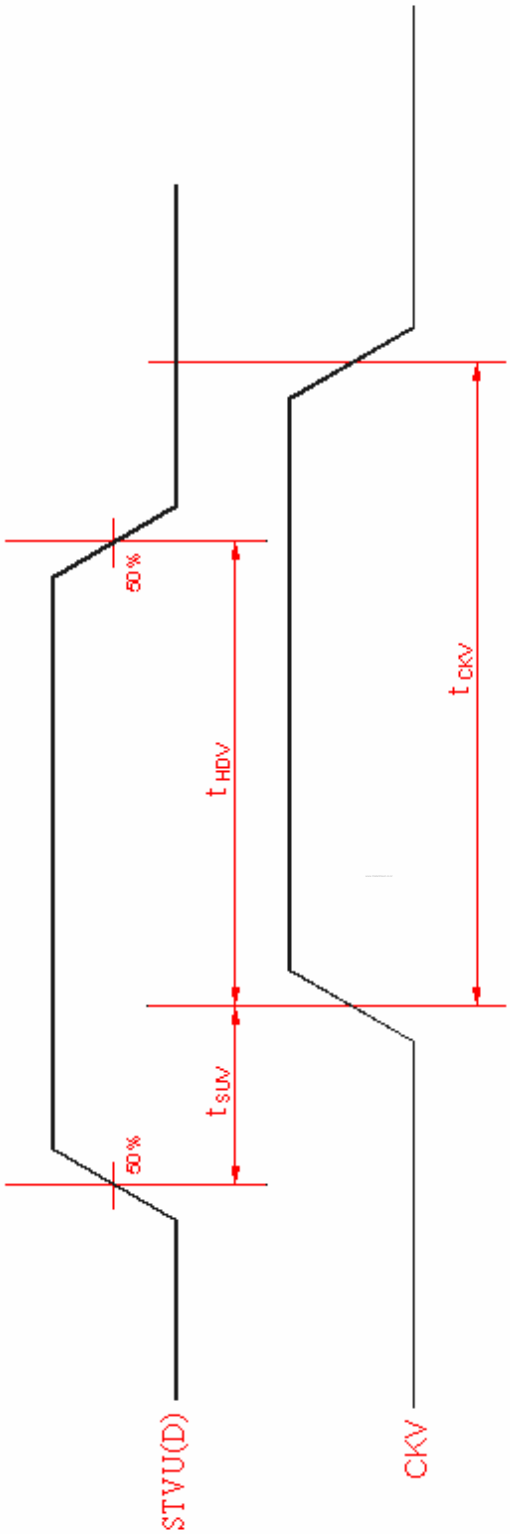


Fig.5 Vertical shift clock timing

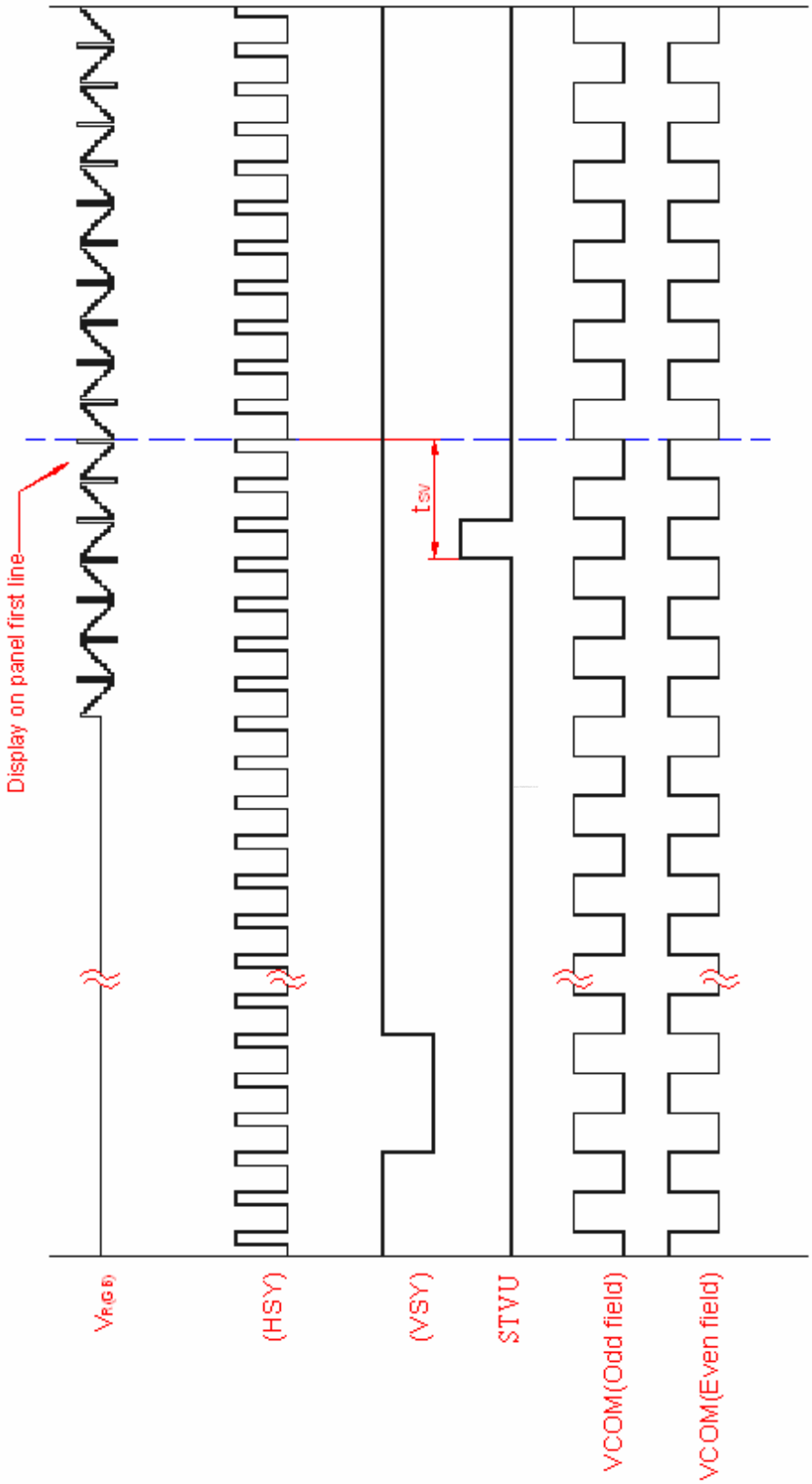


Fig.6-(a) Vertical timing (From up to down)

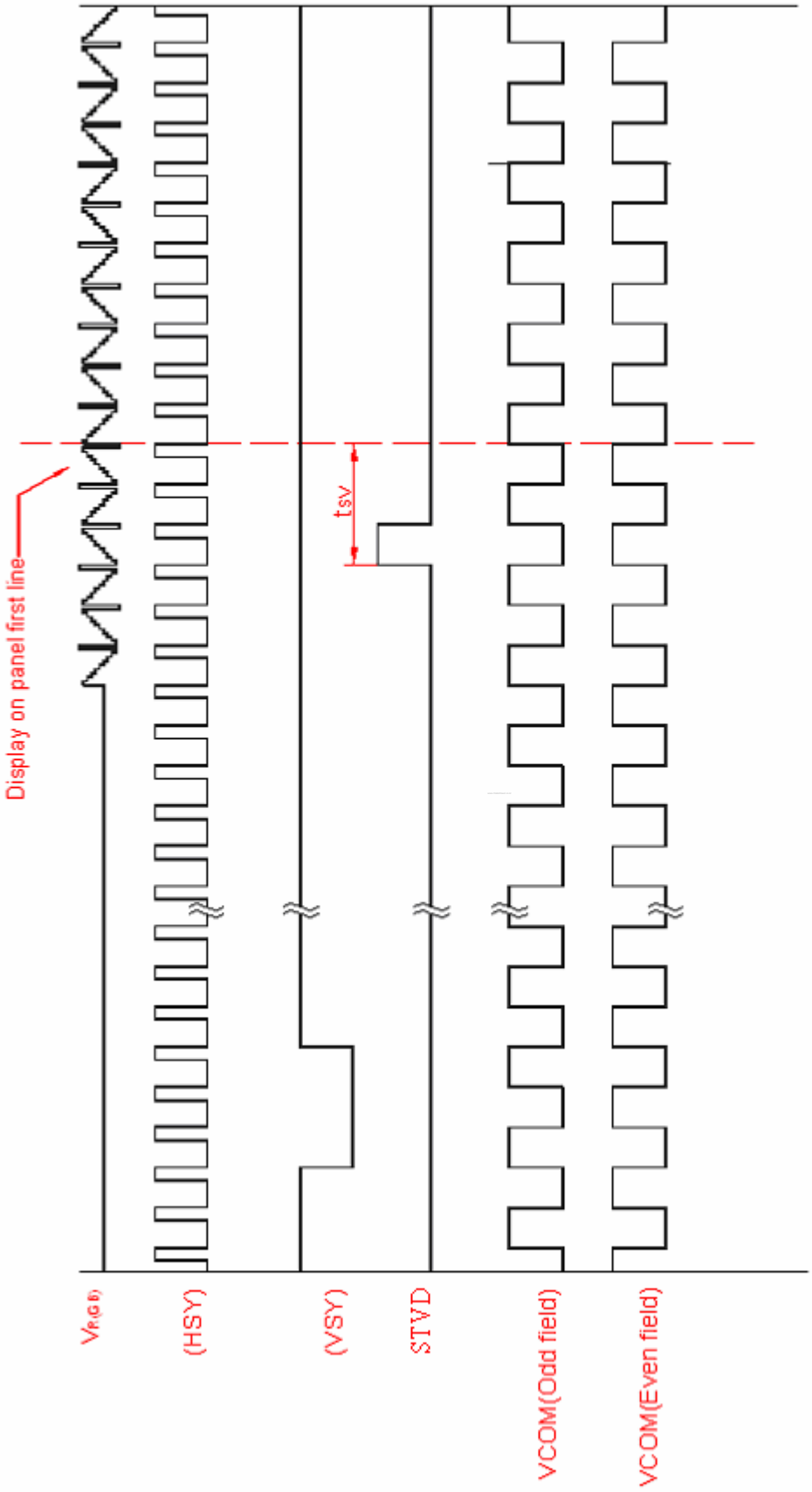


Fig.6-(b) Horizontal timing (From down to up)