isc Thyristors C106D

FEATURES

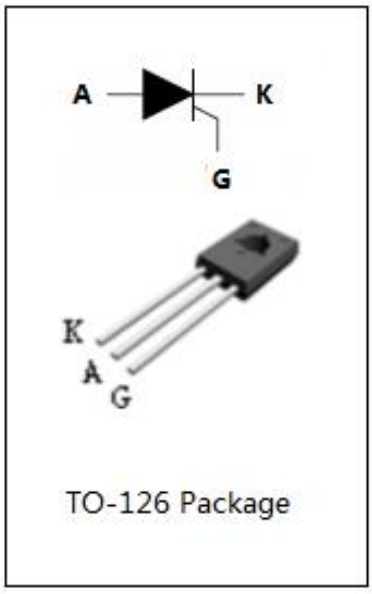
- Glassivated surface for reliability and uniformity
- Practical level triggering and holding characteristics
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for high volume consumer applications such as temperature, light, and speed control; process and remote control, and warning systems where reliability of operation Is important.

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	MIN	UNIT
V_{DRM}	Repetitive peak off-state voltage	400	V
V_{RRM}	Repetitive peak reverse voltage	400	V
$I_{T(AV)}$	On-state current T_A=30℃	2.55	A
$I_{T(RMS)}$	RMS on-state current	4	A
I_{TM}	Surge peak on-state current	20	A
P_{GM}	Peak gate power	0.5	W
$P_{G(AV)}$	Average gate power	0.1	W
T_j	Operating Junction temperature	110	℃
T_{stg}	Storage temperature	-40 ~+150	℃
$R_{th(j-c)}$	Thermal resistance, junction to case	3	℃/W
$R_{th(j-a)}$	Thermal resistance, junction to ambient	75	℃/W



ELECTRICAL CHARACTERISTICS (T_C=25℃ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	V_{RM}=V_{RRM}, V_{RM}=V_{RRM}, Tj=110℃			0.01 0.1	mA
I_{DRM}	Repetitive peak off-state current	V_{DM}=V_{DRM}, V_{DM}=V_{DRM}, Tj=110℃			0.01 0.1	mA
V_{TM}	On-state voltage	I_{TM}= 4A			2.2	V
I_{GT}	Gate-trigger current	V_{AK}=6V; R_L=100Ω			200	μA
V_{GT}	Gate-trigger voltage	V_{AK}=6V; R_L=100Ω			0.8	V
I_H	Holding current	V_{AA}=12V; R_{GK}=1kΩ			3	mA

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