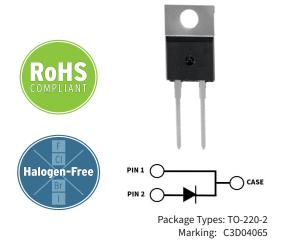


3rd Generation 650 V, 4 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



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Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior

Typical Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

Maximum Ratings (T_c = 25°C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes	
Repetitive Peak Reverse Voltage	V _{RRM}	650	V			
DC Blocking Voltage	V _{DC}	650	V			
		13.5		T _c = 25 °C		
Continuous Forward Current	I _F	6		T _c = 135 °C	Fig. 3	
		4		T _c = 154 °C		
Repetitive Peak Forward Surge Current	I _{FRM}	17	А	$T_c = 25 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave		
		12		T _c = 110 °C, t _p = 10 ms, Half Sine Wave		
Non-Repetitive Forward Surge		30.5		$T_c = 25 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave	F: 0	
Current	FSM	20		$T_c = 110 ^{\circ}\text{C,t}_p = 10 \text{ms, Half Sine Wave}$	Fig. 8	
Non-Repetitive Peak Forward Surge Current	I _{F,Max}	220		T _c = 25 °C, t _p = 10 μs, Pulse		
		160		$T_{c} = 110^{\circ}C, t_{p} = 10 \mu s, Pulse$		
Power Dissipation	P _{tot}	52	W	T _c = 25 °C	Fig. 4	
		22.5		T _c = 110 °C		

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Forward Voltage		1.4	1.7	V	I _F = 4 A, T _j = 25 °C	Fig. 1
	V _F	1.7	2.4		I _F = 4 A, T _j = 175 °C	
Reverse Current		6	30	μΑ	$V_R = 650 \text{ V}, T_j = 25 \text{ °C}$	Fig. 2
	I _R	12	120		$V_R = 650 \text{ V}, T_j = 175 \text{ °C}$	
Total Capacitive Charge	Q _c	15		nC	$V_R = 400 \text{ V}, T_j = 25 \text{ °C}$	Fig. 5
		231			$V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Total Capacitance	c	18.5		pF	$V_R = 200 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		15			$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	1.4		μJ	V _R = 400 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R _{0, JC (TYP)}	2.9	°C/W	
Junction Temperature	T _j	-55 to +175		
Case & Storage Temperature	T _c	-55 to +175	°C	
		1	Nm	M3 Screw
TO-220 Mounting Torque	-	8.8	lbf-in	6-32 Screw

Typical Performance

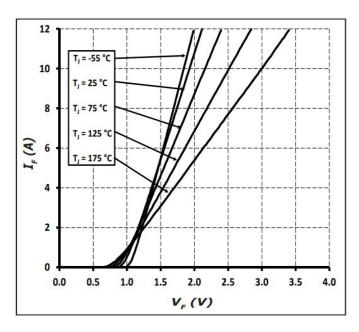


Figure 1Forward Characteristics

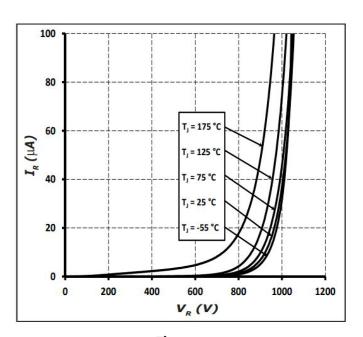


Figure 2Reverse Characteristics

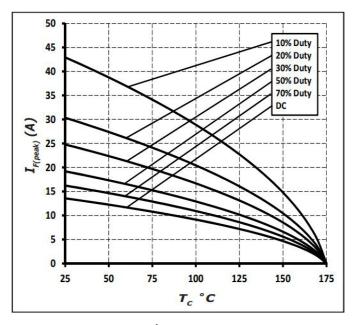


Figure 3Current Derating

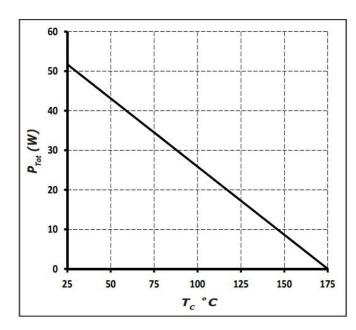


Figure 4Power Derating

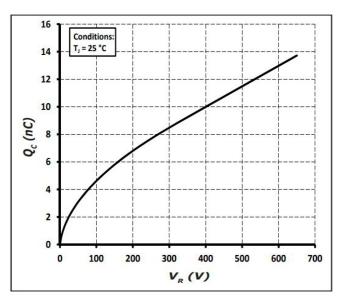


Figure 5Total Capacitance vs. Reverse Voltage

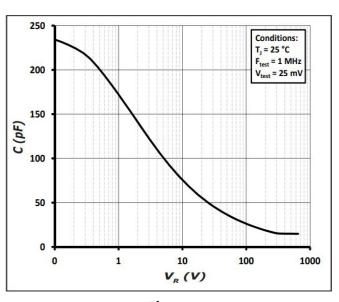
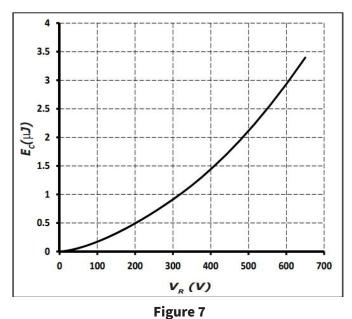


Figure 6Capacitace vs. Reverse Voltage



Capacitance Stored Energy

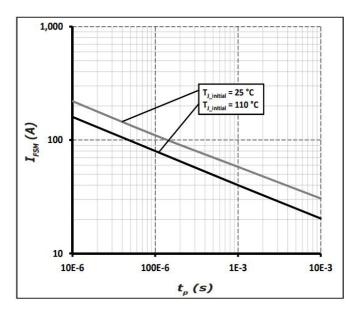


Figure 8Non-Repetitive Peak Forward Surge Current versus Pulse Duration (sinusoidal waveform)

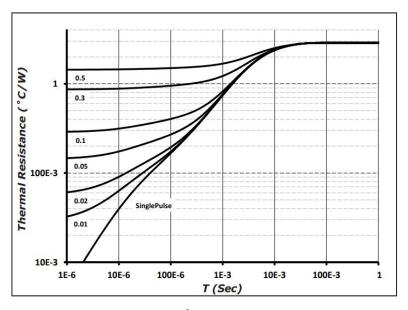


Figure 9Transient Thermal Impedance

Diode Model

$$V_{T}$$

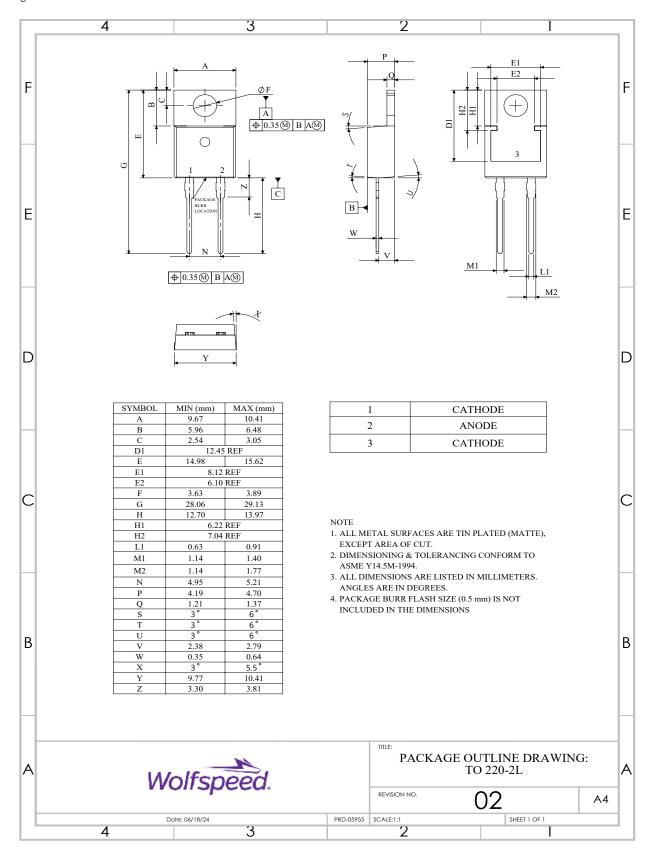
$$Vf_T = V_T + If * R_T$$

 $V_T = 1.00 + (T_J * -1.1*10^{-3})$
 $R_T = 0.069 + (T_J * 8.3*10^{-4})$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

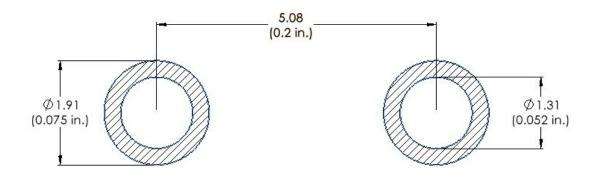
Package Dimensions & Pin-Out

Package: TO-220-2



Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type
C3D04065A	Tube

REACh, RoHS, and Halogen-Free compliance documentation available for this product.

Revision History

Document Version	Date of Release	Description of Changes
1	August-2016	Initial Release
7	March-2023	Update Package Drawing Update Landing Pad
8	July-2023	Updated Test Conditions of I _F and P _{tot} Added Package Marking Statement
9	October - 2024	Legal disclaimer, POD, corrected package marking

Notes & Disclaimer

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Contact info:

4600 Silicon Drive Durham, NC 27703 USA Tel: +1.919.313.5300 www.wolfspeed.com/power

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