

650 V, 4 A Silicon Carbide Schottky Diode

Features

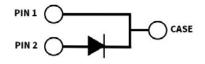
- 650-Volt Schottky rectifier
- Optimized for PFC boost diode application
- Zero reverse recovery current
- Zero forward recovery voltage
- High-frequency operation
- Temperature-independent switching behavior
- Extremely fast switching
- Positive temperature coefficient on V_F

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TO-252-2



Package Types: TO-252-2 Marking: C3D04065

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Typical Applications

- Switch mode power supplies (SMPS)
- Boost diodes in PFC or DC/DC stages
- Free wheeling diodes in inverter stages
- AC/DC converters

Benefits

- Replace bipolar with unipolar rectifiers
- Essentially no switching losses
- Higher efficiency
- Reduction of heat sink requirements
- Parallel devices without thermal runaway

Maximum Ratings (T_c = 25 °C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Note	
Repetitive Peak Reverse Voltage	V _{RRM}	650				
Surge Peak Reverse Voltage	V _{RSM}	650	V			
DC Blocking Voltage	V _{DC}	650				
	I _F	13.5		T _c = 25 °C	Fig. 3	
Continuous Forward Current		6		T _c = 135 °C		
		4		T _c = 155 °C		
Repetitive Peak Forward Surge Current	I _{FRM}	17	А	T _C = 25 °C, t _P = 10 ms, Half Sine Wave		
		12		T _c = 110 °C, t _p = 10 ms, Half Sine Wave		
Non-Repetitive Peak Forward Surge Current	I _{FSM}	25		T _C = 25 °C, t _P = 10 ms, Half Sine Wave	Fig. 8	
		19		T _c = 110 °C, t _p = 10 ms, Half Sine Wave		
Non-Repetitive Peak Forward Surge Current	I _{F, Max}	220		T _C = 25 °C, t _P = 10 μs, Pulse	Fig. 0	
		160		$T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \mu\text{s}, \text{Pulse}$	Fig. 8	
Power Dissipation	P _{tot}	52	W	T _c = 25 °C		
		22.5		T _C = 110 °C	Fig. 4	
Diode dV/dt Ruggedness	dV/dt	200	V/ns	V _R = 0-650 V		
i²t Value	∫i²dt	3.1	A²s	T_{c} = 25 °C, t_{p} = 10 ms		
		1.8		$T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \text{ms}$		
Operating Junction and Storage Temperature	T _J , T _{stg}	-55 to +175	°C			

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Note
Forward Voltage	V _F	1.4	1.7	V	I _F = 4 A, T _J = 25 °C	Fig. 1
		1.7	2.4		I _F = 4 A, T _J = 175 °C	
		6	30	μΑ	V _R = 650 V, T _J = 25 °C	F:- 2
Reverse Current	I _R	12	120		V _R = 650 V, T _J = 175 °C	Fig. 2
Total Capacitive Charge	Q _c	10		nC	$V_R = 400 \text{ V, } I_F = 4 \text{ A}$ $di/dt = 500 \text{ A/}\mu\text{S}$ $T_J = 25 \text{ °C}$	Fig. 5
Total Capacitance	С	231		pF	V _R = 0 V, T _J = 25 °C, f = 1 MHz	Fig. 6
		18.5			V _R = 200 V, T _J = 25 °C, f = 1 MHz	
		15			V _R = 400 V, T _J = 25 °C, f = 1 MHz	
Capacitance Stored Energy	E _c	1.4		μJ	V _R = 400 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Parameter	Symbol	Тур.	Unit	Note
Thermal Resistance from Junction to Case	$R_{\theta JC}$	2.9	°C/W	Fig. 9

Typical Performance

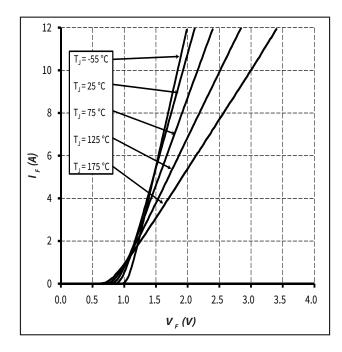
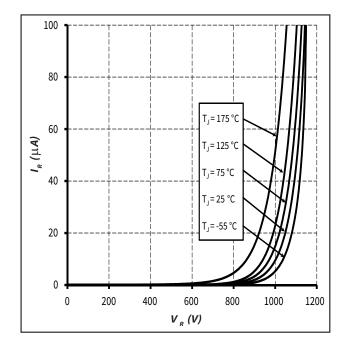


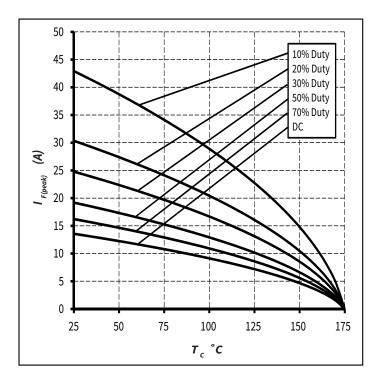
Figure 1. Forward Characteristics



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Figure 2. Reverse Characteristics

Typical Performance



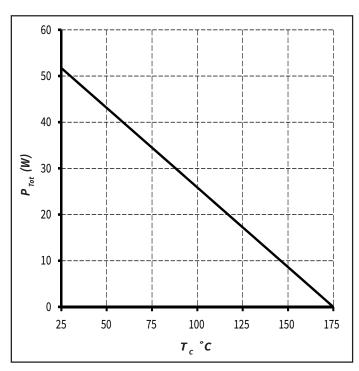
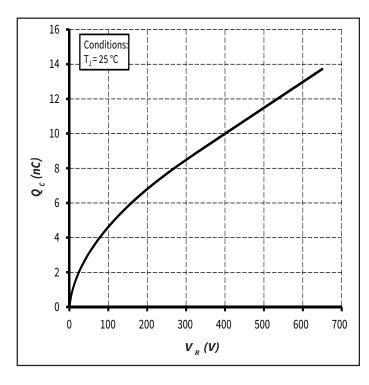


Figure 3. Current Derating

Figure 4. Power Derating





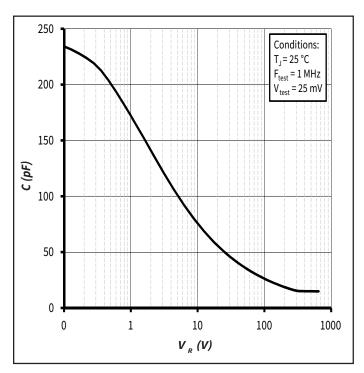
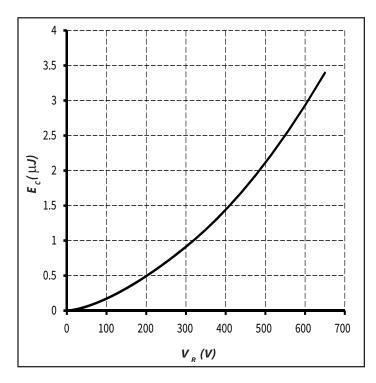


Figure 6. Capacitance vs. Reverse Voltage

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Typical Performance



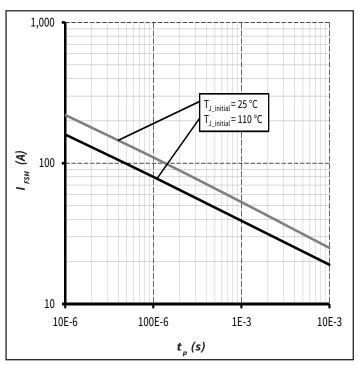


Figure 7. Capacitance Stored Energy

Figure 8. Non-Repetitive Peak Forward Surge Current Versus Pulse Duration (Sinusoidal Waveform)

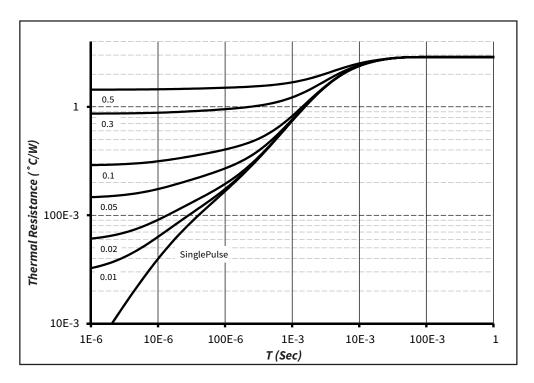
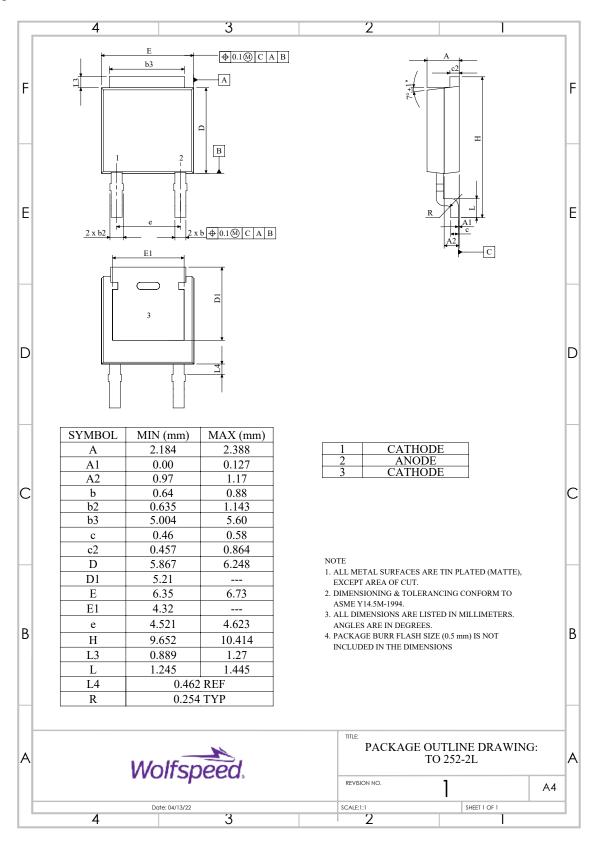


Figure 9. Transient Thermal Impedance

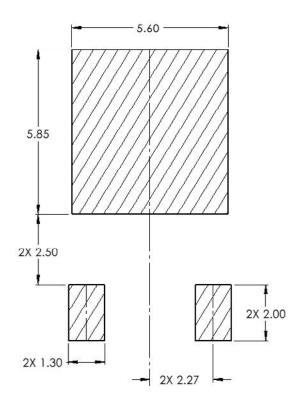


Package Dimensions

Package: TO-252-2



Recommended Solder Pad Layout



Part Number	Package	Marking
C3D04065E	TO-252-2	C3D04065

Diode Model

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$$Vf_T = V_T + If * R_T$$

$$V_T = 1.00 + (T_1 * -1.1*10^{-3})$$

$$R_T = 0.069 + (T_1 * 8.3*10^{-4})$$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Revision History

Current Revision	Date of Release	Description of Changes
5	September-2023	Updated Wolfspeed branding, package drawing, and solder pad lay- out, Removed AEC-Q101 banner
6	October-2023	Corrected solder pad layout and diode model
7	November - 2024	Legal Disclaimer

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