

3rd Generation 650 V, 6 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Package Type: Isolated TO-220-2 Marking: C3D06065I

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Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Electrically Isolated Package (2.5kV)

Typical Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters
- AC/DC Converters

Maximum Ratings (T_c = 25°C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes	
Repetitive Peak Reverse Voltage	V _{RRM}	650				
DC Blocking Voltage	V _{DC}	650	V			
Continuous Formand Comment		13		T _c = 25 °C	F:- 2	
Continuous Forward Current	I _F	6		T _c = 135 °C	Fig. 3	
Repetitive Peak Forward Surge Current		24		T _c = 25 °C, t _p = 10 ms, Half Sine Wave		
	FRM	16		$T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$		
Non-Repetitive Forward Surge		63	Α	T _c = 25 °C, t _p = 10 ms, Half Sine Wave	F: 0	
Current	FSM	49		$T_c = 110 ^{\circ}\text{C,t}_p = 10 \text{ms, Half Sine Wave}$	Fig. 8	
Non-Repetitive Peak Forward		540		$T_{c} = 25 {}^{\circ}\text{C}, t_{p} = 10 \mu\text{s}, \text{Pulse}$		
Surge Current	F,Max	460		T _c = 110°C, t _p = 10 μs, Pulse		
Power Dissipation	P _{tot}	45.5	W	T _c = 25 °C	Fig. 4	
		19.5		T _c = 110 °C		

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Forward Voltage		1.5	1.7	V	I _F = 6 A, T _j = 25 °C	Fig. 1
	V _F	2.0	2.4		I _F = 6 A, T _j = 175 °C	
Reverse Current		8	40	μА	$V_R = 650 \text{ V}, T_j = 25 \text{ °C}$	Fig. 2
	I _R	15.5	160		$V_R = 650 \text{ V}, T_j = 175 \text{ °C}$	
Total Capacitive Charge	Q _c	15		nC	$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, I_F = 6 \text{ A}$	Fig. 5
		295			$V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Total Capacitance	c	28.5		pF	$V_R = 200 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		25.5			$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	2.3		μJ	V _R = 400 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R _{0, JC (TYP)}	3.3	°C/W	
Junction Temperature	T _j	-55 to +175	۰٫	
Case & Storage Temperature	T _c	-55 to +175		
TO 220 Mounting Toyana		1	Nm	M3 Screw
TO-220 Mounting Torque	-	8.8	lbf-in	6-32 Screw

Electrostatic Discharge (ESD) Classifications

Parameter	Symbol	Notes
Human Body Model	НВМ	Class 3B (≥ 8000 V)
Charge Device Model	CDM	Class C3 (≥ 1000 V)

Typical Performance

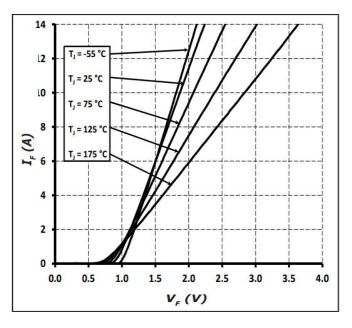


Figure 1Forward Characteristics

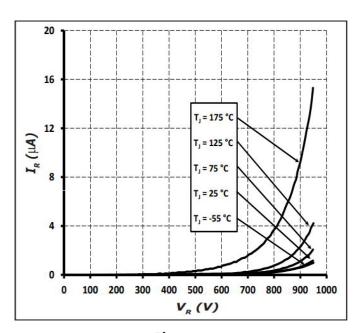


Figure 2Reverse Characteristics

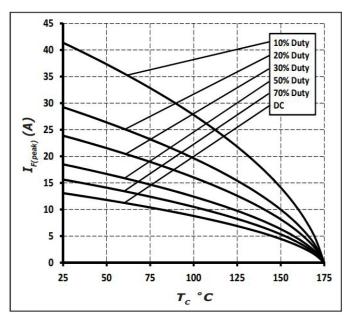


Figure 3Current Derating

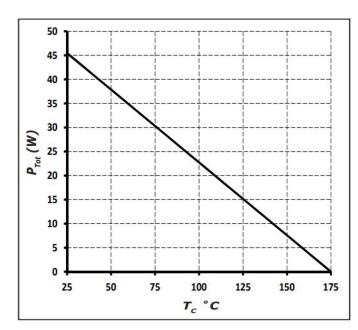
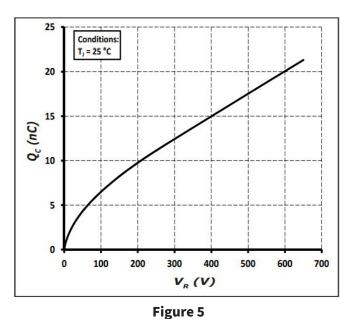


Figure 4Power Derating



Total Capacitance vs. Reverse Voltage

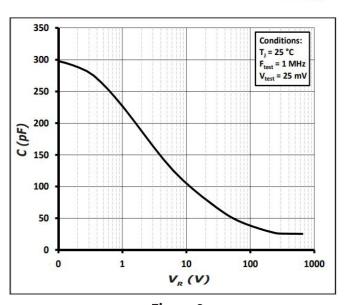


Figure 6Capacitace vs. Reverse Voltage

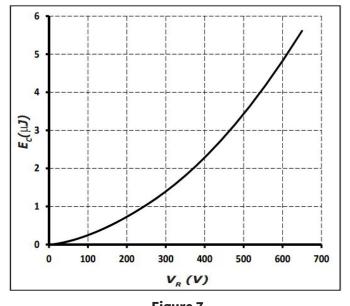


Figure 7Capacitance Stored Energy

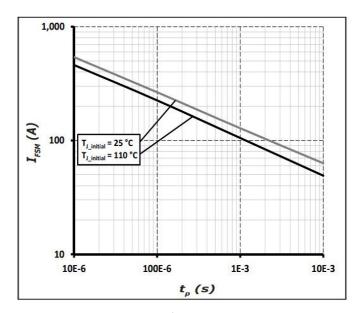


Figure 8Non-Repetitive Peak Forward Surge Current versus Pulse Duration (sinusoidal waveform)

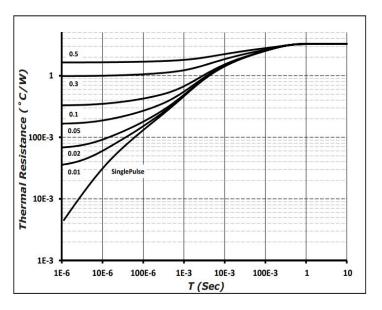


Figure 9Transient Thermal Impedance

Diode Model

$$\begin{array}{c|c} & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & & \\ & \\ & & \\ & & \\ & & \\ & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ &$$

$$Vf_T = V_T + If * R_T$$

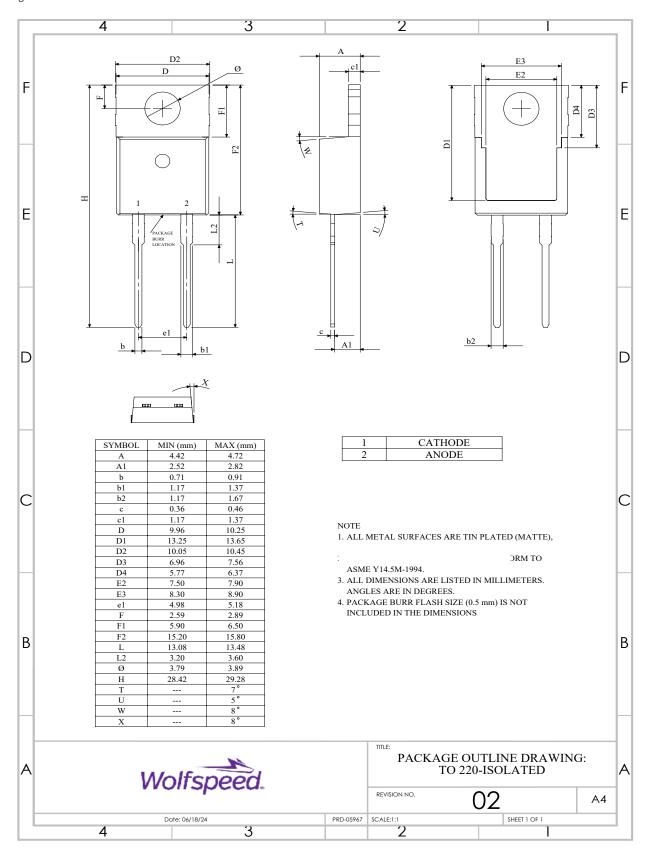
$$V_T = 0.96 + (T_J * -1.1*10^{-3})$$

$$R_T = 0.07 + (T_J * 7.4*10^{-4})$$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

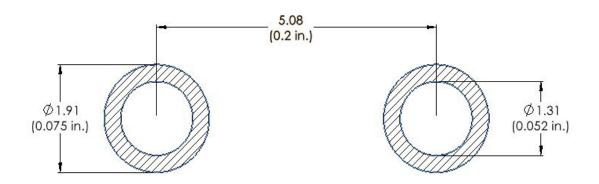
Package Dimensions & Pin-Out

Package: TO-220-2



Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type
C3D06065I	Tube

Revision History

Document Version	Date of Release	Description of Changes
В	January-2016	Initial Release
4	May-2023	Update Package Drawing Update Landing Pad
5	October - 2024	Legal Disclaimer, POD

C3D060651



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