

C3D16065D1

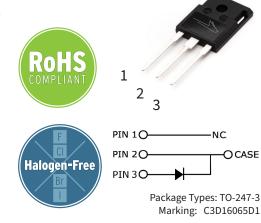
3rd Generation 650 V, 16 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Low Leakage Current (I_R)



Typical Applications

- Industrial Power Supplies
- Battery Charging Systems
- Switch Mode Power Supplies
- AC/DC Converters
- Server/Telecom Power Supplies

Maximum Ratings ($T_c = 25^{\circ}C$ Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes	
Repetitive Peak Reverse Voltage	V _{RRM}	650				
DC Blocking Voltage	V _{DC}	650	V			
		43		T _J = 25 °C		
Continuous Forward Current	I _F	20	A	T _J = 135 °C	Fig. 3	
		16		T _J = 147 °C		
Repetitive Peak Forward Surge Current	I _{FRM}	57		$T_c = 25 \text{ °C}, t_p = 10 \text{ ms}, \text{ Half Sine Wave}$		
		33		$T_c = 110 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$		
Non-Repetitive Peak Forward Surge Current	I _{fsm}	160		$T_c = 25 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	Fig. 8	
		148		$T_c = 110 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$		
Power Dissipation	P _{tot}	173	W	T _J = 25 °C	Fig. 4	
		75		T _J = 110 °C		
i²t value	∫i²dt	128	A²s	T _c = 25C, tp=10ms		
		110		T _c = 110C, tp=10ms		

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Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
		1.5	1.8	V	I _F = 16 A, T _j = 25 °C	Fig. 1
Forward Voltage	V _F	2.0	2.4		I _F = 16 A, T _j = 175 °C	
Reverse Current		18	95	μA	V _R = 650 V, T _j = 25 °C	Fig. 2
	R R	38	378		V _R = 650 V, T _j = 175 °C	
Total Capacitive Charge	Q _c	40		nC	$V_{R} = 400 \text{ V}, \text{ T}_{j} = 25 \text{ °C}, \text{ I}_{F} = 16\text{ A}$	Fig. 5
		740			$V_{R} = 0 V, T_{j} = 25 °C, f = 1 MHz$	
Total Capacitance	с	74		pF	$V_{R} = 200 \text{ V}, \text{ T}_{j} = 25 \text{ °C}, \text{ f} = 1 \text{ MHz}$	Fig. 6
		68			$V_{R} = 400 \text{ V}, \text{ T}_{j} = 25 \text{ °C}, \text{ f} = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	6		μJ	V _R = 400 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

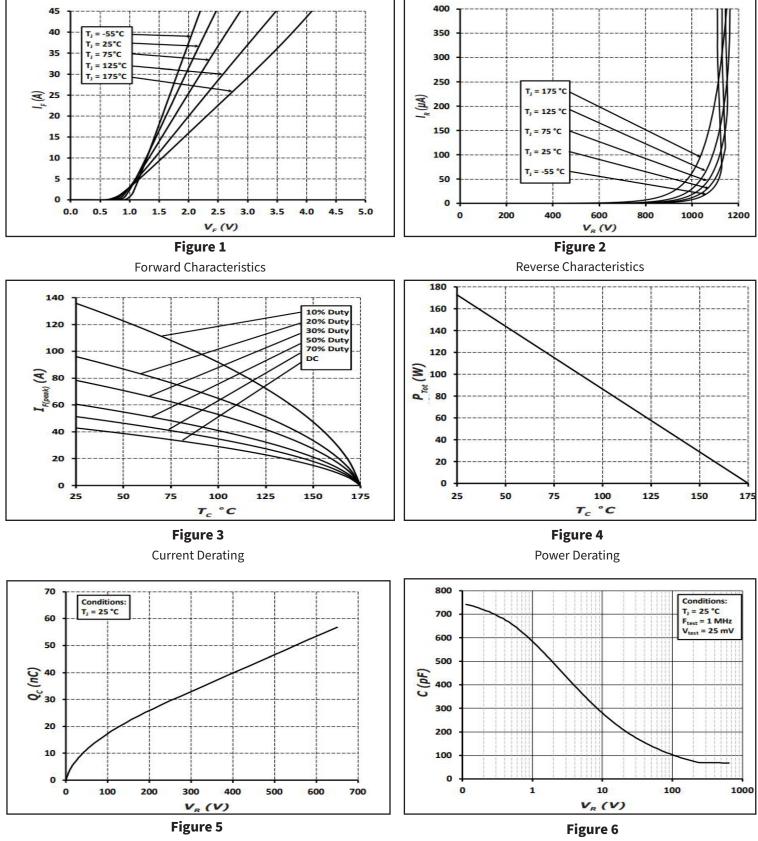
Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	$R_{_{\theta,JC(TYP)}}$	0.86	°C / W	
Junction Temperature	T _j	-55 to +175		
Case & Storage Temperature	T _c	-55 to +175	°C	
		1	Nm	M3 Screw
TO-247 Mounting Torque	-	8.8	lbf-in	6-32 Screw

Electrostatic Discharge (ESD) Classifications

Parameter	Symbol	Notes
Human Body Model	НВМ	Class 3B (≥ 8000 V)
Charge Device Model	CDM	Class C3 (≥ 1000 V)

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Typical Performance



Total Capacitance Charge vs. Reverse Voltage

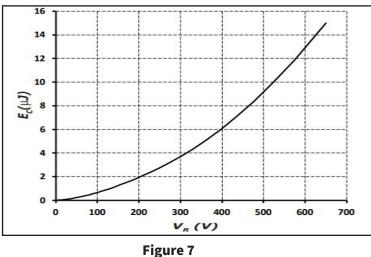
Capacitance vs. Reverse Voltage

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Typical Performance



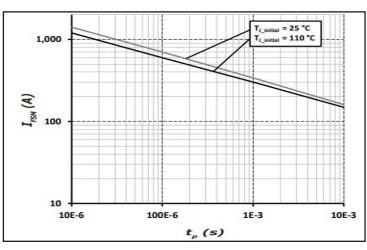
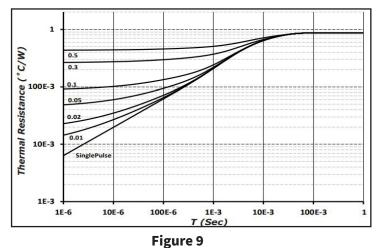


Figure 8

Non-Repetitive Peak Forward Surge Current vs. Pulse Duration



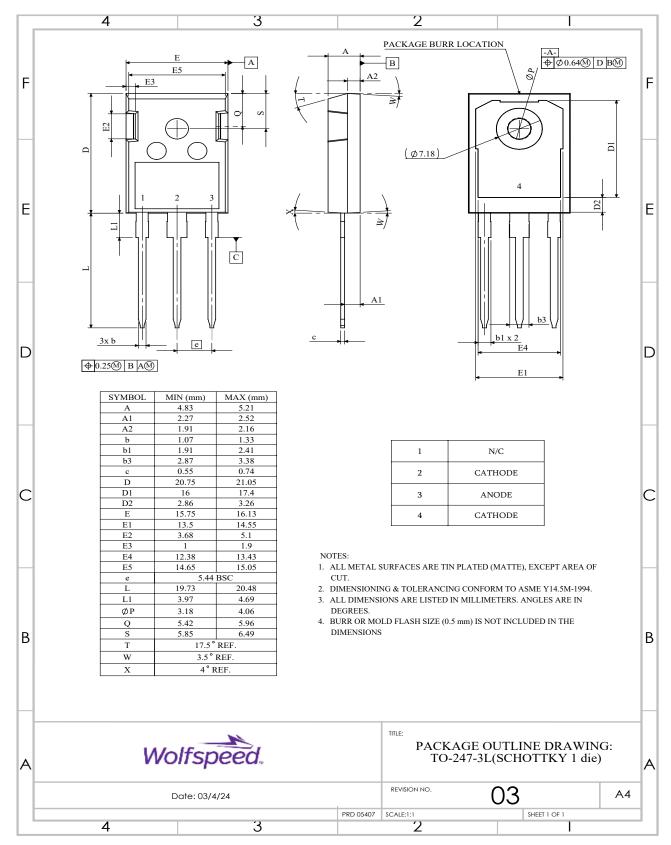
Capacitance Stored Energy

Transient Thermal Impedance

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Package Dimensions & Pin-Out

Package: TO-247-3

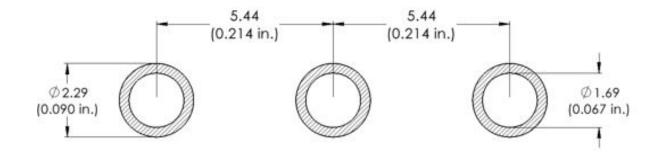


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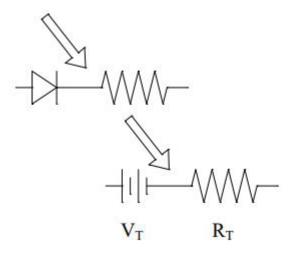
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Recommended Solder Pad Layout

Primary dimensions shown in mm.



Diode Model



 $Vf_T = V_T + If * R_T$ $V_T = 0.97 + (T_J * -1.0*10^{-3})$ $R_T = 0.024 + (T_J * 3.0*10^{-4})$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Product Ordering Information

Order Number	Packing Type		
C3D16065D1	Tube		

REACh, RoHS, and Halogen-Free compliance documentation available for this product.

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Revision History

Document Version	Date of Release	Description of Changes
0	August-2019	Initial Release
1	November-2023	Updated Branding, POD, Package Image, Solder Pad Layout
2	September - 2024	Legal Disclaimer and POD Updated

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Contact info:

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