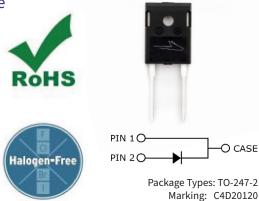


4th Generation 1200 V, 20 A Silicon Carbide Schottky Diode

### **Description**

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



#### **Features**

- Low Forward Voltage (V<sub>F</sub>) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior

### **Typical Applications**

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

# Maximum Ratings (T<sub>c</sub> = 25°C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes	
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	1200	V			
DC Blocking Voltage	V <sub>DC</sub>	1200	V			
	I <sub>F</sub>	54	A	T <sub>c</sub> = 25 °C	Fig. 3	
Continuous Forward Current		26		T <sub>c</sub> = 135 °C		
		20		T <sub>c</sub> = 156 °C		
Repetitive Peak Forward Surge Current		86		T <sub>c</sub> = 25 °C, t <sub>p</sub> = 10 ms, Half Sine Wave		
	FRM	56		$T_c = 110  ^{\circ}\text{C}, t_p = 10  \text{ms}, Half Sine Wave}$		
Non-Repetitive Forward Surge Current	I <sub>FSM</sub>	130		$T_c = 25  ^{\circ}\text{C}, t_p = 10  \text{ms}, Half Sine Wave}$	Fig. 8	
		104		$T_c = 110  ^{\circ}\text{C}, t_p = 10  \text{ms}, Half Sine Wave}$		
Non-Repetitive Peak Forward Surge Current	l <sub>F,Max</sub>	1150		$T_{c} = 25 {}^{\circ}\text{C}, t_{p} = 10 \mu\text{s}, \text{Pulse}$		
		950		$T_{c} = 110 {}^{\circ}\text{C},  t_{p} = 10 \mu\text{s},  \text{Pulse}$		
Power Dissipation	P <sub>tot</sub>	246	W	T <sub>c</sub> = 25 °C	Fig. 4	
		106.5		T <sub>c</sub> = 110 °C	Fig. 4	
i²t Value	∫i²t	84.5	A <sup>2</sup> s	$T_{c} = 25  ^{\circ}\text{C}, t_{p} = 10  \text{ms}$		
		54		$T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \text{ms}$		

### **Electrical Characteristics**

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Forward Voltage	.,	1.5	1.8	V	I <sub>F</sub> = 20 A, T <sub>j</sub> = 25 °C	Fig. 1
	V <sub>F</sub>	2.2	3		I <sub>F</sub> = 20 A, T <sub>j</sub> = 175 °C	
Reverse Current		35	200	- μΑ	V <sub>R</sub> = 1200 V, T <sub>j</sub> = 25 °C	Fig. 2
	I <sub>R</sub>	65	400		V <sub>R</sub> = 1200 V, T <sub>j</sub> = 175 °C	
Total Capacitive Charge	Q <sub>c</sub>	99		nC	$V_R = 800 \text{ V}, T_j = 25 \text{ °C}$	Fig. 5
Total Capacitance		1500			$V_R = 0 \text{ V, T}_j = 25 \text{ °C, f} = 1 \text{ MHz}$	
	С	93		pF	$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		67			$V_R = 800 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E <sub>c</sub>	28		μJ	V <sub>R</sub> = 800 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

### **Thermal & Mechanical Characteristics**

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R <sub>0, JC (TYP)</sub>	0.61	°C/W	
Junction Temperature	T <sub>j</sub>	-55 to +175	°C	
Case & Storage Temperature	T <sub>c</sub>	-55 to +150		
TO 247 Mauriting Tayous		1	Nm	M3 Screw
TO-247 Mounting Torque	-	8.8	lbf-in	6-32 Screw

### **Typical Performance**

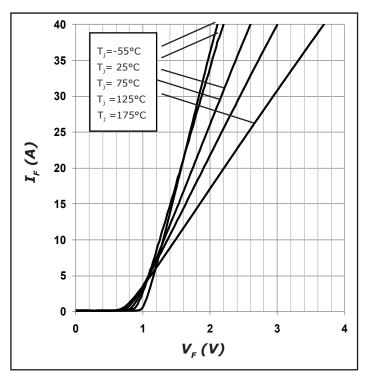
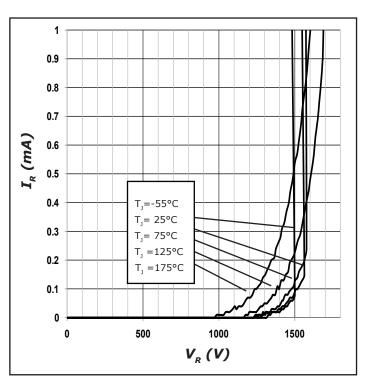


Figure 1. Forward Characteristics



**Figure 2. Reverse Characteristics** 

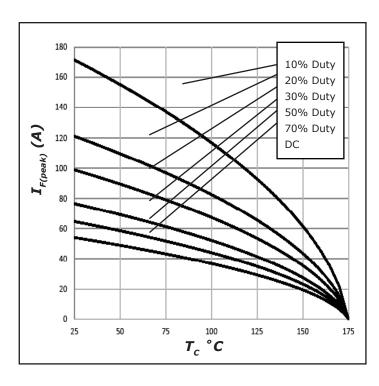


Figure 3. Current Derating

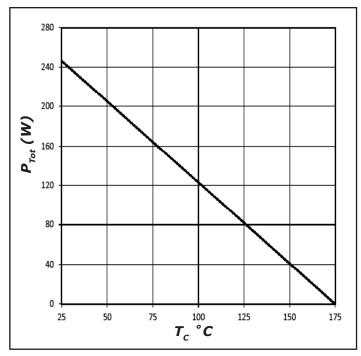


Figure 4. Power Derating

### **Typical Performance**

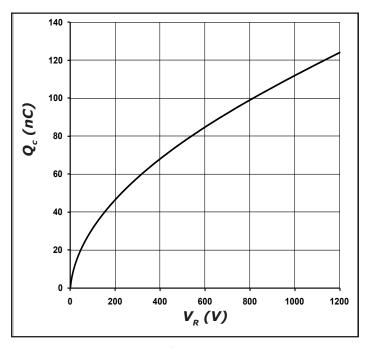


Figure 5.

Total Capacitance Charge vs. Reverse Voltage

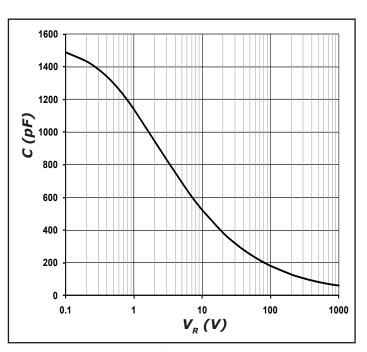
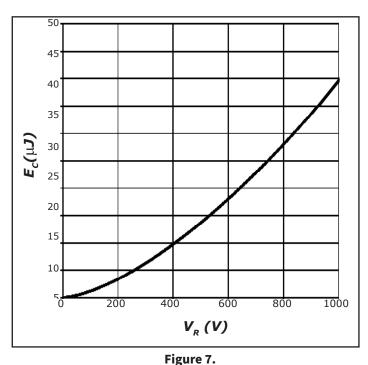


Figure 6.
Capacitance vs. Reverse Voltage



Typical Capacitance Stored Energy

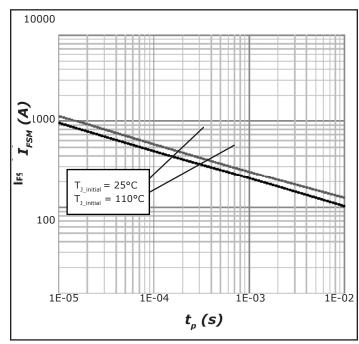


Figure 8. Non-Repetitive Peak Forward Surge Current versus Pulse Duration (sinusoidal waveform)

# **Typical Performance**

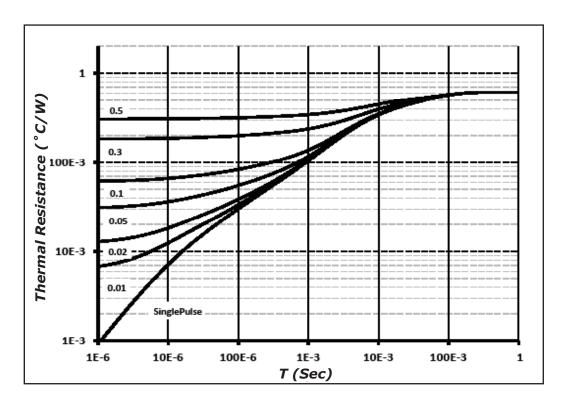
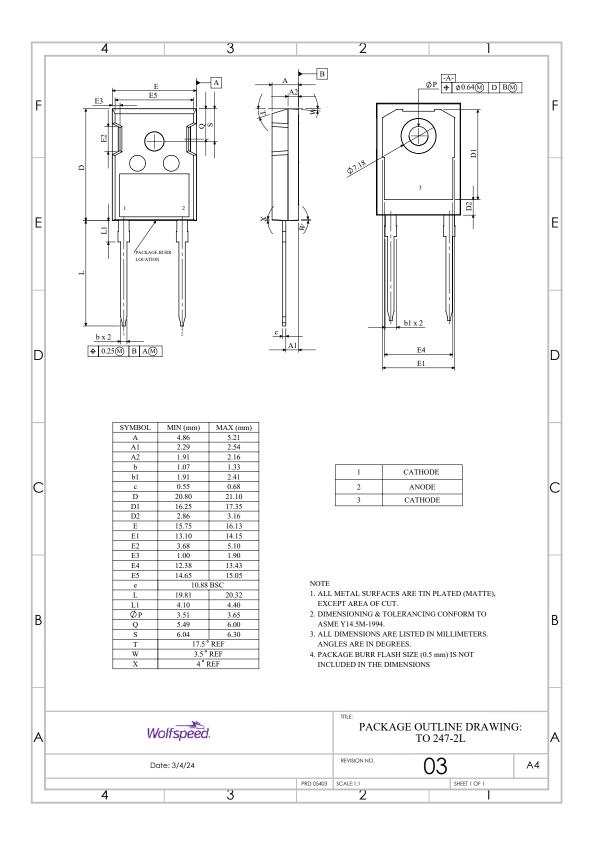


Figure 9
Transient Thermal Impedance

### **Package Dimensions & Pin-Out**

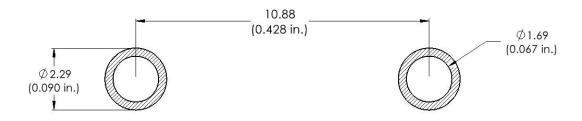
Package: TO-247-2 ( All dimensions are in mm)





### **Recommended Solder Pad Layout**

Package: TO-247-2 (All dimensions are in mm)



# **Product Ordering Information**

Order Number	Packing Type
C4D20120H	Tube

REACh, RoHS, and Halogen-Free compliance documentation available for this product.

# **Revision History**

Document Version	Date of Release	Description of changes
1	January - 2019	Initial Release
3	January-2023	Package Drawing and Landing pad Update
4	August-2024	Notes & Disclaimers, added ROHS and Halogen Free Symbols

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