Transistors Panasonic

2SC6050

Silicon NPN epitaxial planar type

For high frequency amplification, oscillation and mixing

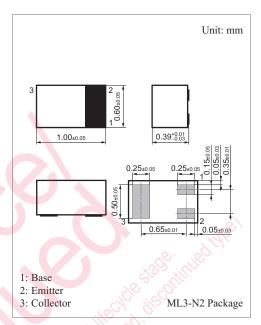
■ Features

- High transition frequency f_T
- ullet Small collector output capacitance (Common base, input open circuited) C_{ob} and reverse transfer capacitance (Common base) C_{rb}
- Optimum for high-density mounting and downsizing of the equipment for Ultraminiature leadless package

 $0.6 \text{ mm} \times 1.0 \text{ mm}$ (height 0.39 mm)

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V_{CBO}	15	V	
Collector-emitter voltage (Base open)	V _{CEO}	10	V	
Emitter-base voltage (Collector open)	V _{EBO}	3	V	
Collector current	I_{C}	50	mA	
Collector power dissipation	P _C	100	mW	
Junction temperature	T _j	125	°C	
Storage temperature	T _{stg}	-55 to +125	°C	



Marking Symbol: 6N

■ Electrical Characteristics $T_a = 25$ °C±3°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = 2 \text{ mA}, I_B = 0$	10			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = 10 \mu A, I_{\rm C} = 0$	3			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 10 \text{ V}, I_{E} = 0$			1	μА
Forward current transfer ratio	h_{FE}	$V_{CH} = 4 \text{ V}, I_C = 5 \text{ mA}$	75		400	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = 20 \text{ mA}, I_B = 4 \text{ mA}$			0.5	V
Transition frequency	f_T	$V_{CB} = 4 \text{ V}, I_{E} = -5 \text{ mA}, f = 200 \text{ MHz}$	1.4	1.9	2.7	GHz
Collector output capacitance (Common base, input open circuited)	Cob	$V_{CB} = 4 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$		1.4		pF
Reverse transfer capacitance (Common base)	C_{rb}	$V_{CB} = 4 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$		0.45		pF
Collector-base parameter	r _{bb'} • c _c	$V_{CB} = 4 \text{ V}, I_{E} = -5 \text{ mA}, f = 31.9 \text{ MHz}$		11		ps
h _{FE} ratio	Δh_{FE}	$V_{CH} = 4 \text{ V}, I_{C} = 100 \mu\text{A} / V_{CH} = 4 \text{ V}, I_{C} = 5 \text{mA}$	0.75		1.6	_

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

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