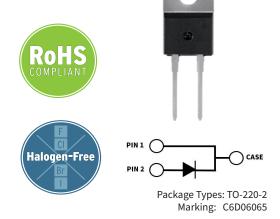


6th Generation 650 V, 6 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



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Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior

Typical Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

Maximum Ratings (T_c = 25°C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes	
Repetitive Peak Reverse Voltage	V _{RRM}	650				
DC Blocking Voltage	V _{DC}	650	V			
		24		T _c = 25 °C		
Continuous Forward Current	I _F	12		T _c = 125 °C	Fig. 3	
		6		T _c = 155 °C		
Repetitive Peak Forward Surge		28		T _c = 25 °C, t _p = 10 ms, Half Sine Wave		
Current	FRM	17	Α	$T_{c} = 110 ^{\circ}\text{C}, t_{p} = 10 \text{ms}, Half Sine Wave}$		
Non-Repetitive Forward Surge		51		T _c = 25 °C, t _p = 10 ms, Half Sine Wave	F: 0	
Current	FSM	46		$T_{c} = 110 ^{\circ}\text{C,t}_{p} = 10 \text{ms, Half Sine Wave}$	Fig. 8	
Non-Repetitive Peak Forward		520		$T_{c} = 25 ^{\circ}\text{C}, t_{p} = 10 \mu\text{s}, \text{Pulse}$		
Surge Current	F,Max	470		$T_{c} = 110^{\circ}\text{C}, t_{p} = 10 \mu\text{s}, \text{Pulse}$		
Power Dissipation		73	W	T _c = 25 °C	F	
	P _{tot}	32		T _C = 110 °C	Fig. 4	

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Famous ad Malda as		1.27	1.50		I _F = 6 A, T _j = 25 °C	F:_ 1
Forward Voltage	V _F	1.37	1.60	V	I _F = 6 A, T _j = 175 °C	Fig. 1
Reverse Current		2	30	μΑ	$V_R = 650 \text{ V}, T_j = 25 \text{ °C}$	Fig. 2
	I _R	15	120		V _R = 650 V, T _j = 175 °C	
Total Capacitive Charge	Q _c	23		nC	$V_R = 400 \text{ V}, T_j = 25 \text{ °C}$	Fig. 5
		394			$V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Total Capacitance	c	44		pF	$V_R = 200 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		36			$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	3.5		μJ	V _R = 400 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R _{0, JC (TYP)}	2.05	°C/W	
Junction Temperature	T _j	-55 to +175		
Case & Storage Temperature	T _c	-55 to +175	°C	
	-	1	Nm	M3 Screw
TO-220 Mounting Torque		8.8	lbf-in	6-32 Screw

Typical Performance

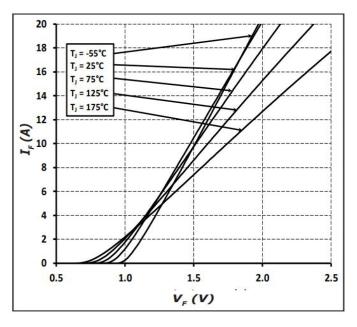


Figure 1Forward Characteristics

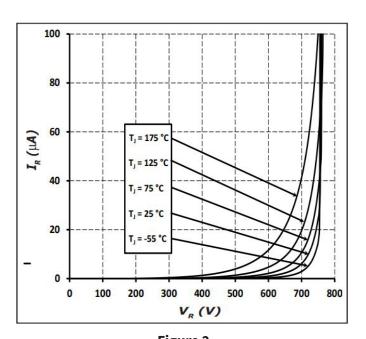


Figure 2Reverse Characteristics

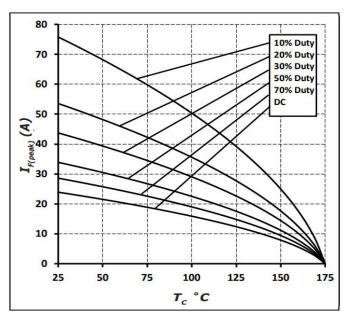


Figure 3Current Derating

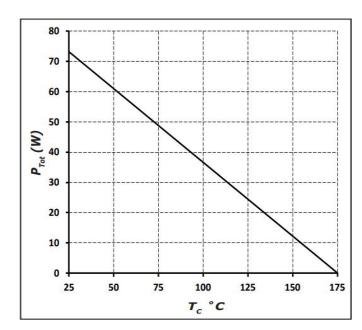
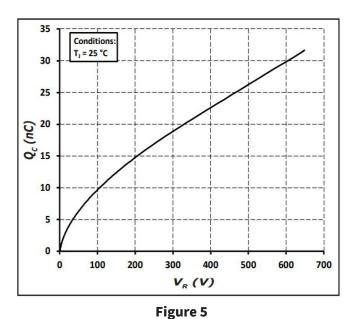


Figure 4Power Derating



Total Capacitance vs. Reverse Voltage

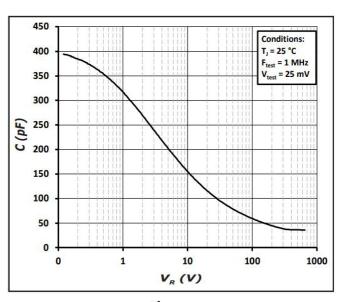


Figure 6Capacitace vs. Reverse Voltage

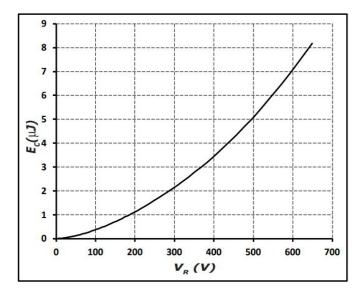


Figure 7Capacitance Stored Energy

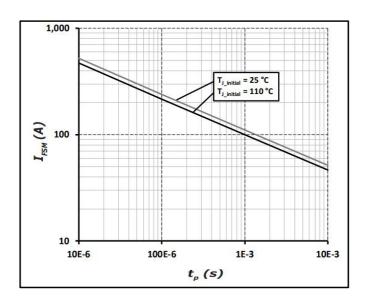


Figure 8Non-Repetitive Peak Forward Surge Current versus Pulse Duration (sinusoidal waveform)

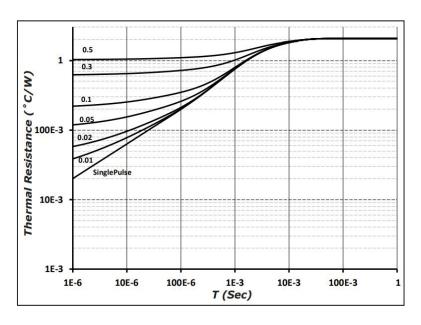
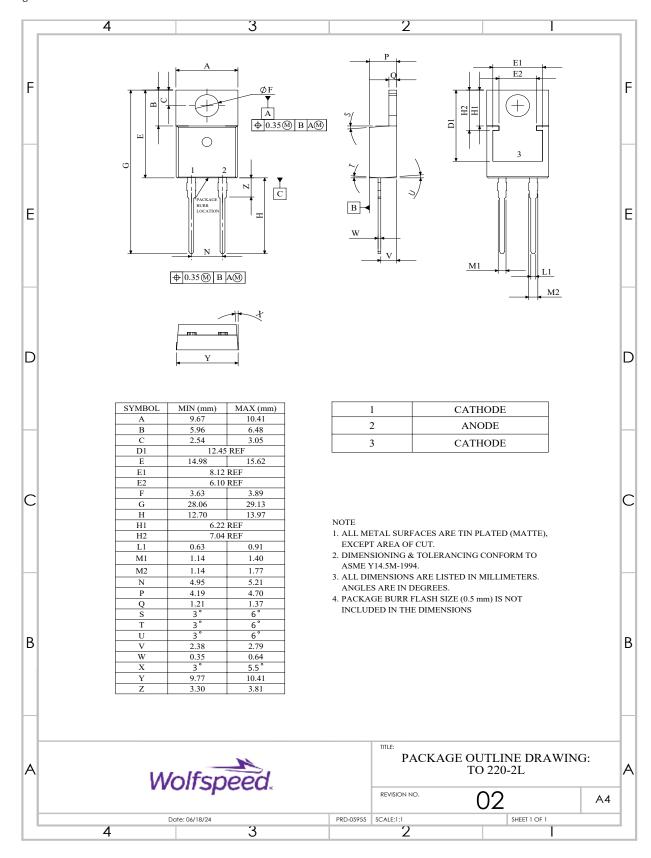


Figure 9Transient Thermal Impedance

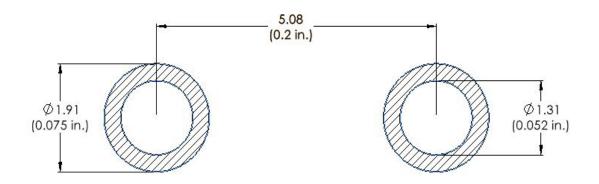
Package Dimensions & Pin-Out

Package: TO-220-2



Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type
C6D06065A	Tube

Revision History

Document Version	Date of Release	Description of Changes
0	April-2019	Initial Release
1	March-2023	Update Package Drawing Update Landing Pad
2	July-2023	Updated Test Conditions of I _F and P _{tot} Added Package Marking Statement
3	October - 2024	Legal disclaimer, POD, corrected package marking

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