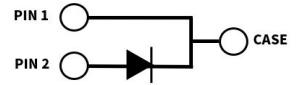


# C6D10065A

## 6th Generation 650 V, 10 A Silicon Carbide Schottky Diode

### Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Package Types: TO-220-2  
Marking: C6D10065

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### Features

- Low Forward Voltage ( $V_F$ ) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior

### Typical Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

### Maximum Ratings ( $T_c = 25^\circ\text{C}$ Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes
Repetitive Peak Reverse Voltage	$V_{RRM}$	650	V		
DC Blocking Voltage	$V_{DC}$	650			
Continuous Forward Current	$I_F$	37	A	$T_c = 25^\circ\text{C}$	Fig. 3
		19		$T_c = 125^\circ\text{C}$	
		10		$T_c = 155^\circ\text{C}$	
Repetitive Peak Forward Surge Current	$I_{FRM}$	45	A	$T_c = 25^\circ\text{C}, t_p = 10\text{ ms}, \text{Half Sine Wave}$	
		27		$T_c = 110^\circ\text{C}, t_p = 10\text{ ms}, \text{Half Sine Wave}$	
Non-Repetitive Forward Surge Current	$I_{FSM}$	86	A	$T_c = 25^\circ\text{C}, t_p = 10\text{ ms}, \text{Half Sine Wave}$	Fig. 8
		75		$T_c = 110^\circ\text{C}, t_p = 10\text{ ms}, \text{Half Sine Wave}$	
Non-Repetitive Peak Forward Surge Current	$I_{F,Max}$	1250	A	$T_c = 25^\circ\text{C}, t_p = 10\text{ }\mu\text{s}, \text{Pulse}$	
		1100		$T_c = 110^\circ\text{C}, t_p = 10\text{ }\mu\text{s}, \text{Pulse}$	
Power Dissipation	$P_{tot}$	109	W	$T_c = 25^\circ\text{C}$	Fig. 4
		47		$T_c = 110^\circ\text{C}$	

## Electrical Characteristics

Parameter	Symbol	Typ.	Max.	Unit	Test Conditions	Notes
Forward Voltage	$V_F$	1.27	1.50	V	$I_F = 10 \text{ A}, T_j = 25 \text{ }^\circ\text{C}$	Fig. 1
		1.37	1.60		$I_F = 10 \text{ A}, T_j = 175 \text{ }^\circ\text{C}$	
Reverse Current	$I_R$	2	50	$\mu\text{A}$	$V_R = 650 \text{ V}, T_j = 25 \text{ }^\circ\text{C}$	Fig. 2
		15	200		$V_R = 650 \text{ V}, T_j = 175 \text{ }^\circ\text{C}$	
Total Capacitive Charge	$Q_C$	34		nC	$V_R = 400 \text{ V}, T_j = 25 \text{ }^\circ\text{C}$	Fig. 5
Total Capacitance	C	611		pF	$V_R = 0 \text{ V}, T_j = 25 \text{ }^\circ\text{C}, f = 1 \text{ MHz}$	Fig. 6
		67			$V_R = 200 \text{ V}, T_j = 25 \text{ }^\circ\text{C}, f = 1 \text{ MHz}$	
		53			$V_R = 400 \text{ V}, T_j = 25 \text{ }^\circ\text{C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	$E_C$	5.2		$\mu\text{J}$	$V_R = 400 \text{ V}$	Fig. 7

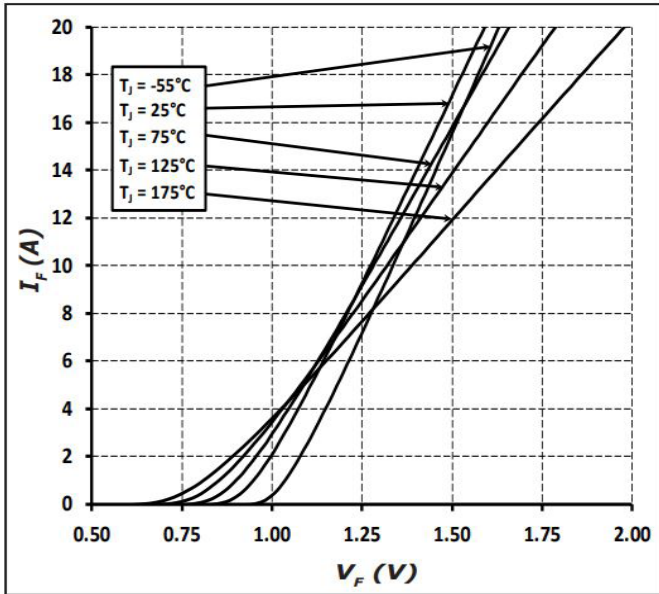
### Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

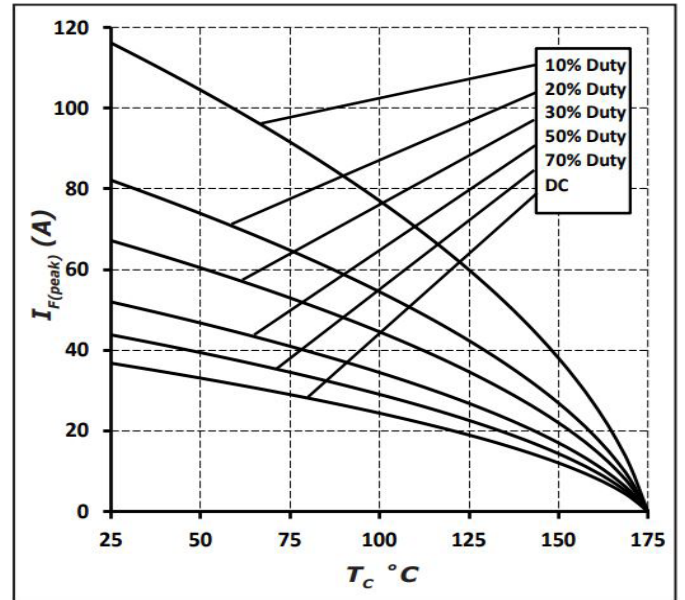
## Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	$R_{\theta, JC (TYP)}$	1.62	$^\circ\text{C} / \text{W}$	
Junction Temperature	$T_j$	-55 to +175	$^\circ\text{C}$	
Case & Storage Temperature	$T_c$	-55 to +175		
TO-220 Mounting Torque	-	1	Nm	M3 Screw
		8.8	lbf-in	6-32 Screw

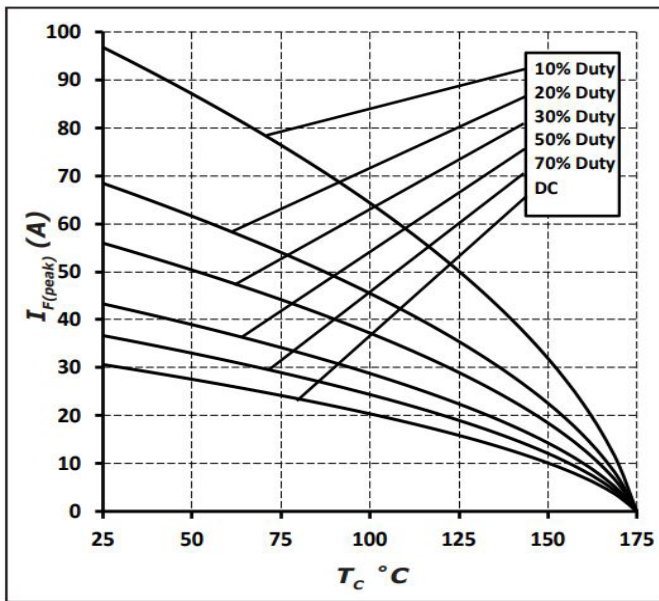
Typical Performance



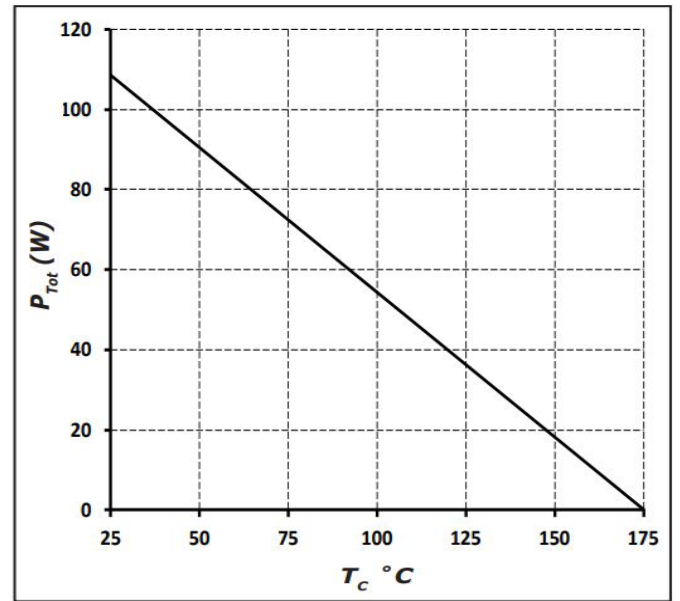
**Figure 1**  
Forward Characteristics



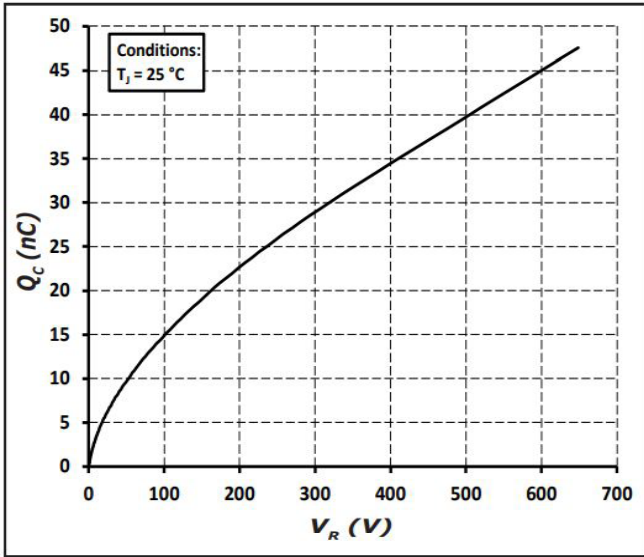
**Figure 2**  
Reverse Characteristics



**Figure 3**  
Current Derating

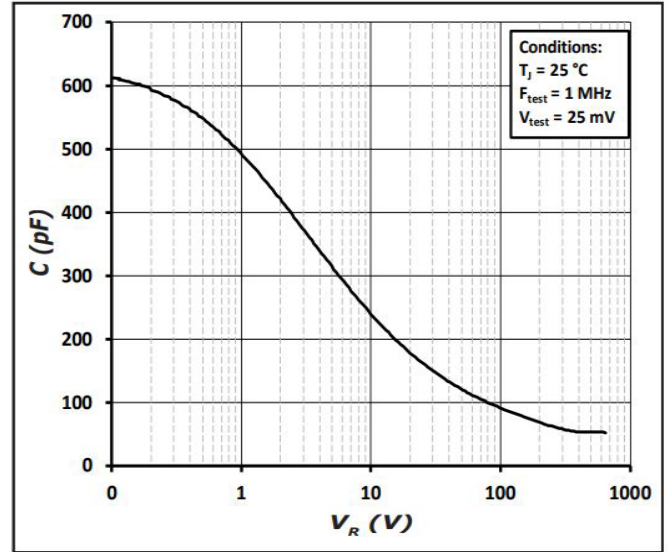


**Figure 4**  
Power Derating



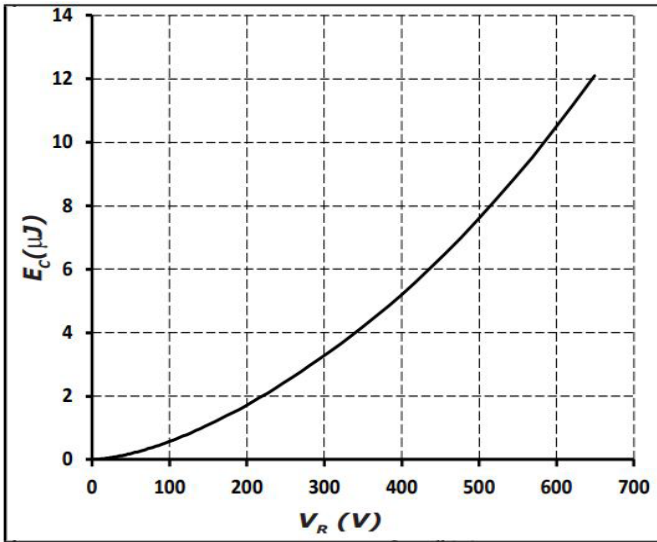
**Figure 5**

Total Capacitance vs. Reverse Voltage



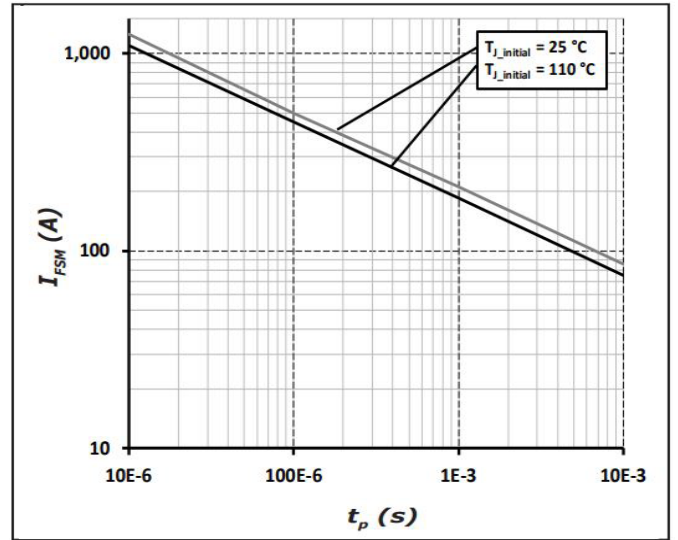
**Figure 6**

Capacitance vs. Reverse Voltage



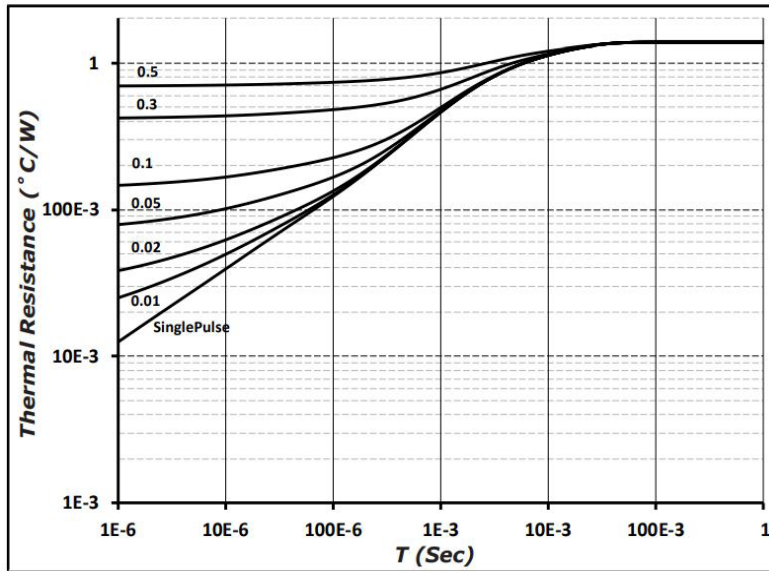
**Figure 7**

Capacitance Stored Energy



**Figure 8**

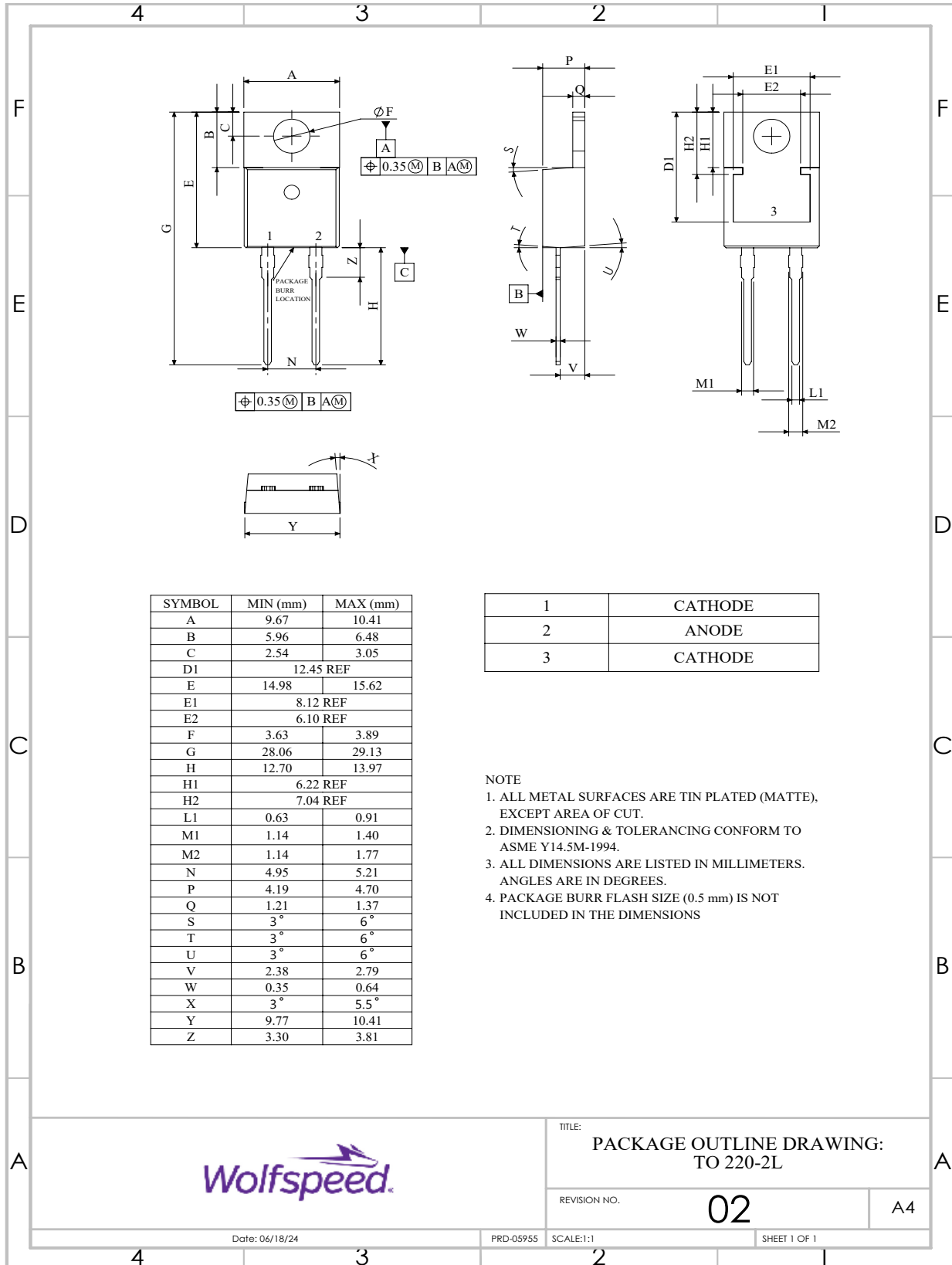
Non-Repetitive Peak Forward Surge Current versus Pulse Duration (sinusoidal waveform)



**Figure 9**  
Transient Thermal Impedance

### Package Dimensions & Pin-Out

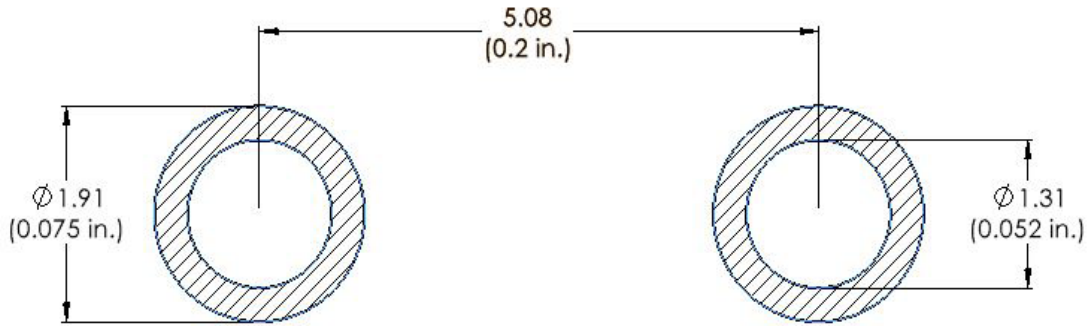
Package: TO-220-2





### Recommended Solder Pad Layout

Primary dimensions shown in mm.



### Product Ordering Information

Order Number	Packing Type
C6D10065A	Tube



## Revision History

Document Version	Date of Release	Description of Changes
-	April-2019	Initial Release
0	March-2023	Update Package Drawing Update Landing Pad
1	July-2023	Updated Test Conditions of $I_F$ and $P_{tot}$ Added Package Marking Statement
2	October - 2024	Legal Disclaimer, POD



## Notes & Disclaimer

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### Contact info:

4600 Silicon Drive  
Durham, NC 27703 USA  
Tel: +1.919.313.5300  
[www.wolfspeed.com/power](http://www.wolfspeed.com/power)

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