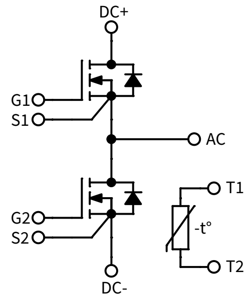
CAB5R0A23GM4, CAB5R0A23GM4T

V_{DS}	2300 V
$R_{DS(on)}$	5.0 mΩ

2300 V, 5.0 mΩ, Silicon Carbide, Half-Bridge Module

Technical Features

- Ultra-Low Loss
- High Frequency Operation
- Zero Turn-Off Tail Current from MOSFET
- Normally-Off, Fail-Safe Device Operation
- Aluminium Nitride Substrate
- Optional Pre-Applied Thermal Interface Material



Typical Applications

- DC Fast Chargers
- Energy Storage Systems
- High-Efficiency Converters / Inverters
- Renewable Energy
- Smart-Grid / Grid-Tied Distributed Generation
- Solar Inverters

System Benefits

- Enables Compact, Lightweight Systems
- Enables Two-Level Conversion for 1500 VDC Systems
- Increased System Efficiency due to Low Switching & Conduction Losses of SiC
- Reduced Thermal Requirements and System Cost

Key Parameters

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	Note
Drain-Source Voltage	V_{DS}			2300	V		
Maximum Gate-Source Voltage	$V_{GS(max)}$	-8		+19	V	Transient	Fig. 33 Note 1
Operational Gate-Source Voltage	$V_{GS(op)}$		-4/15		V	Static	Fig. 33 Note 1
DC Continuous Drain Current ($T_{VJ} \leq 150$ °C)	I_D			150	A	$V_{GS} = 15$ V, $T_{HS} = 75$ °C, $T_{VJ} \leq 150$ °C	Notes 2,3,4 Fig. 20
Pulsed Drain Current	I_{DM}			300	A	t_{Pmax} limited by T_{VJmax} $V_{GS} = 15$ V, $T_{HS} = 75$ °C	Notes 2,3,4 Fig. 20
Power Dissipation	P_D		710		W	$T_{HS} = 75$ °C, $T_{VJ} \leq 150$ °C	Note 5 Fig. 21
Virtual Junction Temperature	$T_{VJ(op)}$	-40		150	°C	Operation	

Note (1): Recommended turn-on gate voltage is 15 V with ±5% regulation tolerance
Note (2): Current limit at $T_{HS} = 75$°C, $T_{VJ} \leq 150$ °C imposed by package
Note (3): Continuous DC operational limit set by DC- pins. See Figure 22 for implementable AC current
Note (4): Verified by design
Note (5): $P_D = (T_{VJ} - T_{HS})/R_{TH(JH,typ)}$

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MOSFET Characteristics (Per Position) (T_{VJ} = 25 °C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	Note
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	2300			V	V_{GS} = 0 V, T_{VJ} = -40 °C	
Gate Threshold Voltage	$V_{GS(th)}$	1.8	2.5	4.0	V	V_{DS} = V_{GS}, I_D = 114 mA	
			2.1		V	V_{DS} = V_{GS}, I_D = 114 mA, T_{VJ} = 150°C	
Zero Gate Voltage Drain Current	I_{DSS}		15	900	µA	V_{GS} = 0 V, V_{DS} = 2300 V	
Gate-Source Leakage Current	I_{GSS}		75	1500	nA	V_{GS} = 15 V, V_{DS} = 0 V	
Drain-Source On-State Resistance (Devices Only)	$R_{DS(on)}$		5.0	7.0	mΩ	V_{GS} = 15 V, I_D = 240 A	Fig. 2 Fig. 3
			11.9		mΩ	V_{GS} = 15 V, I_D = 240 A, T_{VJ} = 150 °C	
Transconductance	g_{fs}		235		S	V_{DS} = 20 V, I_D = 240 A	Fig. 4
			230		S	V_{DS} = 20 V, I_D = 240 A, T_{VJ} = 150 °C	
Turn-On Switching Energy, T_{VJ} = 25 °C T_{VJ} = 125 °C T_{VJ} = 150 °C	E_{On}		4.5 5.0 5.2		mJ	V_{DD} = 1200 V, I_D = 240 A, V_{GS} = -4 V/15 V, $R_{G(OFF)}$ = 0.0 Ω, $R_{G(ON)}$ = 0.0 Ω, $L_σ$ = 18 nH	Fig. 11 Fig. 13
Turn-Off Switching Energy, T_{VJ} = 25 °C T_{VJ} = 125 °C T_{VJ} = 150 °C	E_{Off}		4.3 4.6 4.7		mJ		
Internal Gate Resistance	$R_{G(int)}$		1.1		Ω	f = 100 kHz	
Input Capacitance	C_{iss}		36.6		nF	V_{GS} = 0 V, V_{DS} = 1500 V, V_{AC} = 25 mV, f = 100 kHz	Fig. 9
Output Capacitance	C_{oss}		0.60		nF		
Reverse Transfer Capacitance	C_{rss}		48		pF		
Gate to Source Charge	Q_{GS}		275		nC	V_{DS} = 1500 V, V_{GS} = -4 V/15 V, I_D = 240 A, Per IEC60747-8-4 pg 21	
Gate to Drain Charge	Q_{GD}		235		nC		
Total Gate Charge	Q_G		880		nC		
FET Thermal Resistance, Junction to Heatsink	$R_{th\ JHS}$		0.105		°C/W	Measured with Pre-Applied TIM	Fig. 17

Diode Characteristics (Per Position) (T_{VJ} = 25 °C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	Notes
Body Diode Forward Voltage	V_{SD}		5.8		V	V_{GS} = -4 V, I_{SD} = 240 A	Fig. 7
			5.3		V	V_{GS} = -4 V, I_{SD} = 240 A, T_{VJ} = 150 °C	
DC Source-Drain Current (Body Diode)	$I_{SD\ BD}$		130		A	V_{GS} = -4 V, T_{HS} = 75 °C, T_{VJ} ≤ 150 °C	Note 5 Fig. 20
Reverse Recovery Time	t_{RR}		395		ns	V_{GS} = -4 V, I_{SD} = 240 A, V_R = 1200 V di/dt = 20 A/ns, T_{VJ} = 150 °C	Fig. 32
Reverse Recovery Charge	Q_{RR}		11.6		µC		
Peak Reverse Recovery Current	I_{RRM}		290		A		
Reverse Recovery Energy, T_{VJ} = 25 °C T_{VJ} = 125 °C T_{VJ} = 150 °C	E_{RR}		2.3 8.2 12		mJ	V_{DD} = 1200 V, I_D = 240 A, V_{GS} = -4 V/15 V, $R_{G(ON)}$ = 0.0 Ω, $L_σ$ = 18 nH	Fig. 14

Module Physical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Package Resistance, M1 (High-Side)	R_{pkg1}		1.5		mΩ	T_C = 125°C, Note 6
Package Resistance, M2 (Low-Side)	R_{pkg2}		1.4			
Stray Inductance	L_{Stray}		11		nH	Between DC- and DC+, f = 10 MHz
Case Temperature	T_C	-40		125	°C	
Mounting Torque	M_S		2.0	2.3	N-m	M4 bolts
Weight	W		36		g	
Case Isolation Voltage	V_{isol}	5			kV	AC, 50 Hz, 1 minute
Comparative Tracking Index	CTI	600				
Clearance Distance			8.1		mm	Terminal to Terminal
			13.2			Terminal to Heatsink
Creepage Distance			9.8			Terminal to Terminal
			14.9			Terminal to Heatsink

Note (6): Total Effective Resistance (Per Switch Position) = MOSFET $R_{DS(on)}$ + Switch Position Package Resistance

Temperature Sensor (NTC) Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Rated Resistance	R_{NTC}		5.0		kΩ	T_{NTC} = 25°C
Resistance Tolerance at 25 °C	ΔR/R	-5		5	%	
Beta Value (T_2 = 50 °C)	$ß_{25/50}$		3380		K	
Beta Value (T_2 = 80 °C)	$ß_{25/80}$		3468		K	
Beta Value (T_2 = 100 °C)	$ß_{25/100}$		3523		K	
Power Dissipation	P_{Max}			10	mW	T_{NTC} = 25°C

Typical Performance

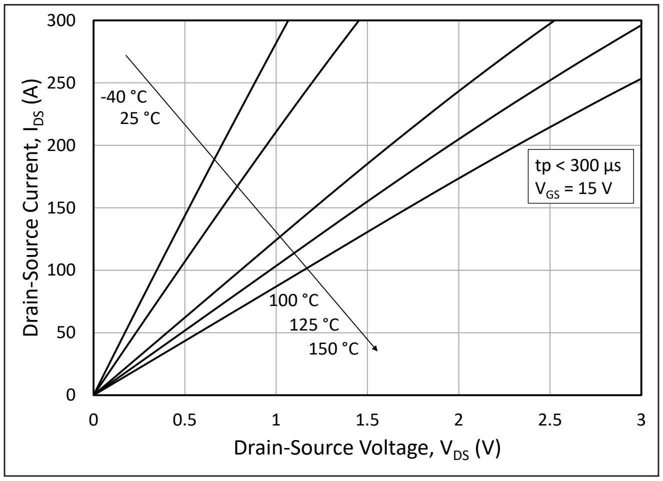


Figure 1. Output Characteristics for Various Junction Temperatures

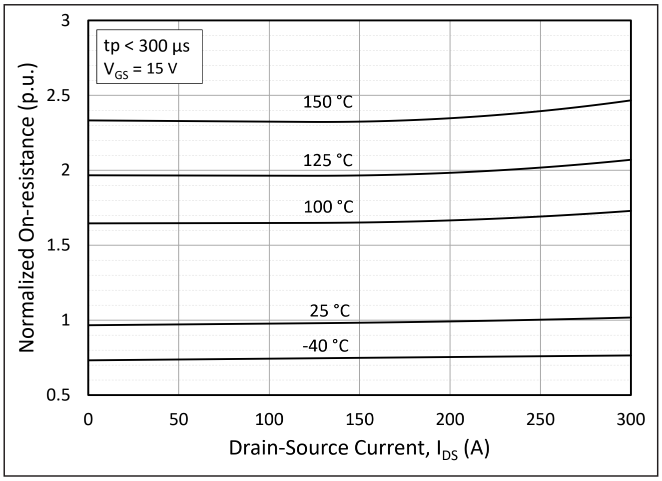


Figure 2. Normalized On-State Resistance vs. Drain Current for Various Junction Temperatures

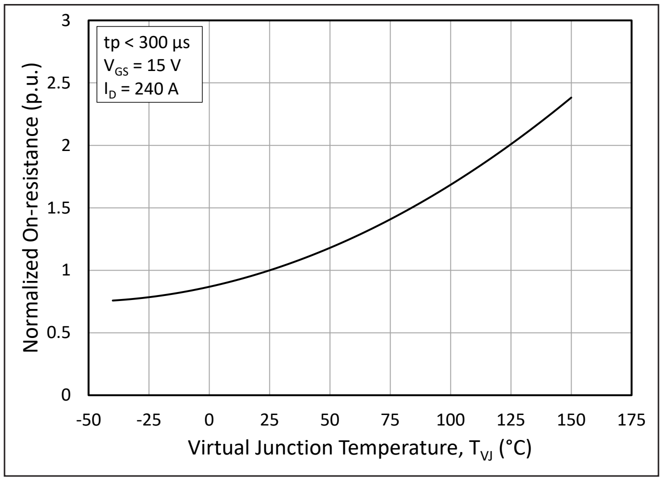


Figure 3. Normalized On-State Resistance vs. Junction Temperature

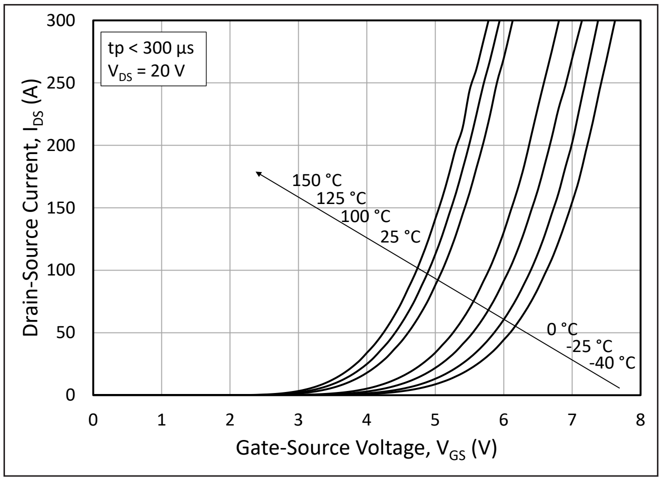


Figure 4. Transfer Characteristic for Various Junction Temperatures

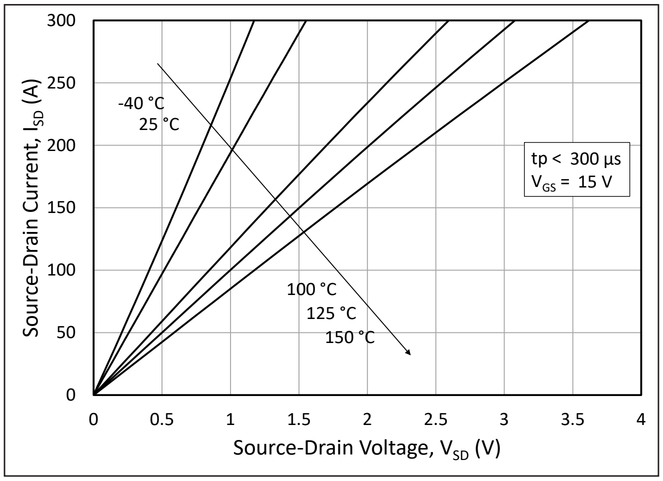


Figure 5. 3rd Quadrant Characteristic vs. Junction Temperatures at V_{GS} = 15 V

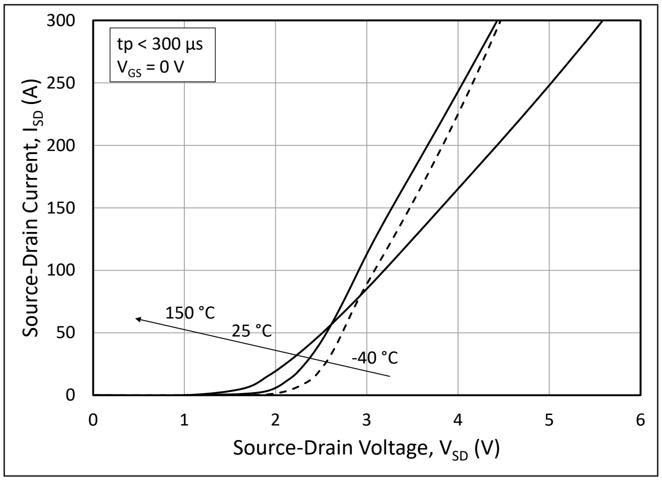


Figure 6. 3rd Quadrant Characteristic vs. Junction Temperatures at V_{GS} = 0 V (Body Diode)

Typical Performance

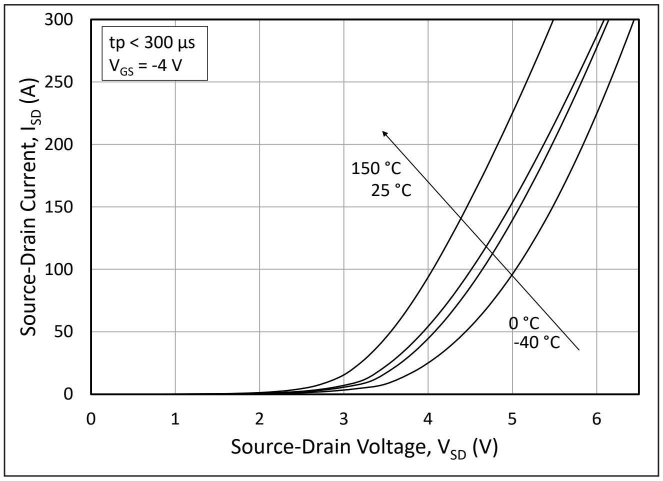


Figure 7. 3rd Quadrant Characteristic vs. Junction Temperatures at V_{GS} = - 4 V (Body Diode)

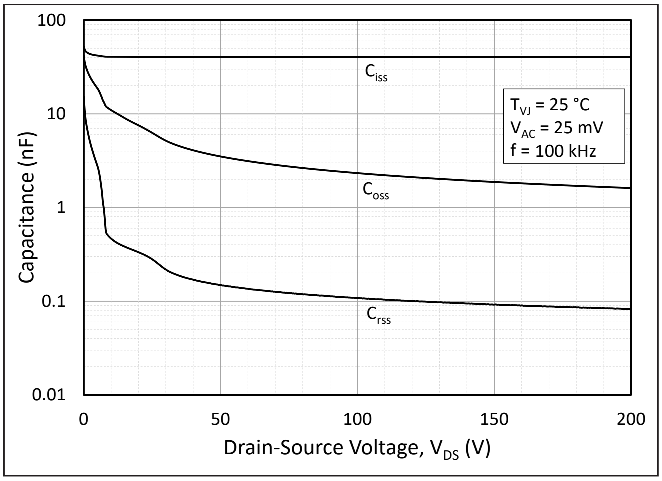


Figure 8. Typical Capacitances vs. Drain to Source Voltage (0 - 200V)

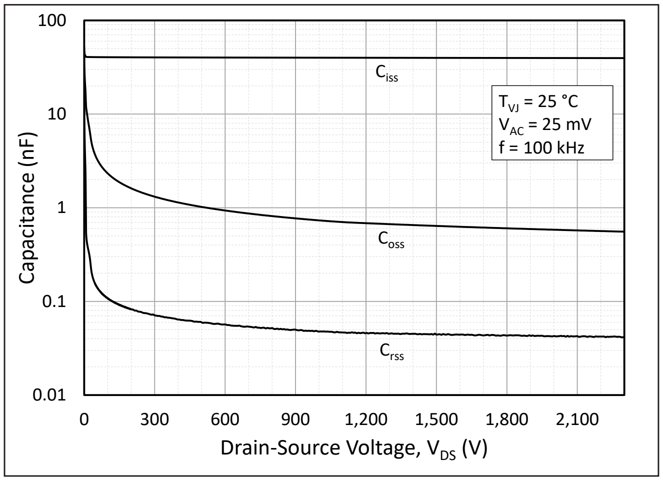


Figure 9. Typical Capacitances vs. Drain to Source Voltage (0 - 2300V)

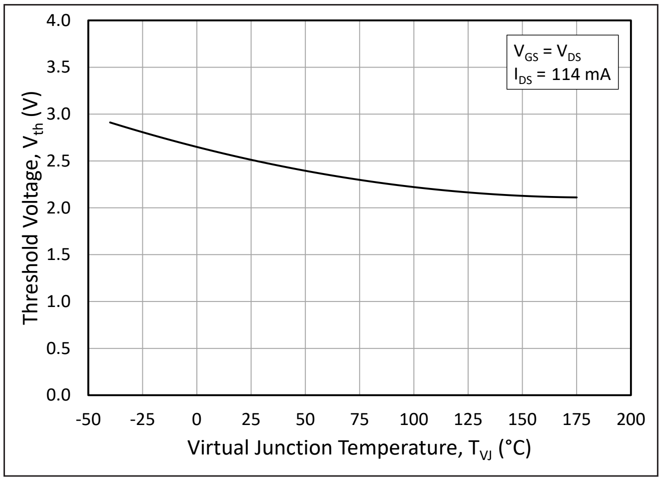


Figure 10. Threshold Voltage vs. Junction Temperature

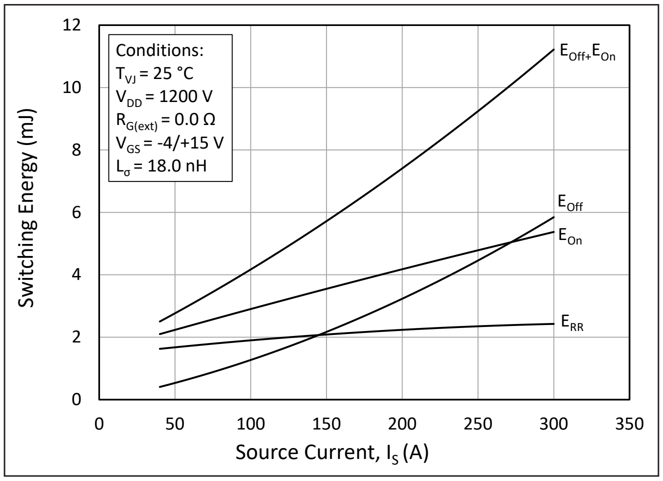


Figure 11. Switching Energy vs. Drain Current (V_{DD} = 1200 V)

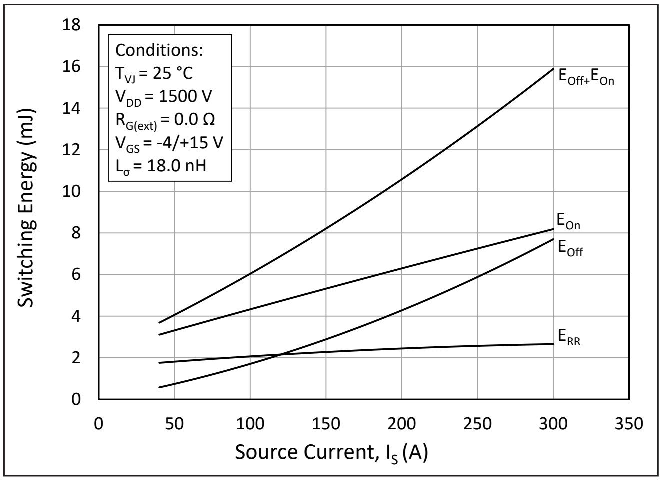


Figure 12. Switching Energy vs. Drain Current (V_{DD} = 1500 V)

Typical Performance

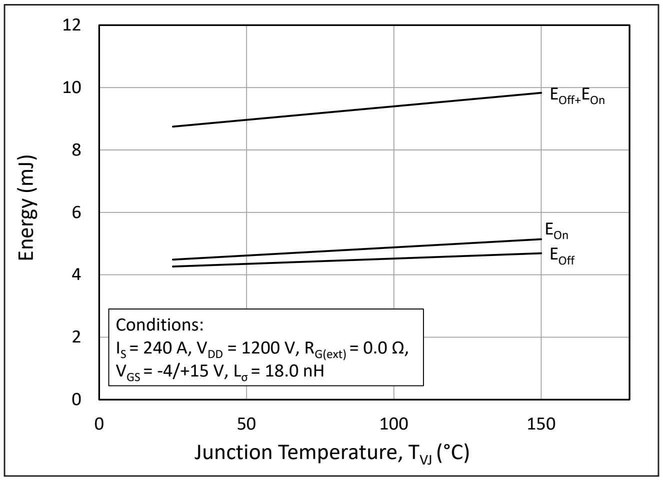


Figure 13. MOSFET Switching Energy vs. Junction Temperature

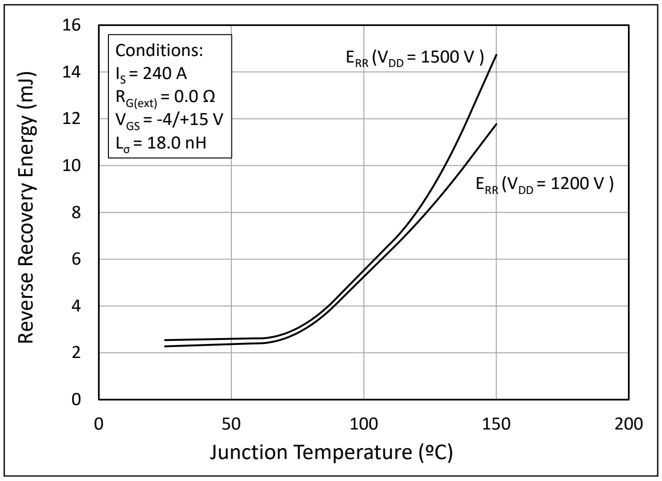


Figure 14. Reverse Recovery Energy vs. Junction Temperature

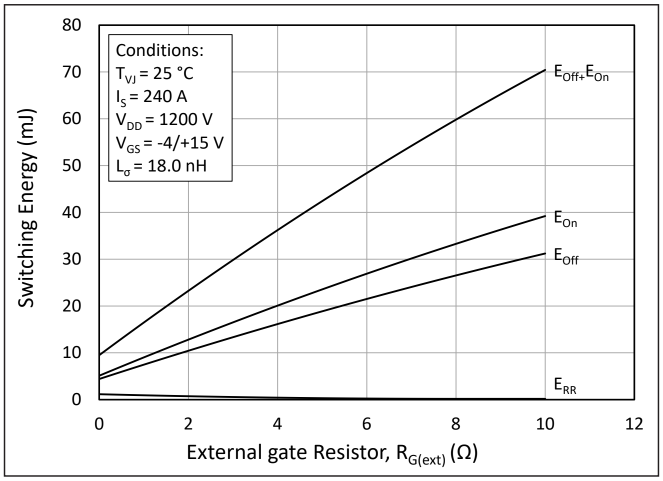


Figure 15. MOSFET Switching Energy vs. External Gate Resistance

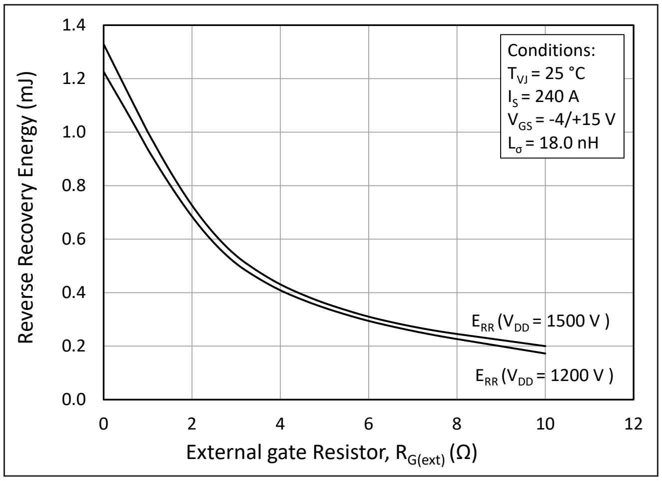


Figure 16. Reverse Recovery Energy vs. External Gate Resistance

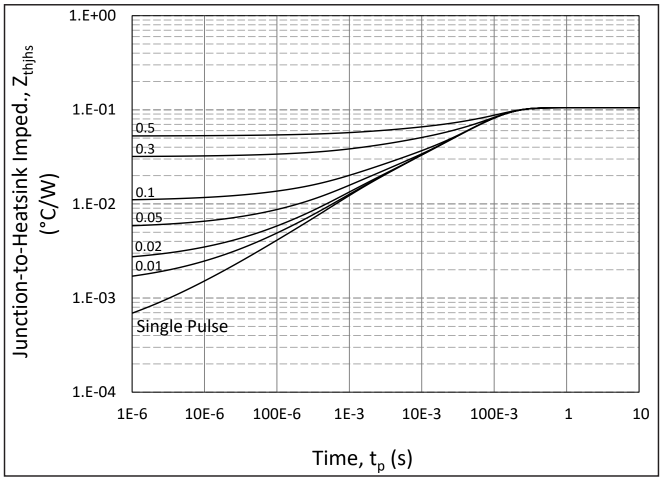


Figure 17. MOSFET Junction to Heatsink Transient Thermal Impedance, $Z_{th\ JHS}$ (°C/W)

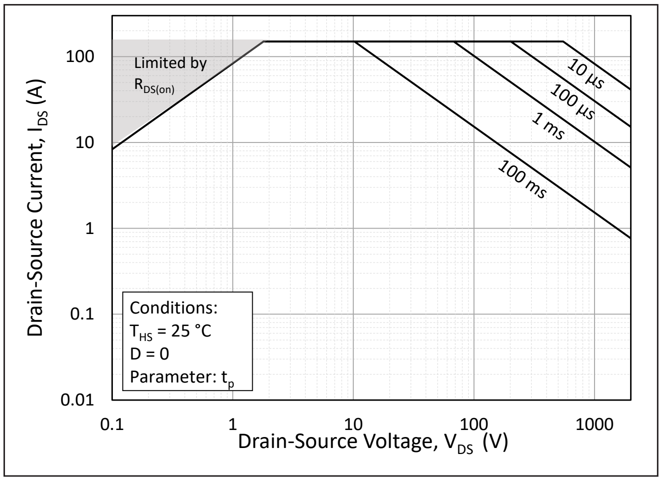


Figure 18. Forward Bias Safe Operating Area (FBSOA)

Typical Performance

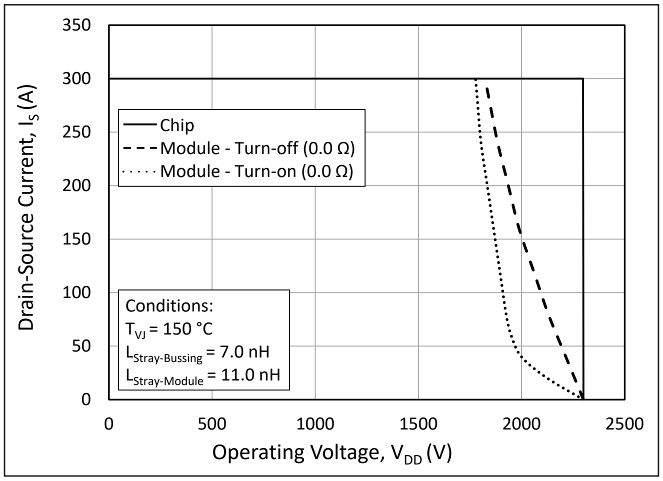


Figure 19. Switching Safe Operating Area

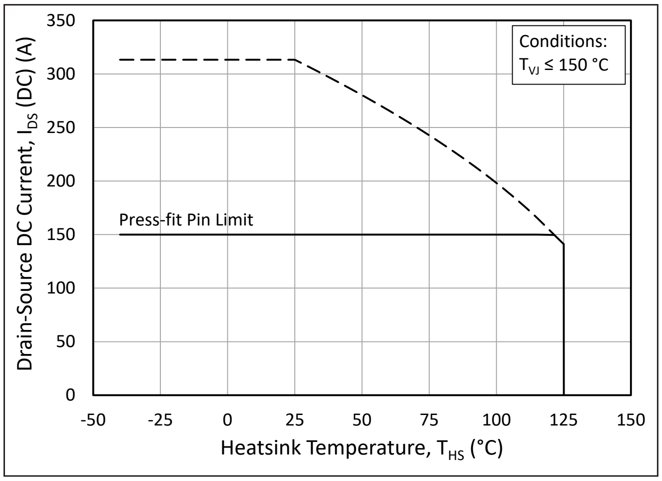


Figure 20. Continuous Drain Current Derating vs. Heatsink Temperature

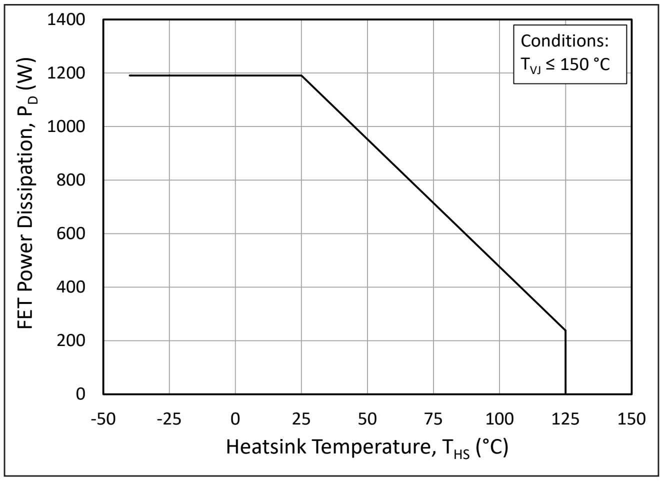


Figure 21. Maximum Power Dissipation Derating vs. Heatsink Temperature

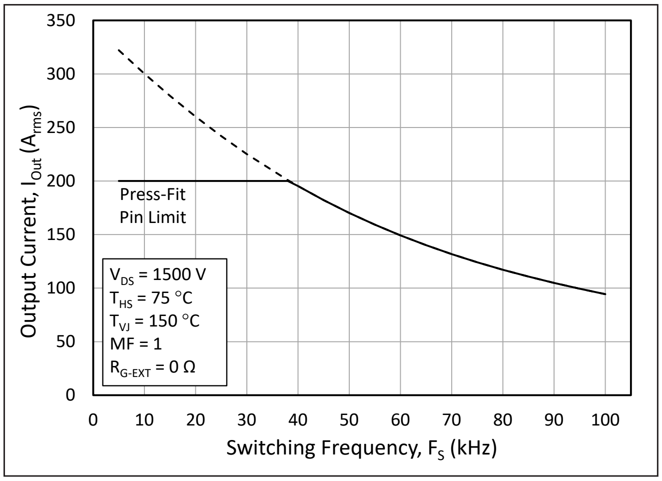


Figure 22. Typical Output Current Capability vs. Switching Frequency (Inverter Application)

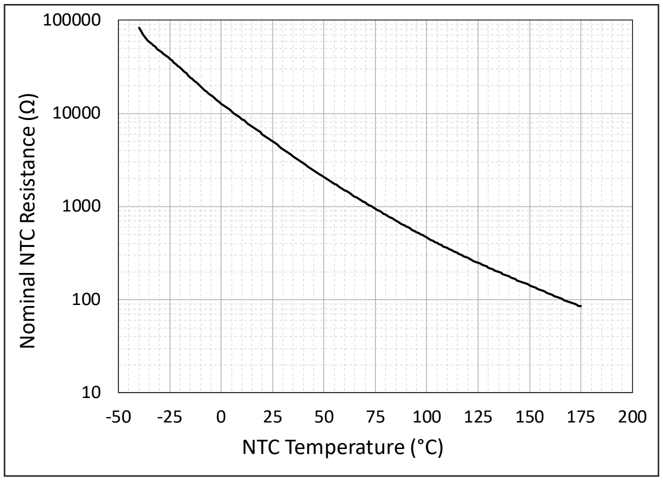


Figure 23. Nominal NTC Resistance vs. NTC Temperature

Timing Characteristics

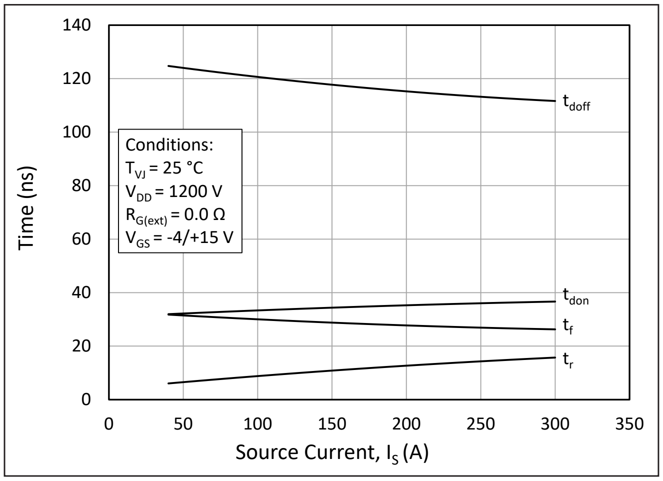


Figure 24. Timing vs. Source Current

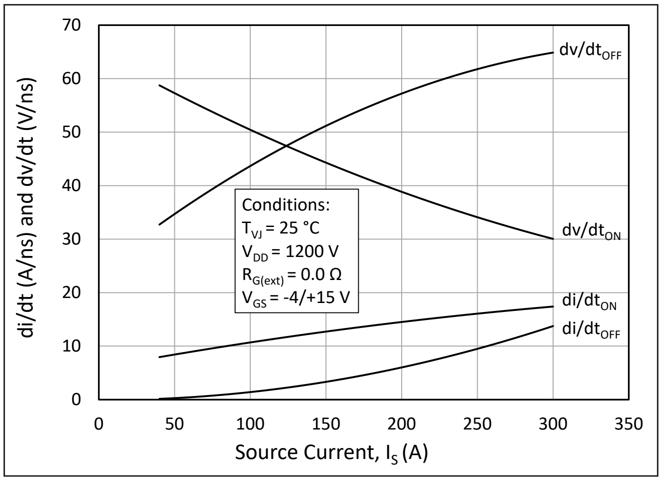


Figure 25. dv/dt and di/dt vs. Source Current

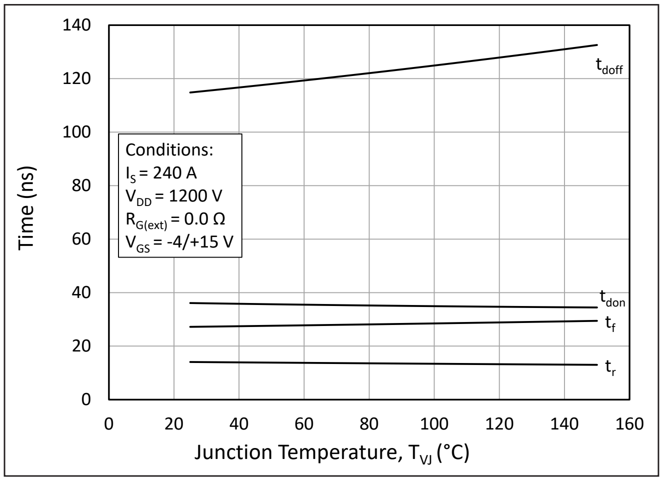


Figure 26. Timing vs. Junction Temperature

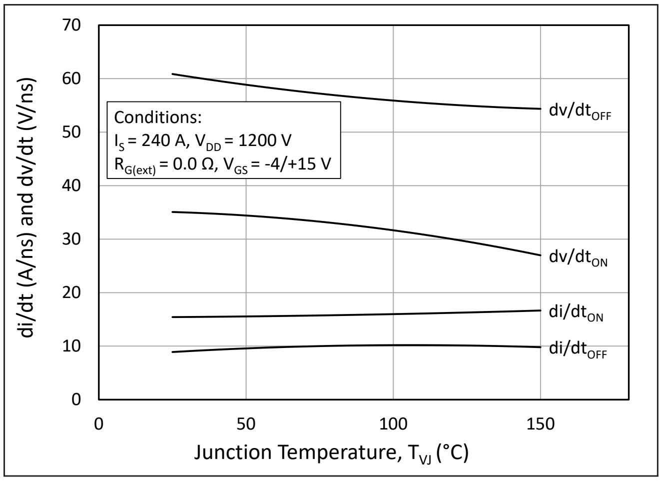


Figure 27. dv/dt and di/dt vs. Junction Temperature

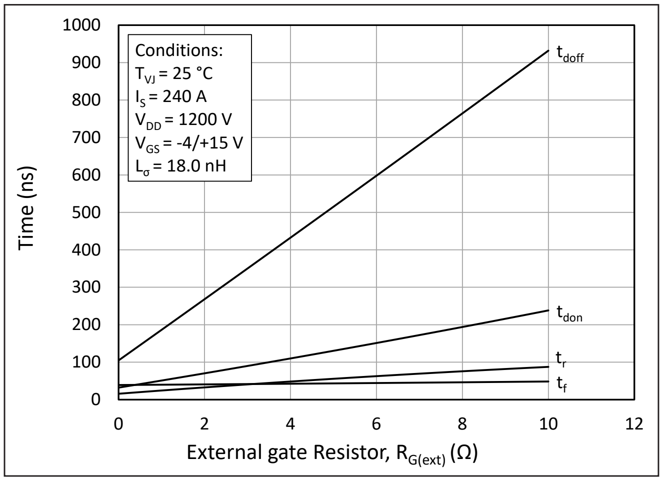


Figure 28. Timing vs. External Gate Resistance

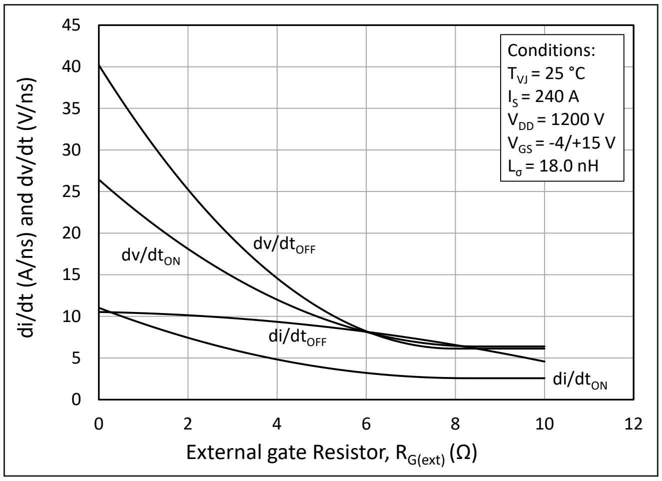


Figure 29. dv/dt and di/dt vs. External Gate Resistance

Definitions

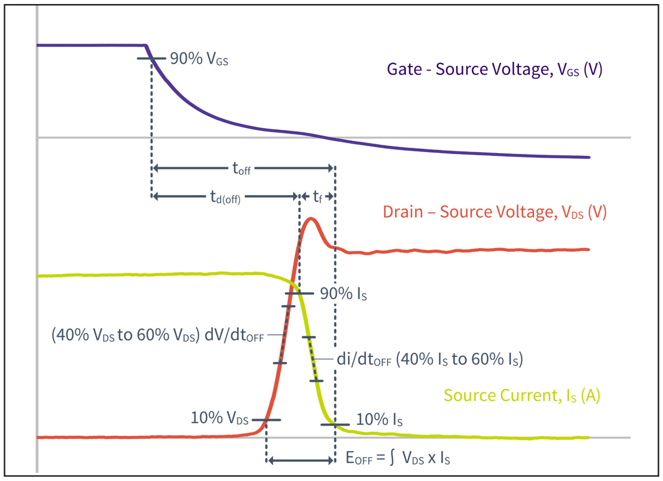


Figure 30. Turn-off Transient Definitions

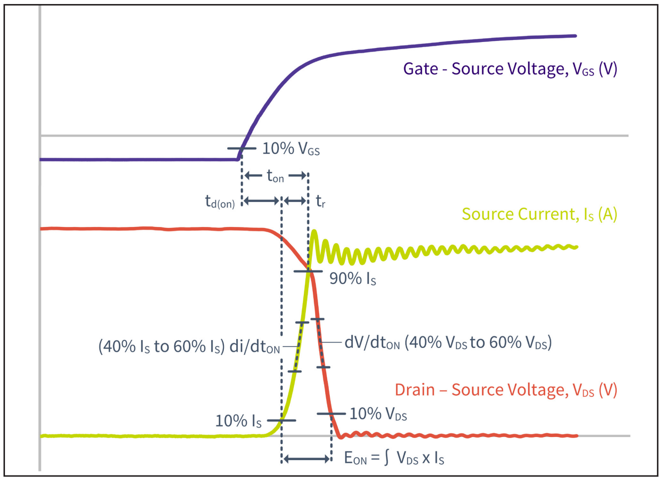


Figure 31. Turn-on Transient Definitions

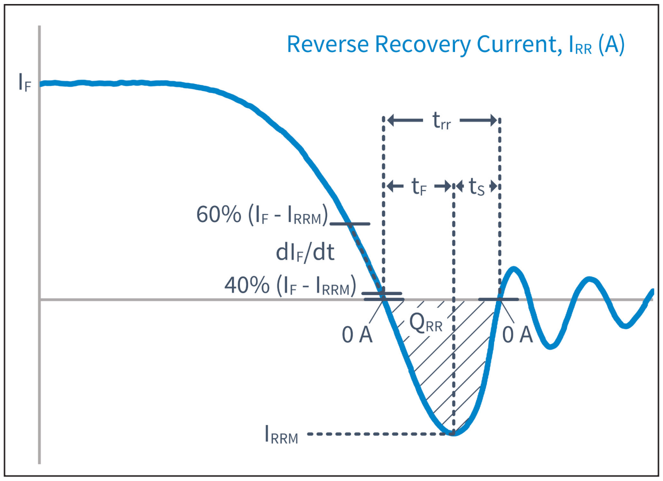


Figure 32. Reverse Recovery Definitions

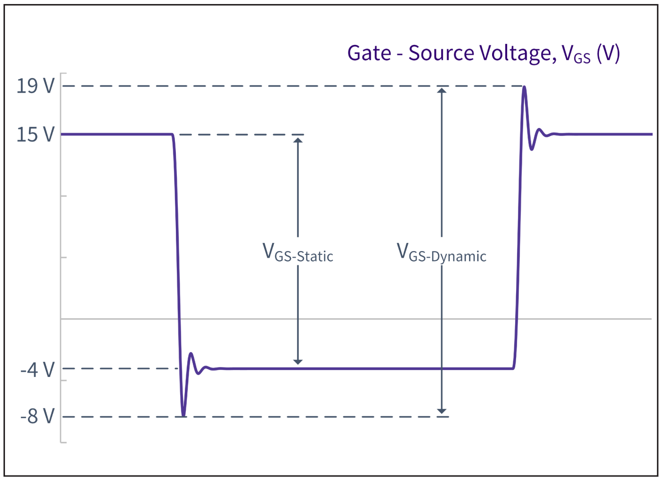
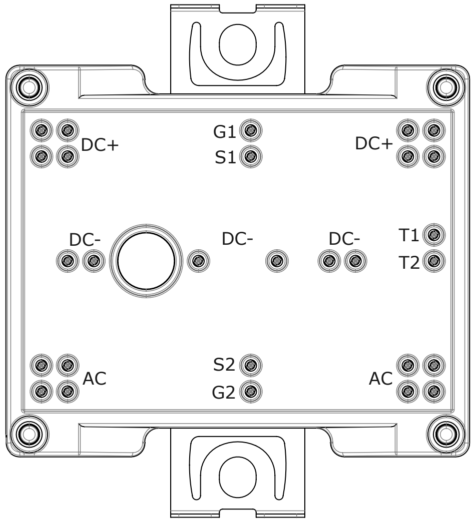
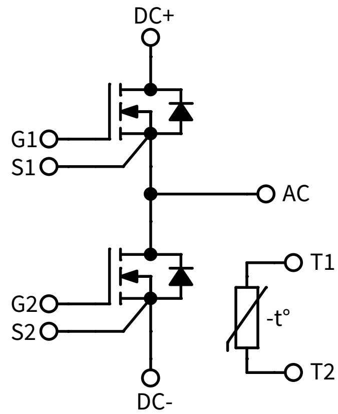


Figure 33. V_{GS} Transient Definitions

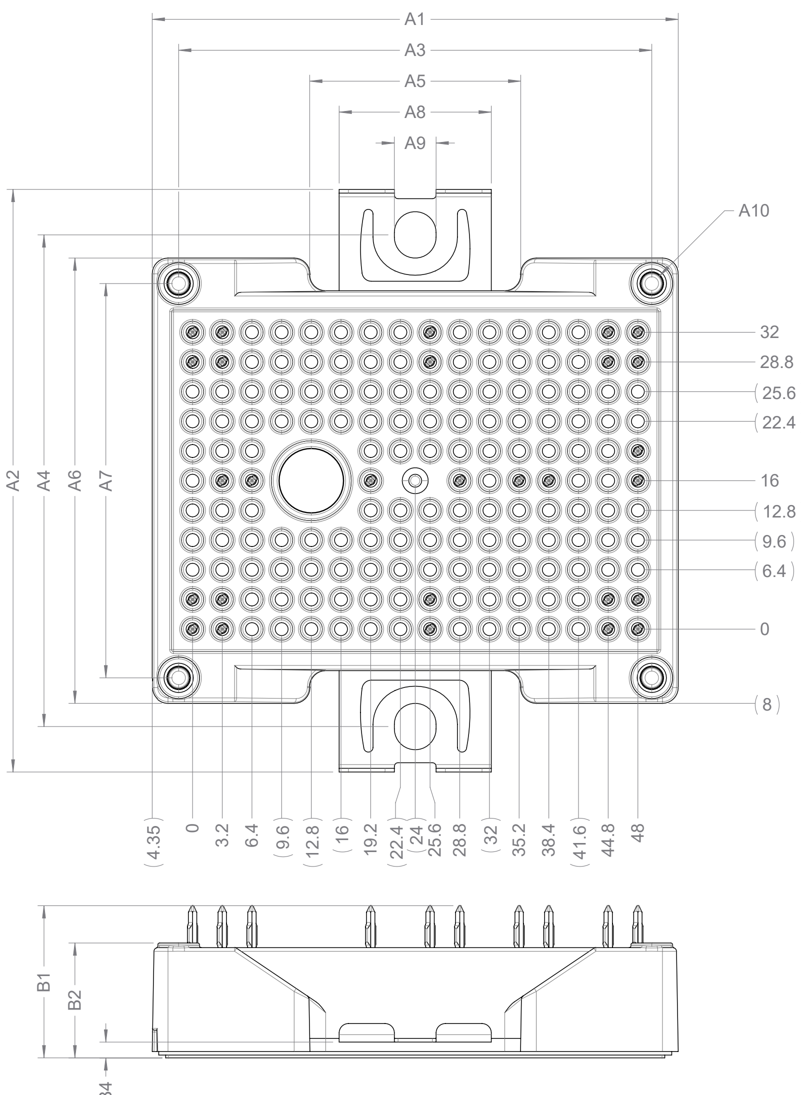
Note (7): A gate driver featuring the IXDD614SI gate driver IC was used to evaluate dynamic performance. The typical parasitic turn-on resistance of 0.4 Ω and the parasitic turn-off resistance of 0.3 Ω are not included in the RG(ext) values on this datasheet.

Schematic and Pin Out





Package Dimension (mm)



DIMENSION TABLE		
SYMBOL	DIMENSION	TOLERANCE
A1	56.7	±0.30
A2	62.8	±0.50
A3	51	±0.15
A4	(53)	REF.
A5	22.7	±0.30
A6	48	±0.30
A7	42.5	±0.15
A8	16.4	±0.20
A9	4.5	±0.10
A10	∅2.3 ↧8.5	∅: $^{+0}_{-0.10}$ ↧: ±0.30
B1	16.4	±0.50
B2	12.33	±0.35
B4	1.8	±0.20
ALL PIN LOCATIONS		±0.40

Product Ordering Code

Part Number	Description
CAB5R0A23GM4	Without Pre-Applied Phase Change Thermal Interface Material
CAB5R0A23GM4T	With Pre-Applied Phase Change Thermal Interface Material

Supporting Links & Tools

Simulation Tools & Support

- All LTSpice Models
- All PLECS Models
- SpeedFit 2.0 Design Simulator™
- Technical Support Forum

Compatible Evaluation Hardware

- EVAL-ADUM4146WHB1Z: Analog Devices® Gate Driver Board
- UCC21710QDWEVM-054: Texas Instruments® Gate Driver Board
- CGD1700HB2M-UNA: Wolfspeed Gate Driver Board
- CGD12HB00D: Differential Transceiver Daughter Board Companion Tool for Differential Gate Drivers

Application Notes

- PRD-02302: Wolfpack Mounting Instructions and PCB Requirements
- PRD-06379: Environmental Considerations for Power Electronics
- PRD-07845: Power Module Baseplate Capacitance and Electromagnetic Compatibility
- PRD-07933: Wolfspeed Power Module Thermal Interface Material Application User Guide
- PRD-07968: Wolfspeed WolfPACK™ Dynamic Performance
- PRD-08376: Thermal Characterization Methods and Applications
- PRD-08710: Measuring Stray Inductance in Power Electronics Systems

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This product has not been designed or tested for use in, and is not intended for use in, any application in which failure of the product would reasonably be expected to cause death, personal injury, or property damage. For purposes of (but without limiting) the foregoing, this product is not designed, intended, or authorized for use as a critical component in equipment implanted into the human body, life-support machines, cardiac defibrillators, and similar emergency medical equipment; air traffic control systems; or equipment used in the planning, construction, maintenance, or operation of nuclear facilities. Notwithstanding any application-specific information, guidance, assistance, or support that Wolfspeed may provide, the buyer of this product is solely responsible for determining the suitability of this product for the buyer’s purposes, including without limitation (1) selecting the appropriate Wolfspeed products for the buyer’s application, (2) designing, validating, and testing the buyer’s application, and (3) ensuring the buyer’s application meets applicable standards and any other legal, regulatory, and safety-related requirements.

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Wolfspeed representative or from the Product Documentation sections of www.wolfspeed.com.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact your Wolfspeed representative to ensure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

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