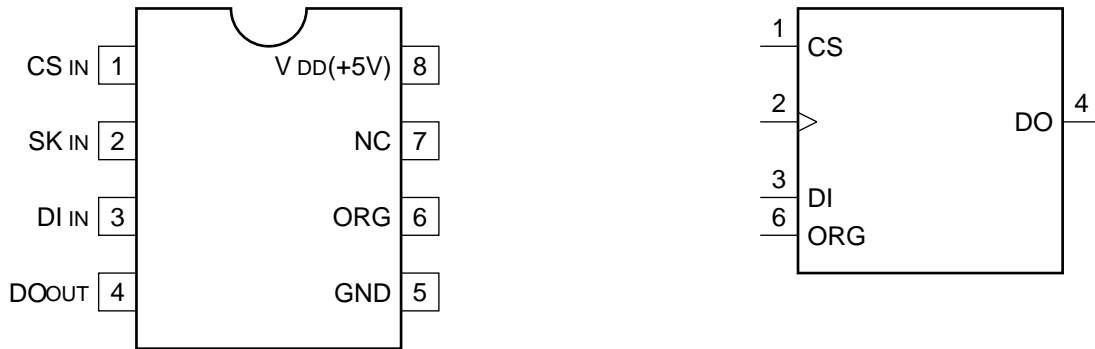

C-MOS 1024(64x16/128x8)BIT ELECTRICALLY ERASABLE PROM
- TOP VIEW -



INPUT

- CS ; CHIP SELECT
- DI ; SERIAL DATA INPUT
- ORG ; BIT CONFIGURATION SELECT
- SK ; SERIAL CLOCK

ORG	BIT CONFIGURATION
0	128x8
1	64x16
OPEN	64x16

OUTPUT

- DO ; SERIAL DATA OUTPUT

- 0 ; LOW LEVEL
- 1 ; HIGH LEVEL

