

Dual Interleaved Current Mode Controller

Description

The CD5032 dual current mode PWM controller contains all the features needed to control either two independent forward DC/DC converters or a single high current converter comprised of two interleaved power stages. The two controller channels operate 180° out of phase thereby reducing input ripple current.

The CD5032 operates over a wide input range up to 100 V and compound (bipolar + CMOS) gate drivers that provide a robust 2.5A peak sink current. The adjustable maximum PWM duty cycle reduce stress on the primary side MOSFET switches. Additional features include programmable line undervoltage lockout, cycle-by-cycle current limit, hiccup mode fault operation with adjustable response time, PWM slope compensation, soft-start, and a 2MHz capable oscillator with synchronization capability.

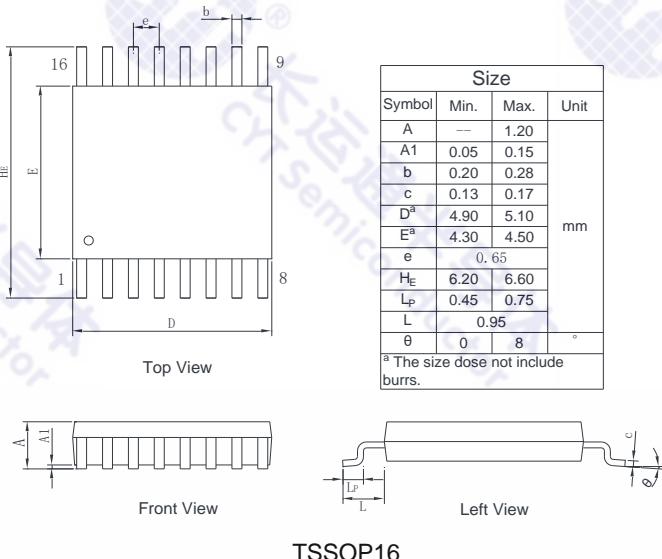
Absolute Maximum Ratings

VIN to GND	-0.3V to 105V	All other inputs to GND	-0.3V to 7V
VCC to GND	-0.3V to 16V	Junction Temperature	150°C
CS to GND	-0.3V to 1.25V	Storage Temperature Range	-65°C to 150°C
RT /SYNC, RES, DCL to GND	-0.3V to 5.5V		

Recommended Operating Conditions

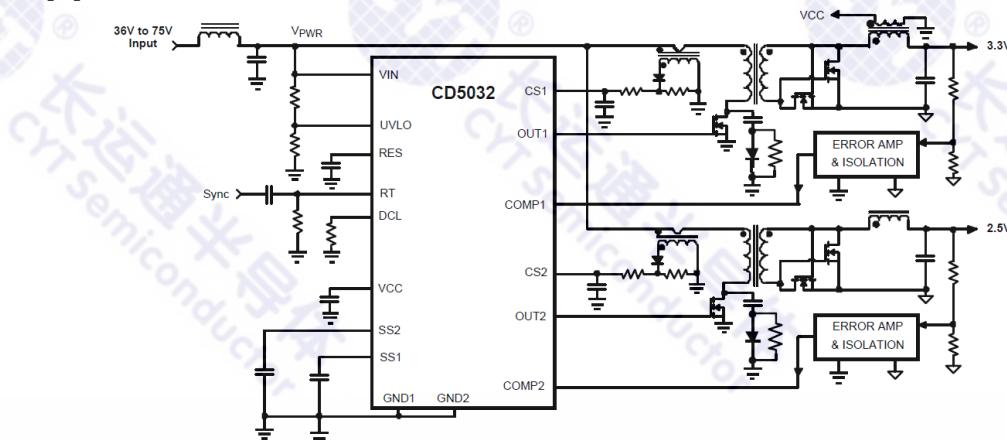
VIN Voltage	13V to 100V	Operating Junction Temperature(T_J)	-40°C to 125°C
VCC Voltage	8V to 15V		

Package Diagram



TSSOP16

Typical Application Circuit



CYT
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