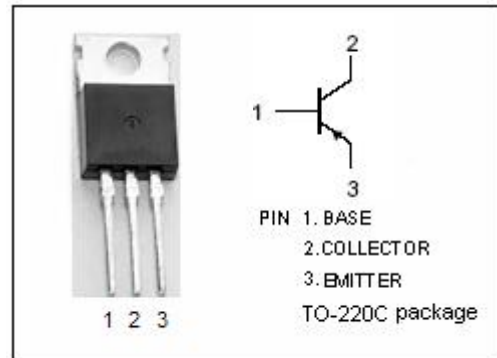
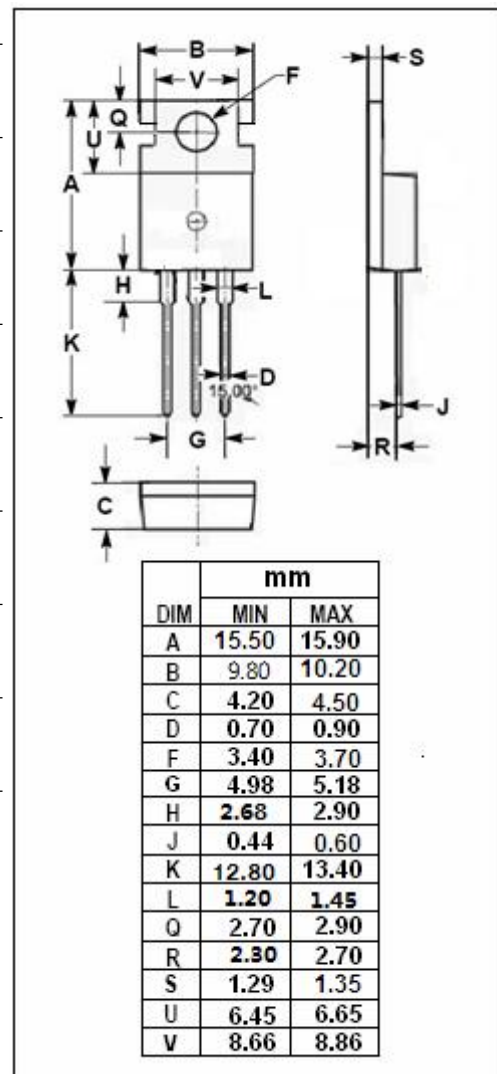


isc Silicon PNP Power Transistor
CD551B
DESCRIPTION

- Minimum Lot-to-Lot variations for robust device performance and reliable operation


ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-35	V
V _{CEO}	Collector-Emitter Voltage	-35	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current-Continuous	-3	A
P _C	Collector Power Dissipation @ T _C =25°C	1.8	W
T _J	Junction Temperature	125	°C
T _{stg}	Storage Temperature Range	-55~125	°C



isc Silicon PNP Power Transistor

CD551B

ELECTRICAL CHARACTERISTICS

T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-35			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = -1mA; I _E = 0	-35			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -0.1mA; I _C = 0	-5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -0.5A; I _B = -0.05A			-1.0	V
V _{BE(sat)}	Base-Emitter On Voltage	I _C = -0.5A; V _{CE} = -0.05V			-1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -30V; I _E = 0			-20	μ A
I _{CEO}	Emitter Cutoff Current	V _{CE} = -30V; I _C = 0			-100	μ A
h _{FE}	DC Current Gain	I _C = -0.5A; V _{CE} = -2V	55		270	

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