



Features

- Four unidirectional ESD protections
- RoHS compliant*
- Low capacitance
- Surge protection



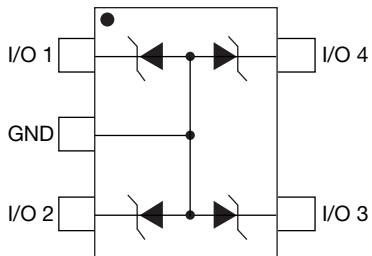
This model is obsolete and not recommended for new designs.

CDSOT353-T05 - TVS Diode Quad Array

General Information

The CDSOT353-T05 device provides ESD, EFT and Surge protection for high speed data ports meeting IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements. The Transient Voltage Suppressor array offers a Working Peak Reverse Voltage of 5.0 V and Minimum Breakdown Voltage of 6.0 V.

The SOT353 packaged device will mount directly onto the industry standard SOT353 footprint. Bourns® Chip Diodes are easy to handle with standard pick and place equipment and their flat configuration minimizes roll away.



Electrical & Thermal Characteristics (@ TA = 25 °C Unless Otherwise Noted)

Parameter	Symbol	CDSOT353-T05	Unit
Peak Pulse Power ($t_p = 8/20 \mu s$) (NOTE 1)	P _{PK}	25	W
Storage Temperature	T _{STG}	-55 to +150	°C
Operating Temperature	T _{OPR}	-55 to +125	°C
Minimum Breakdown Voltage @ 1 mA	V _{BR}	6.0	V
Working Peak Voltage	V _M	5.0	V
Maximum Clamping Voltage @ $I_p = 2 A$	V _P	13	V
Maximum Leakage Current @ 3 V	I _L	1	μA
Typical Capacitance 2.5 V @ 1 MHz	C	9.5	pF

Notes:

1. See Peak Pulse Power vs. Pulse Time.

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*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011.
Specifications are subject to change without notice.

The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.
Users should verify actual device performance in their specific applications.

Applications

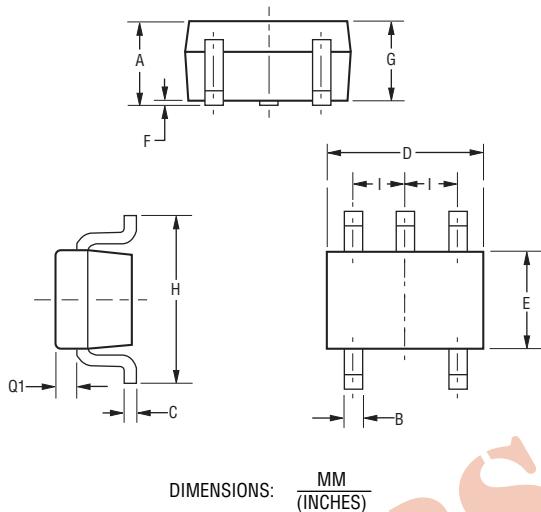
- High speed data ports
- Mobile phones & accessories
- Video/graphics cards
- Set top boxes
- Communication systems

CDSOT353-T05 - TVS Diode Quad Array

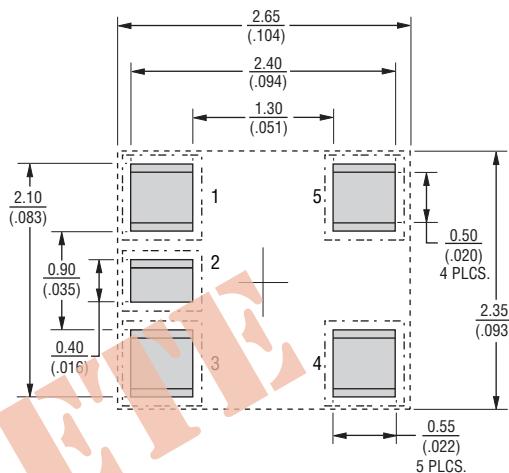
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Product Dimensions

This is an RoHS compliant molded SOT353 package with 100 % Matte Sn on the lead frame. It weighs approximately 15 mg and has a flammability rating of UL 94V-0.



Recommended Footprint



Typical Part Marking

CDSOT353-T05..... D5

Dimension	Min.	Max.
A	0.80 (0.031)	1.10 (0.043)
B	0.15 (0.006)	0.30 (0.012)
C	0.10 (0.004)	0.18 (0.007)
D	1.80 (0.071)	2.20 (0.087)
E	1.15 (0.045)	1.35 (0.053)
F	-	0.10 (0.004)
G	0.80 (0.031)	1.00 (0.039)
H	1.80 (0.071)	2.40 (0.094)
I	0.65 TYP. (0.026)	
Q1	0.10 (0.004)	0.40 (0.016)

How to Order

CD SOT353 - T 05

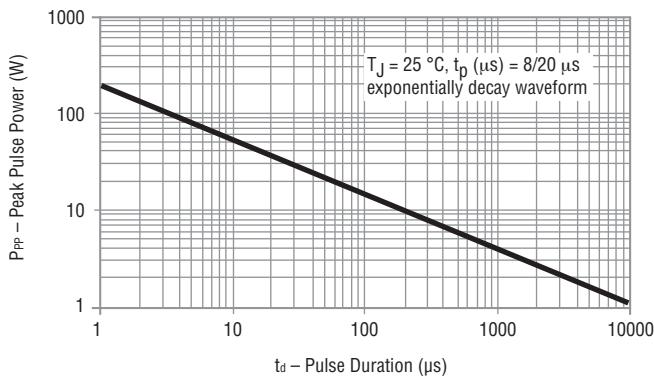
Common Diode _____
Chip Diode _____
Package _____
SOT353 = SOT353 Package
Model _____
T = Transient Voltage Suppressor
Working Peak Reverse Voltage _____
05 = 5 V_{RWM} (Volts)

CDSOT353-T05 - TVS Diode Quad Array

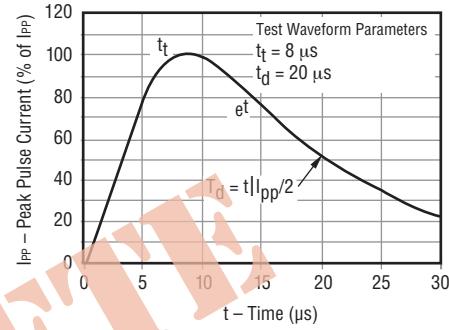
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Rating & Characteristic Curves

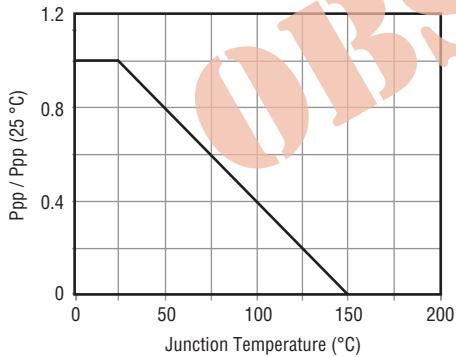
Non-Repetitive Peak Pulse Power vs. Pulse Time



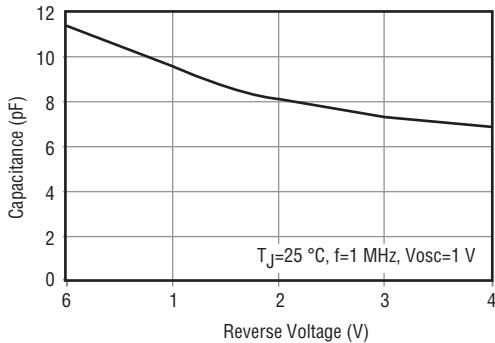
Pulse Waveform



Peak Pulse Power vs. Junction Temperature



Typical Junction Capacitance



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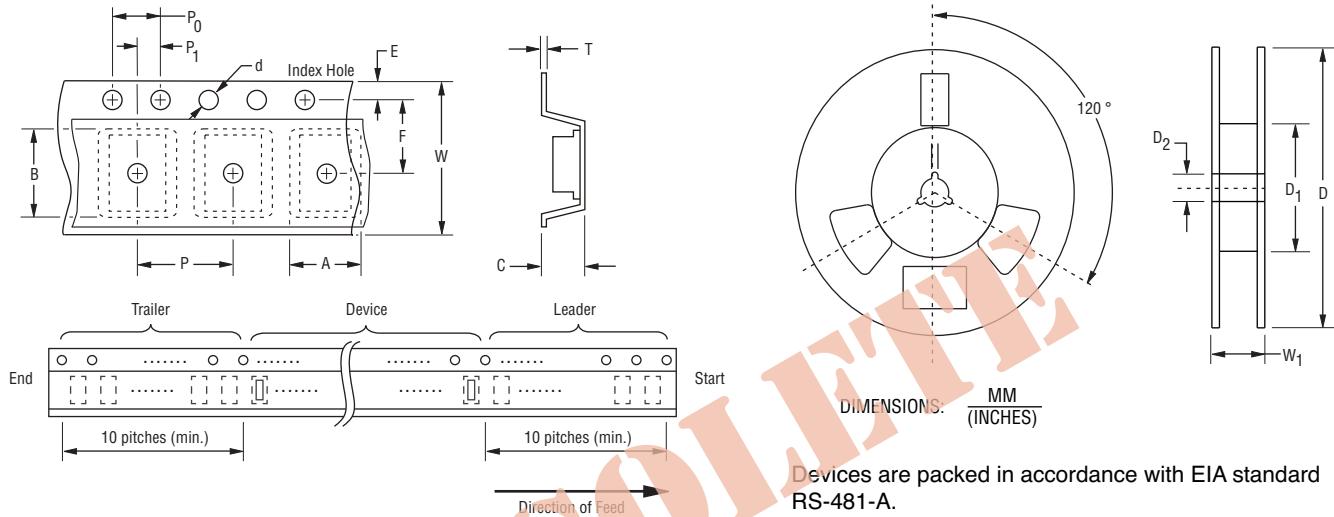
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CDSOT353-T05 - TVS Diode Quad Array

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Packaging Information

The product will be dispensed in tape and reel format (see diagram below).



Devices are packed in accordance with EIA standard RS-481-A.

Item	Symbol	CDSOT353
Carrier Width	A	2.25 ± 0.10 (0.088 ± 0.004)
Carrier Length	B	2.34 ± 0.10 (0.092 ± 0.004)
Carrier Depth	C	1.22 ± 0.10 (0.048 ± 0.004)
Sprocket Hole	d	1.55 ± 0.05 (0.061 ± 0.002)
Reel Outside Diameter	D	178 (7.008)
Reel Inner Diameter	D ₁	50.0 (1.969) MIN.
Feed Hole Diameter	D ₂	13.0 ± 0.20 (0.512 ± 0.008)
Sprocket Hole Position	E	1.75 ± 0.10 (0.069 ± 0.004)
Punch Hole Position	F	3.50 ± 0.05 (0.138 ± 0.002)
Punch Hole Pitch	P	4.00 ± 0.10 (0.157 ± 0.004)
Sprocket Hole Pitch	P ₀	4.00 ± 0.10 (0.157 ± 0.004)
Embossment Center	P ₁	2.00 ± 0.05 (0.079 ± 0.002)
Overall Tape Thickness	T	0.20 ± 0.10 (0.008 ± 0.004)
Tape Width	W	8.00 ± 0.20 (0.315 ± 0.008)
Reel Width	W ₁	14.4 (0.567) MAX.
Quantity per Reel	--	3000

REV. 09/15

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