

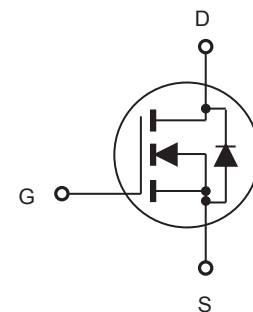
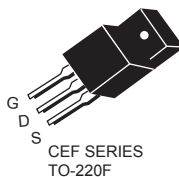
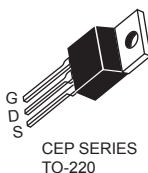
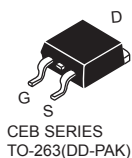


# CEP540L/CEB540L CEF540L

## N-Channel Enhancement Mode Field Effect Transistor

### FEATURES

- 100V, 36A,  $R_{DS(ON)} = 50m\Omega$  @  $V_{GS} = 10V$ .  
 $R_{DS(ON)} = 53m\Omega$  @  $V_{GS} = 5V$ .
- Super high dense cell design for extremely low  $R_{DS(ON)}$ .
- High power and current handing capability.
- Lead free product is acquired.
- TO-220 & TO-263 package.



### ABSOLUTE MAXIMUM RATINGS $T_C = 25^\circ\text{C}$ unless otherwise noted

| Parameter                                                                                 | Symbol         | Limit      | Units               |
|-------------------------------------------------------------------------------------------|----------------|------------|---------------------|
| Drain-Source Voltage                                                                      | $V_{DS}$       | 100        | V                   |
| Gate-Source Voltage                                                                       | $V_{GS}$       | $\pm 20$   | V                   |
| Drain Current-Continuous                                                                  | $I_D$          | 36         | A                   |
| Drain Current-Pulsed <sup>a</sup>                                                         | $I_{DM}$       | 120        | A                   |
| Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$<br>- Derate above $25^\circ\text{C}$ | $P_D$          | 140        | W                   |
|                                                                                           |                | 0.91       | W/ $^\circ\text{C}$ |
| Single Pulsed Avalanche Energy <sup>d</sup>                                               | $E_{AS}$       | 310        | mJ                  |
| Single Pulsed Avalanche Current <sup>d</sup>                                              | $I_{AS}$       | 18         | A                   |
| Operating and Store Temperature Range                                                     | $T_J, T_{stg}$ | -55 to 175 | $^\circ\text{C}$    |

### Thermal Characteristics

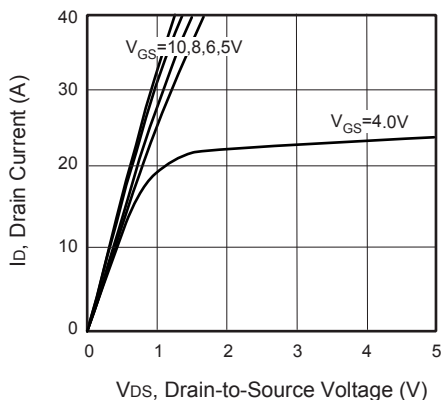
| Parameter                               | Symbol          | Limit | Units              |
|-----------------------------------------|-----------------|-------|--------------------|
| Thermal Resistance, Junction-to-Case    | $R_{\theta JC}$ | 1.1   | $^\circ\text{C/W}$ |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 62.5  | $^\circ\text{C/W}$ |



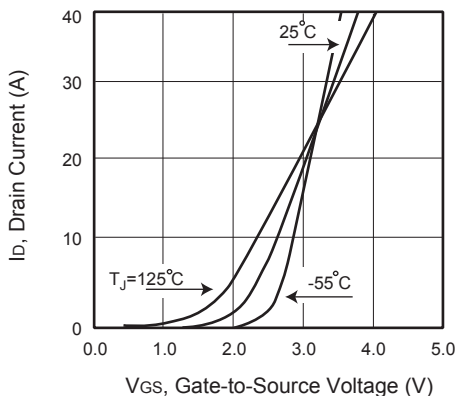
# CEP540L/CEB540L CEF540L

## Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

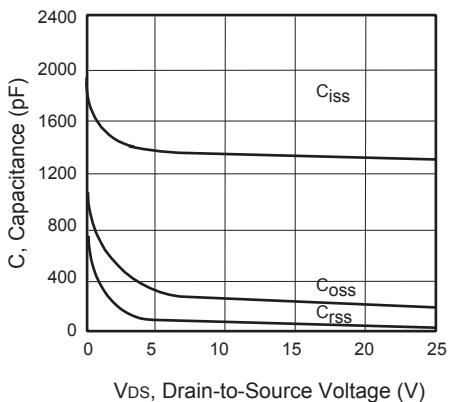
| Parameter                                                                                                                                                                                                                                                                                                                                                                                          | Symbol       | Test Condition                                                    | Min | Typ  | Max  | Units      |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------|-------------------------------------------------------------------|-----|------|------|------------|
| Off Characteristics                                                                                                                                                                                                                                                                                                                                                                                |              |                                                                   |     |      |      |            |
| Drain-Source Breakdown Voltage                                                                                                                                                                                                                                                                                                                                                                     | $BV_{DSS}$   | $V_{GS} = 0V, I_D = 250\mu A$                                     | 100 |      |      | V          |
| Zero Gate Voltage Drain Current                                                                                                                                                                                                                                                                                                                                                                    | $I_{DSS}$    | $V_{DS} = 100V, V_{GS} = 0V$                                      |     |      | 25   | $\mu A$    |
| Gate Body Leakage Current, Forward                                                                                                                                                                                                                                                                                                                                                                 | $I_{GSSF}$   | $V_{GS} = 20V, V_{DS} = 0V$                                       |     |      | 100  | nA         |
| Gate Body Leakage Current, Reverse                                                                                                                                                                                                                                                                                                                                                                 | $I_{GSSR}$   | $V_{GS} = -20V, V_{DS} = 0V$                                      |     |      | -100 | nA         |
| On Characteristics <sup>b</sup>                                                                                                                                                                                                                                                                                                                                                                    |              |                                                                   |     |      |      |            |
| Gate Threshold Voltage                                                                                                                                                                                                                                                                                                                                                                             | $V_{GS(th)}$ | $V_{GS} = V_{DS}, I_D = 250\mu A$                                 | 1   |      | 3    | V          |
| Static Drain-Source                                                                                                                                                                                                                                                                                                                                                                                | $R_{DS(on)}$ | $V_{GS} = 10V, I_D = 18A$                                         |     | 40   | 50   | m $\Omega$ |
| On-Resistance                                                                                                                                                                                                                                                                                                                                                                                      |              | $V_{GS} = 5V, I_D = 15A$                                          |     | 43   | 53   | m $\Omega$ |
| Forward Transconductance                                                                                                                                                                                                                                                                                                                                                                           | $g_{FS}$     | $V_{DS} = 25V, I_D = 18A$                                         |     | 14   |      | S          |
| Dynamic Characteristics <sup>c</sup>                                                                                                                                                                                                                                                                                                                                                               |              |                                                                   |     |      |      |            |
| Input Capacitance                                                                                                                                                                                                                                                                                                                                                                                  | $C_{iss}$    | $V_{DS} = 25V, V_{GS} = 0V,$<br>$f = 1.0\text{ MHz}$              |     | 1295 |      | pF         |
| Output Capacitance                                                                                                                                                                                                                                                                                                                                                                                 | $C_{oss}$    |                                                                   |     | 199  |      | pF         |
| Reverse Transfer Capacitance                                                                                                                                                                                                                                                                                                                                                                       | $C_{rss}$    |                                                                   |     | 40   |      | pF         |
| Switching Characteristics <sup>c</sup>                                                                                                                                                                                                                                                                                                                                                             |              |                                                                   |     |      |      |            |
| Turn-On Delay Time                                                                                                                                                                                                                                                                                                                                                                                 | $t_{d(on)}$  | $V_{DD} = 50V, I_D = 18A,$<br>$V_{GS} = 10V, R_{GEN} = 5.1\Omega$ |     | 13   | 26   | ns         |
| Turn-On Rise Time                                                                                                                                                                                                                                                                                                                                                                                  | $t_r$        |                                                                   |     | 3.1  | 7    | ns         |
| Turn-Off Delay Time                                                                                                                                                                                                                                                                                                                                                                                | $t_{d(off)}$ |                                                                   |     | 55   | 110  | ns         |
| Turn-Off Fall Time                                                                                                                                                                                                                                                                                                                                                                                 | $t_f$        |                                                                   |     | 5    | 10   | ns         |
| Total Gate Charge                                                                                                                                                                                                                                                                                                                                                                                  | $Q_g$        | $V_{DS} = 80V, I_D = 18A,$<br>$V_{GS} = 10V$                      |     | 40   | 80   | nC         |
| Gate-Source Charge                                                                                                                                                                                                                                                                                                                                                                                 | $Q_{gs}$     |                                                                   |     | 3.7  |      | nC         |
| Gate-Drain Charge                                                                                                                                                                                                                                                                                                                                                                                  | $Q_{gd}$     |                                                                   |     | 10   |      | nC         |
| Drain-Source Diode Characteristics and Maximun Ratings                                                                                                                                                                                                                                                                                                                                             |              |                                                                   |     |      |      |            |
| Drain-Source Diode Forward Current                                                                                                                                                                                                                                                                                                                                                                 | $I_S$        |                                                                   |     |      | 36   | A          |
| Drain-Source Diode Forward Voltage <sup>b</sup>                                                                                                                                                                                                                                                                                                                                                    | $V_{SD}$     | $V_{GS} = 0V, I_S = 18A$                                          |     |      | 1.3  | V          |
| Notes :<br><sup>a</sup> Repetitive Rating : Pulse width limited by maximum junction temperature.<br><sup>b</sup> Pulse Test : Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 2\%$ .<br><sup>c</sup> Guaranteed by design, not subject to production testing.<br><sup>d</sup> L = 1mH, I <sub>AS</sub> = 15A, V <sub>DD</sub> = 50V, R <sub>G</sub> = 25 $\Omega$ , Starting T <sub>J</sub> = 25 °C |              |                                                                   |     |      |      |            |



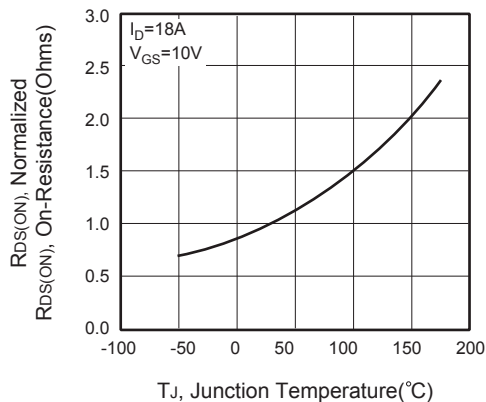
**Figure 1. Output Characteristics**



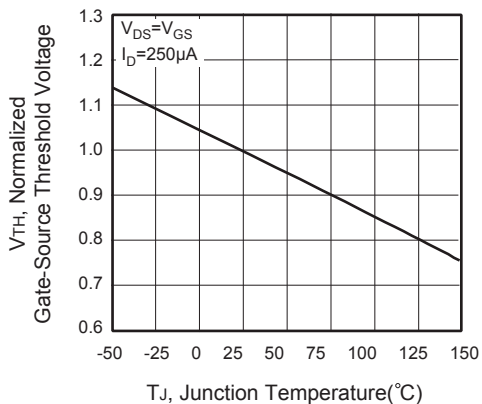
**Figure 2. Transfer Characteristics**



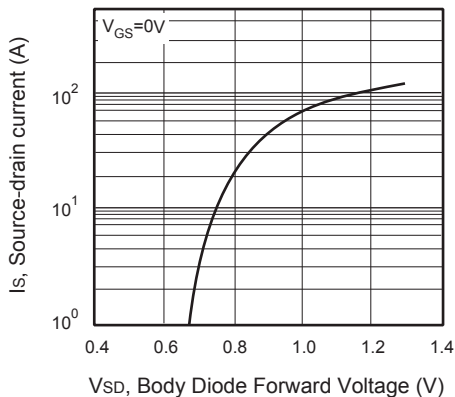
**Figure 3. Capacitance**



**Figure 4. On-Resistance Variation with Temperature**



**Figure 5. Gate Threshold Variation with Temperature**



**Figure 6. Body Diode Forward Voltage Variation with Source Current**

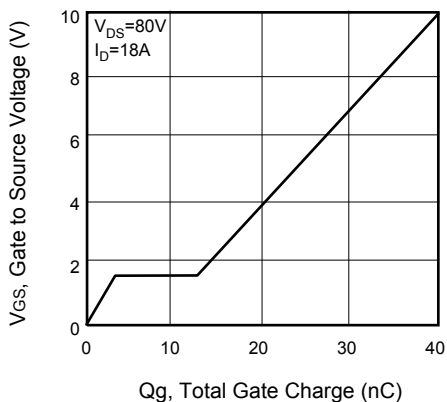


Figure 7. Gate Charge

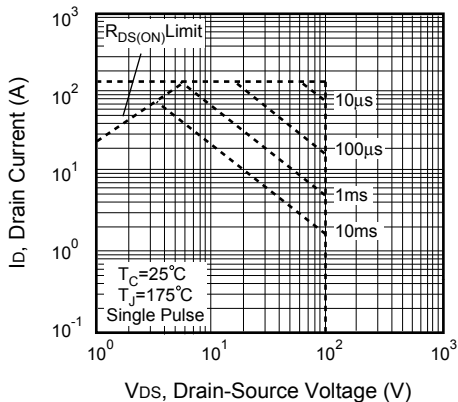


Figure 8. Maximum Safe Operating Area

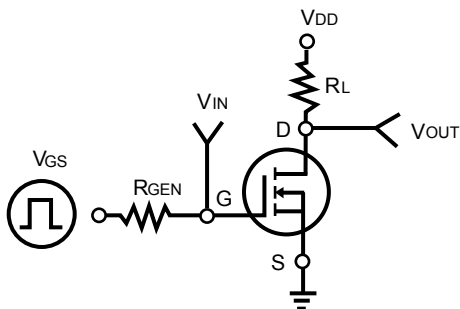


Figure 9. Switching Test Circuit

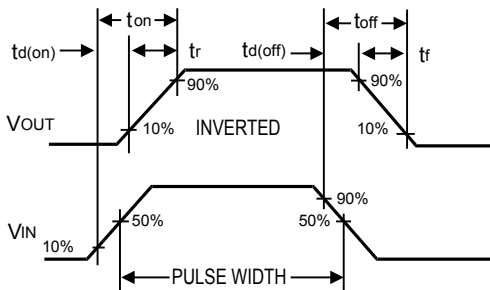


Figure 10. Switching Waveforms

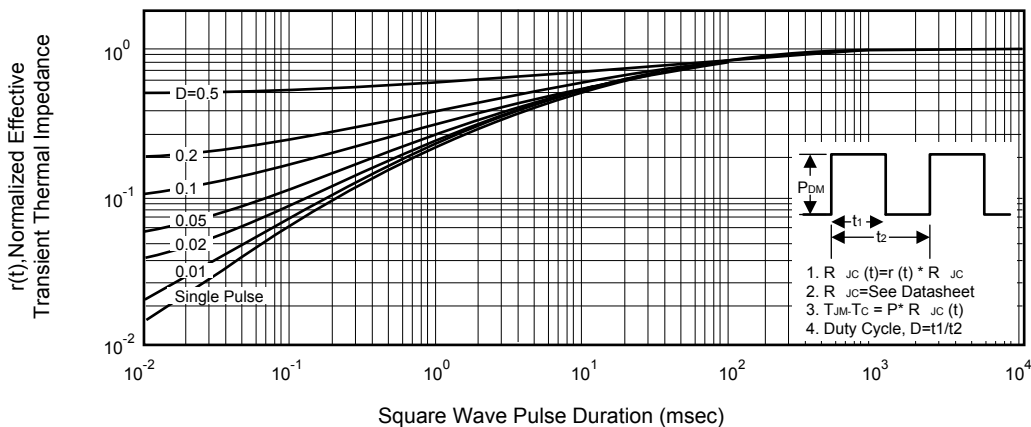


Figure 11. Normalized Thermal Transient Impedance Curve