

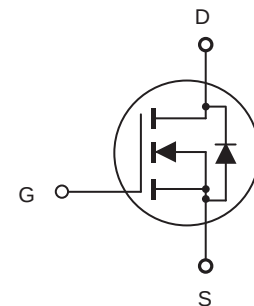


# CED62A3/CEU62A3

## N-Channel Enhancement Mode Field Effect Transistor

### FEATURES

- 30V, 55A,  $R_{DS(ON)} = 11.5m\Omega$  @  $V_{GS} = 10V$ .  
 $R_{DS(ON)} = 16m\Omega$  @  $V_{GS} = 4.5V$ .
- Super high dense cell design for extremely low  $R_{DS(ON)}$ .
- High power and current handling capability.
- Lead free product is acquired.
- TO-251 & TO-252 package.



### ABSOLUTE MAXIMUM RATINGS $T_C = 25^\circ\text{C}$ unless otherwise noted

| Parameter                                                                                 | Symbol         | Limit      | Units               |
|-------------------------------------------------------------------------------------------|----------------|------------|---------------------|
| Drain-Source Voltage                                                                      | $V_{DS}$       | 30         | V                   |
| Gate-Source Voltage                                                                       | $V_{GS}$       | $\pm 20$   | V                   |
| Drain Current-Continuous                                                                  | $I_D$          | 55         | A                   |
| Drain Current-Pulsed <sup>a</sup>                                                         | $I_{DM}$       | 165        | A                   |
| Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$<br>- Derate above $25^\circ\text{C}$ | $P_D$          | 60         | W                   |
|                                                                                           |                | 0.4        | W/ $^\circ\text{C}$ |
| Operating and Store Temperature Range                                                     | $T_J, T_{stg}$ | -55 to 175 | $^\circ\text{C}$    |

### Thermal Characteristics

| Parameter                               | Symbol          | Limit | Units              |
|-----------------------------------------|-----------------|-------|--------------------|
| Thermal Resistance, Junction-to-Case    | $R_{\theta JC}$ | 2.5   | $^\circ\text{C/W}$ |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 50    | $^\circ\text{C/W}$ |



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## Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

| Parameter                                                                                                                                                                                                                   | Symbol       | Test Condition                                                  | Min | Typ  | Max  | Units      |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------|-----------------------------------------------------------------|-----|------|------|------------|
| Off Characteristics                                                                                                                                                                                                         |              |                                                                 |     |      |      |            |
| Drain-Source Breakdown Voltage                                                                                                                                                                                              | $BV_{DSS}$   | $V_{GS} = 0V, I_D = 250\mu A$                                   | 30  |      |      | V          |
| Zero Gate Voltage Drain Current                                                                                                                                                                                             | $I_{DSS}$    | $V_{DS} = 30V, V_{GS} = 0V$                                     |     |      | 1    | $\mu A$    |
| Gate Body Leakage Current, Forward                                                                                                                                                                                          | $I_{GSSF}$   | $V_{GS} = 20V, V_{DS} = 0V$                                     |     |      | 100  | nA         |
| Gate Body Leakage Current, Reverse                                                                                                                                                                                          | $I_{GSSR}$   | $V_{GS} = -20V, V_{DS} = 0V$                                    |     |      | -100 | nA         |
| On Characteristics <sup>b</sup>                                                                                                                                                                                             |              |                                                                 |     |      |      |            |
| Gate Threshold Voltage                                                                                                                                                                                                      | $V_{GS(th)}$ | $V_{GS} = V_{DS}, I_D = 250\mu A$                               | 1   |      | 3    | V          |
| Static Drain-Source On-Resistance                                                                                                                                                                                           | $R_{DS(on)}$ | $V_{GS} = 10V, I_D = 14A$                                       |     | 9.5  | 11.5 | m $\Omega$ |
| On-Resistance                                                                                                                                                                                                               |              | $V_{GS} = 4.5V, I_D = 11A$                                      |     | 13   | 16   | m $\Omega$ |
| Forward Transconductance                                                                                                                                                                                                    | $g_{FS}$     | $V_{DS} = 5V, I_D = 12A$                                        |     | 26   |      | S          |
| Dynamic Characteristics <sup>c</sup>                                                                                                                                                                                        |              |                                                                 |     |      |      |            |
| Input Capacitance                                                                                                                                                                                                           | $C_{ISS}$    | $V_{DS} = 15V, V_{GS} = 0V,$<br>$f = 1.0\text{ MHz}$            |     | 1100 |      | pF         |
| Output Capacitance                                                                                                                                                                                                          | $C_{OSS}$    |                                                                 |     | 600  |      | pF         |
| Reverse Transfer Capacitance                                                                                                                                                                                                | $C_{RSS}$    |                                                                 |     | 180  |      | pF         |
| Switching Characteristics <sup>c</sup>                                                                                                                                                                                      |              |                                                                 |     |      |      |            |
| Turn-On Delay Time                                                                                                                                                                                                          | $t_{d(on)}$  | $V_{DD} = 15V, I_D = 14A,$<br>$V_{GS} = 10V, R_{GEN} = 6\Omega$ |     | 19   | 48   | ns         |
| Turn-On Rise Time                                                                                                                                                                                                           | $t_r$        |                                                                 |     | 36   | 72   | ns         |
| Turn-Off Delay Time                                                                                                                                                                                                         | $t_{d(off)}$ |                                                                 |     | 97   | 175  | ns         |
| Turn-On Fall Time                                                                                                                                                                                                           | $t_f$        |                                                                 |     | 68   | 135  | ns         |
| Total Gate Charge                                                                                                                                                                                                           | $Q_g$        | $V_{DS} = 10V, I_D = 14A,$<br>$V_{GS} = 10V$                    |     | 35   | 42   | nC         |
| Gate-Source Charge                                                                                                                                                                                                          | $Q_{gs}$     |                                                                 |     | 6    |      | nC         |
| Gate-Drain Charge                                                                                                                                                                                                           | $Q_{gd}$     |                                                                 |     | 11   |      | nC         |
| Drain-Source Diode Characteristics and Maximum Ratings                                                                                                                                                                      |              |                                                                 |     |      |      |            |
| Drain-Source Diode Forward Current                                                                                                                                                                                          | $I_S$        |                                                                 |     |      | 55   | A          |
| Drain-Source Diode Forward Voltage <sup>b</sup>                                                                                                                                                                             | $V_{SD}$     | $V_{GS} = 0V, I_S = 2.3A$                                       |     |      | 1.3  | V          |
| Notes :<br>a.Repetitive Rating : Pulse width limited by maximum junction temperature.<br>b.Pulse Test : Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 2\%$ .<br>c.Guaranteed by design, not subject to production testing. |              |                                                                 |     |      |      |            |



# CED62A3/CEU62A3

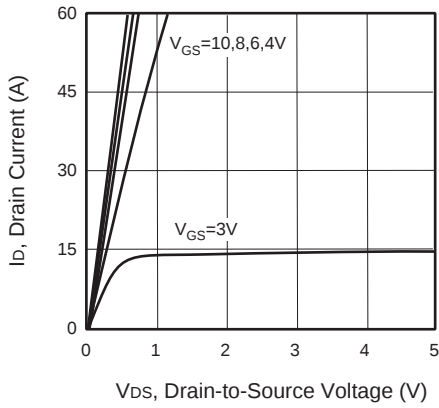


Figure 1. Output Characteristics

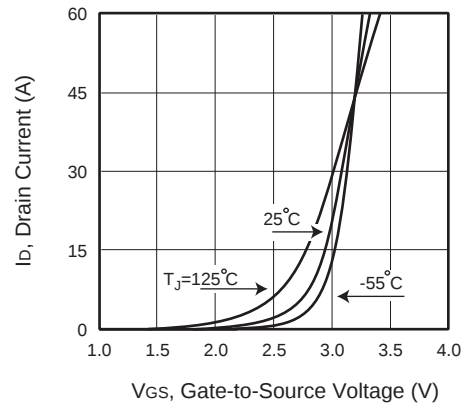


Figure 2. Transfer Characteristics

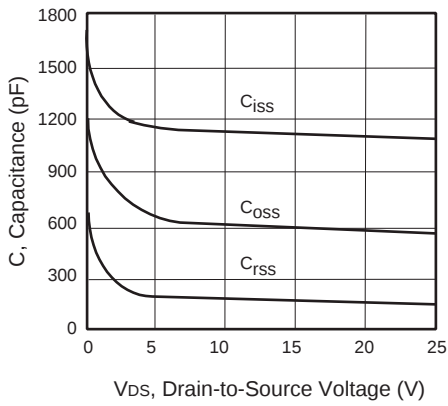


Figure 3. Capacitance

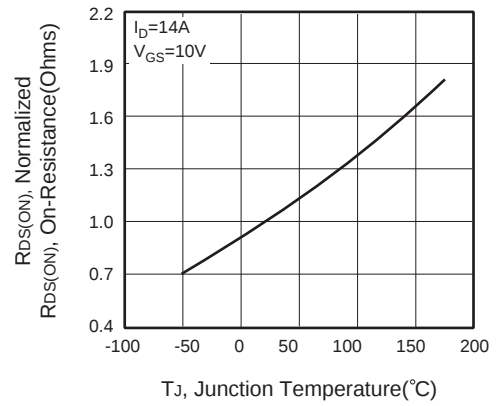


Figure 4. On-Resistance Variation with Temperature



Figure 5. Gate Threshold Variation with Temperature

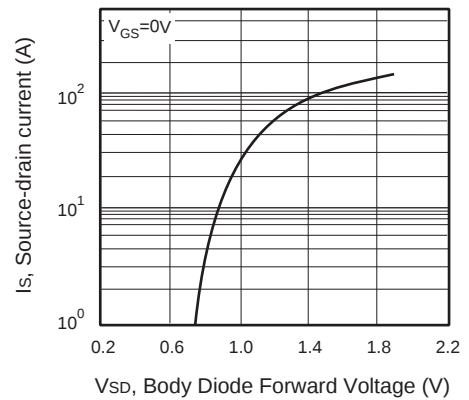


Figure 6. Body Diode Forward Voltage Variation with Source Current



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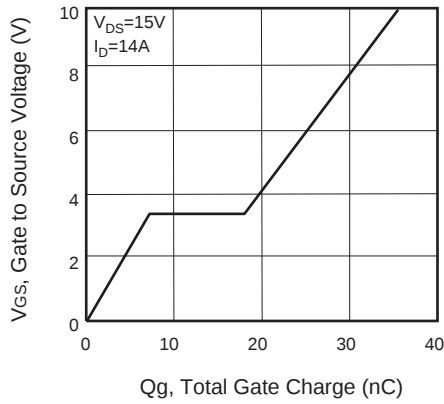


Figure 7. Gate Charge

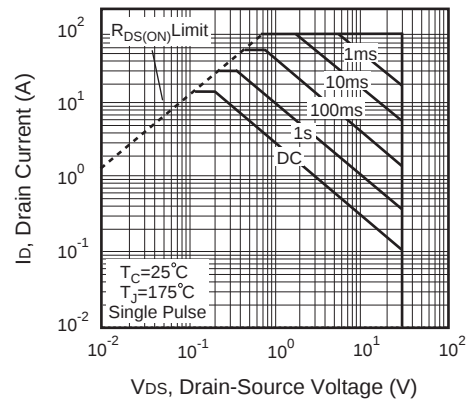


Figure 8. Maximum Safe Operating Area

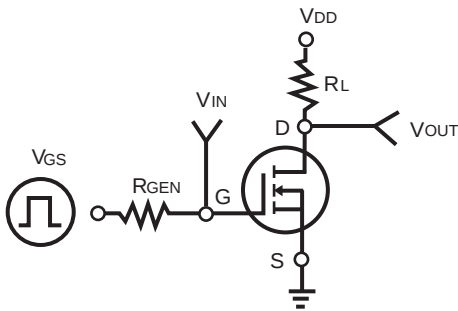


Figure 9. Switching Test Circuit

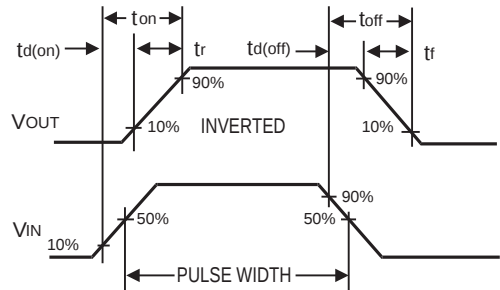


Figure 10. Switching Waveforms

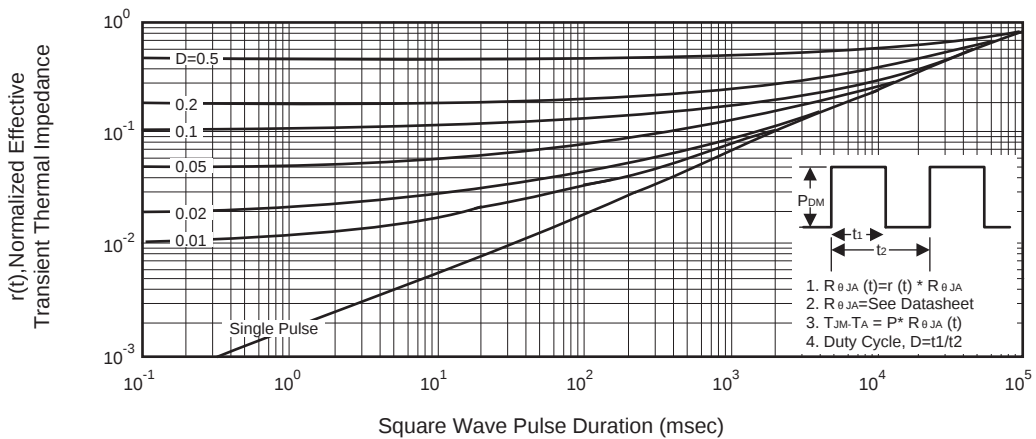


Figure 11. Normalized Thermal Transient Impedance Curve