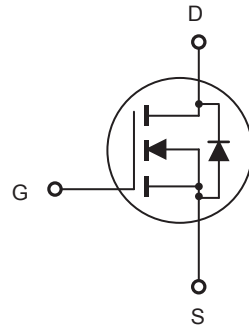
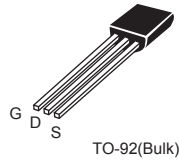
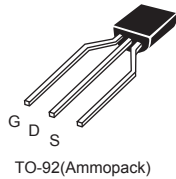


## N-Channel Enhancement Mode Field Effect Transistor

### FEATURES

- 600V, 1A,  $R_{DS(ON)} = 9.3 \Omega$  @  $V_{GS} = 10V$ .
- High dense cell design for extremely low  $R_{DS(ON)}$ .
- Rugged and reliable.
- Lead free product is acquired.
- TO-92(Bulk) & TO-92(Ammopack) package.



### ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ C$ unless otherwise noted

| Parameter                             | Symbol         | Limit      | Units      |
|---------------------------------------|----------------|------------|------------|
| Drain-Source Voltage                  | $V_{DS}$       | 600        | V          |
| Gate-Source Voltage                   | $V_{GS}$       | $\pm 30$   | V          |
| Drain Current-Continuous              | $I_D^a$        | 0.4        | A          |
| Drain Current-Pulsed <sup>b</sup>     | $I_{DM}$       | 1.6        | A          |
| Maximum Power Dissipation             | $P_D$          | 3.1        | W          |
| Operating and Store Temperature Range | $T_J, T_{stg}$ | -55 to 150 | $^\circ C$ |

### Thermal Characteristics

| Parameter                            | Symbol          | Limit | Units        |
|--------------------------------------|-----------------|-------|--------------|
| Thermal Resistance, Junction-to-Lead | $R_{\theta JL}$ | 40    | $^\circ C/W$ |

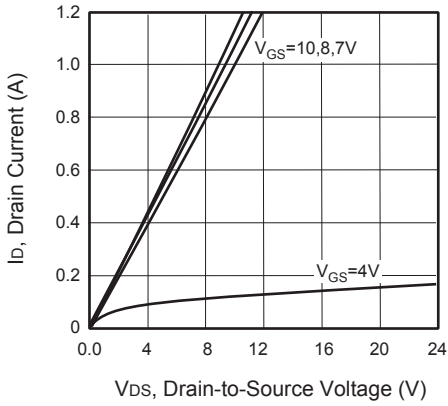


## Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

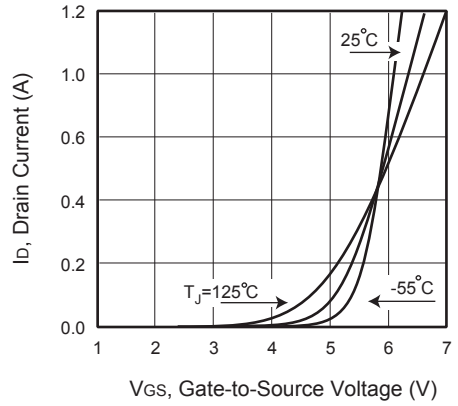
| Parameter   | Symbol       | Test Condition  | Min | Typ | Max  | Units    |
|---|--------------|---|-----|-----|------|----------|
| <b>Off Characteristics</b>  |              |   |     |     |      |          |
| Drain-Source Breakdown Voltage  | $BV_{DSS}$   | $V_{GS} = 0V, I_D = 250\mu A$                                 | 600 |     |      | V        |
| Zero Gate Voltage Drain Current   | $I_{DSS}$    | $V_{DS} = 600V, V_{GS} = 0V$                                  |     |     | 20   | $\mu A$  |
| Gate Body Leakage Current, Forward  | $I_{GSSF}$   | $V_{GS} = 30V, V_{DS} = 0V$                                   |     |     | 100  | nA       |
| Gate Body Leakage Current, Reverse  | $I_{GSSR}$   | $V_{GS} = -30V, V_{DS} = 0V$                                  |     |     | -100 | nA       |
| <b>On Characteristics</b>   |              |   |     |     |      |          |
| Gate Threshold Voltage  | $V_{GS(th)}$ | $V_{GS} = V_{DS}, I_D = 250\mu A$                             | 2   |     | 4    | V        |
| Static Drain-Source On-Resistance   | $R_{DS(on)}$ | $V_{GS} = 10V, I_D = 0.4A$                                    |     | 7.3 | 9.3  | $\Omega$ |
| <b>Dynamic Characteristics <sup>c</sup></b>   |              |   |     |     |      |          |
| Forward Transconductance  | $g_{FS}^b$   | $V_{DS} = 15V, I_D = 0.4A$                                    |     |     | 10   | S        |
| Input Capacitance   | $C_{iss}$    | $V_{DS} = 25V, V_{GS} = 0V, f = 1.0\text{ MHz}$               |     | 210 |      | pF       |
| Output Capacitance  | $C_{oss}$    |   |     | 55  |      | pF       |
| Reverse Transfer Capacitance  | $C_{rss}$    |   |     | 25  |      | pF       |
| <b>Switching Characteristics <sup>c</sup></b>   |              |   |     |     |      |          |
| Turn-On Delay Time  | $t_{d(on)}$  | $V_{DD} = 300V, I_D = 0.5A, V_{GS} = 10V, R_{GEN} = 10\Omega$ |     | 19  | 25   | ns       |
| Turn-On Rise Time   | $t_r$        |   |     | 10  | 13   | ns       |
| Turn-Off Delay Time   | $t_{d(off)}$ |   |     | 31  | 40   | ns       |
| Turn-Off Fall Time  | $t_f$        |   |     | 37  | 48   | ns       |
| Total Gate Charge   | $Q_g$        | $V_{DS} = 480V, I_D = 1A, V_{GS} = 10V$                       |     | 6   | 12   | nC       |
| Gate-Source Charge  | $Q_{gs}$     |   |     | 0.7 |      | nC       |
| Gate-Drain Charge   | $Q_{gd}$     |   |     | 3.5 |      | nC       |
| <b>Drain-Source Diode Characteristics and Maximum Ratings</b>   |              |   |     |     |      |          |
| Drain-Source Diode Forward Current  | $I_S$        |   |     |     | 0.4  | A        |
| Drain-Source Diode Forward Voltage <sup>b</sup>   | $V_{SD}$     | $V_{GS} = 0V, I_S = 0.4A$                                     |     |     | 1.5  | V        |
| <b>Notes :</b> <input type="checkbox"/><br>a.Repetitive Rating : Pulse width limited by maximum junction temperature.<br>b.Pulse Test : Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 2\%$ . <input type="checkbox"/><br>c.Guaranteed by design, not subject to production testing. <input type="checkbox"/> |              |   |     |     |      |          |



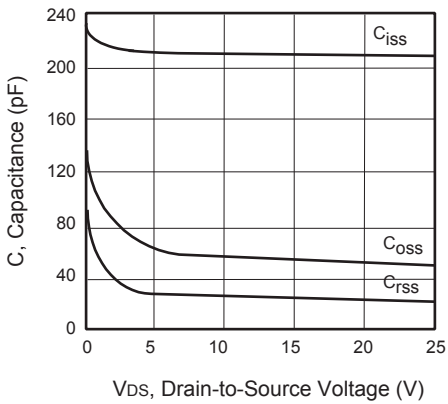
# CEK01N6G



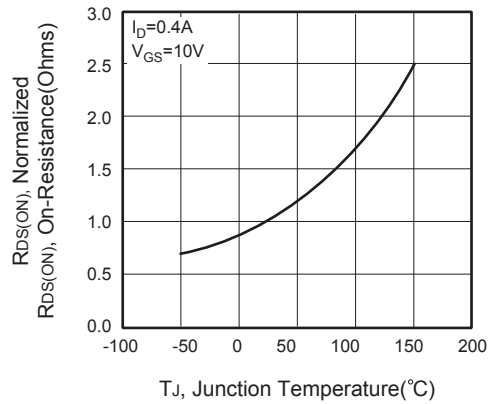
**Figure 1. Output Characteristics**



**Figure 2. Transfer Characteristics**



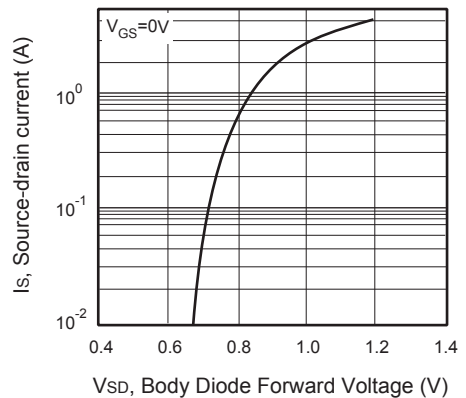
**Figure 3. Capacitance**



**Figure 4. On-Resistance Variation with Temperature**



**Figure 5. Gate Threshold Variation with Temperature**



**Figure 6. Body Diode Forward Voltage Variation with Source Current**



# CEK01N6G

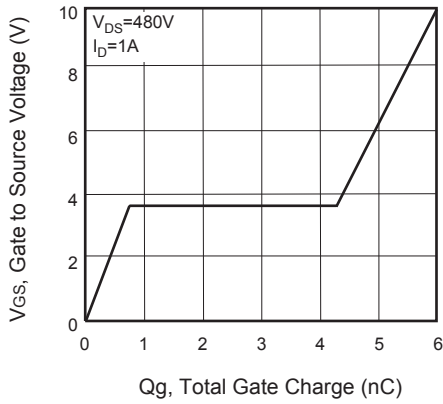


Figure 7. Gate Charge

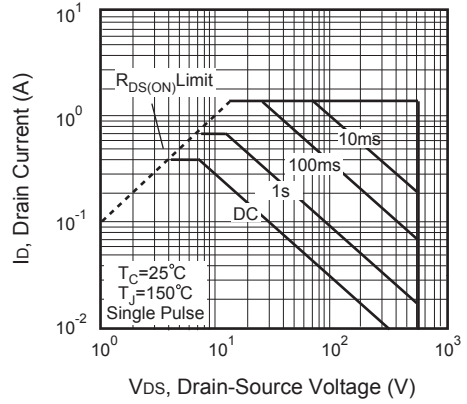


Figure 8. Maximum Safe Operating Area

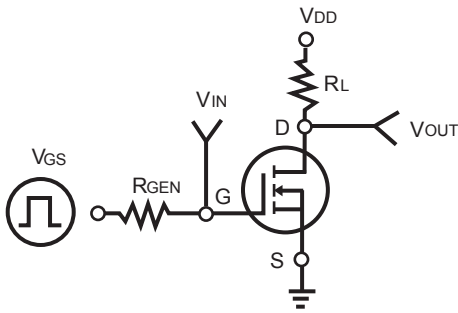


Figure 9. Switching Test Circuit

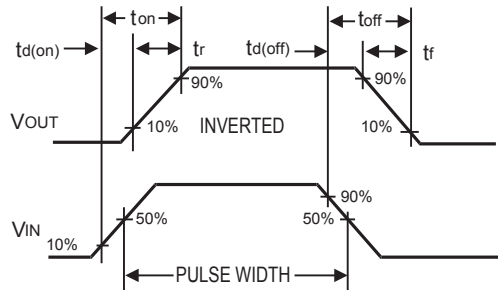


Figure 10. Switching Waveforms

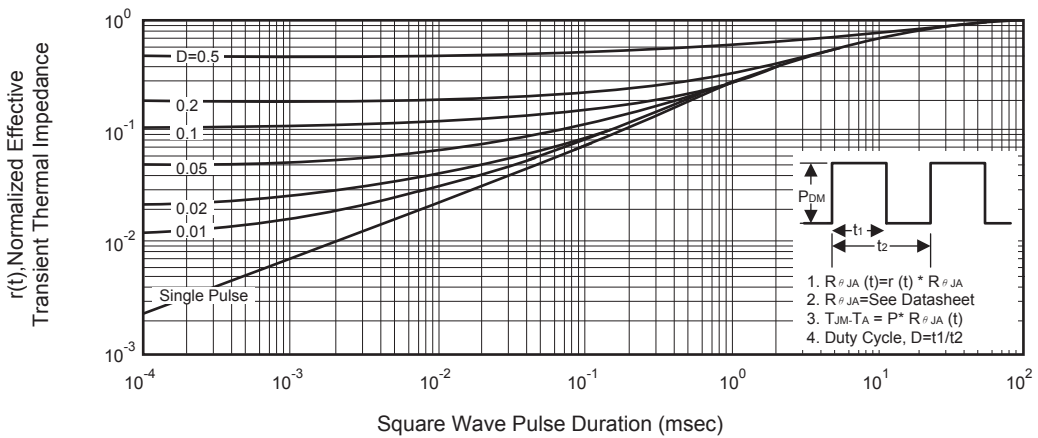


Figure 11. Normalized Thermal Transient Impedance Curve