

OVERVIEW

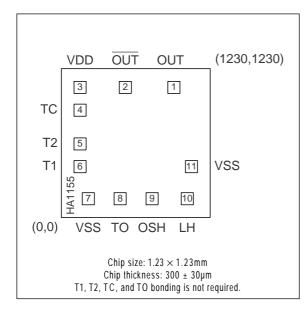
The CF1155 series are CMOS melody LSIs that, together with a battery and piezoelectric buzzer, operate in one of 3 melody modes determined by input level or bonding options. They also feature an oscillator stop function in non-play mode and a variable pull-down resistance function that responds to input levels in order to reduce power consumption, reduce cost, and extend battery life, making them ideal in a wide range of applications including greeting cards and toys.

FEATURES

- Requires few external components
- 1.2 to 3.6V wide operating voltage range
- Low power consumption
- 3 melody modes (bonding option)
- Oscillator stop function in non-play mode
- Power saving pull-down resistor built-in
- RC oscillator circuit
- Power-ON initialization function
- 2 V_{SS} pad connections
- Wide pitch dynamic range (G₃ to D₇)
- Chip form

PAD LAYOUT and COORDINATES

(Unit: µm)



| Number | Pad | Х | Υ | |
|--------|----------|---------|------|--|
| 1 | OUT | 930 | 1075 | |
| 2 | OUT | 529 | 1075 | |
| 3 | VDD | 155 | 1075 | |
| 4 | TC | 155 | 887 | |
| 5 | T2 | 155 | 608 | |
| 6 | T1 | 155 | 420 | |
| 7 | VSS | VSS 223 | | |
| 8 | 8 TO 486 | | 155 | |
| 9 | OSH | 750 | 155 | |
| 10 | LH | 1041 | 155 | |
| 11 | VSS | 1075 | 420 | |

ORDERING INFORMATION

| Device | Package |
|--------|-----------|
| CF1155 | Chip form |

PAD DESCRIPTION

| Name | Function |
|------------|--|
| OSH, LH | Melody mode control inputs. Built-in pull-down resistors mean LOW-level signals are obtained when inputs are open circuit. The resistance of the pull-down resistor varies with the applied voltage, as described in the Electrical Characteristics. |
| OUT, OUT | Piezoelectric speaker driver outputs. Both pins are LOW in non-play mode. OUT is LOW and OUT is HIGH during output for a musical rest note. Both pins are HIGH during the gap between musical notes. |
| VDD | Supply. The rear surface of the chip is V _{DD} level. |
| VSS | Ground |
| T1, T2, TC | Test inputs. Pull-down resistor built-in. |
| TO | Test output. The oscillator frequency (typ. 50kHz) is output for monitoring purposes. |

SPECIFICATIONS

Absolute Maximum Ratings

| Parameter | Symbol | Condition | Rating | Unit |
|-----------------------------|-------------------|-----------|--|------|
| Supply voltage range | $V_{DD} - V_{SS}$ | | -0.3 to 5.0 | V |
| Input voltage range | V _{IN} | | V _{SS} - 0.2 to V _{DD} + 0.2 | V |
| Operating temperature range | T _{opr} | | -20 to 80 | °C |
| Storage temperature range | T _{stg} | | -65 to 150 | °C |

Electrical Characteristics

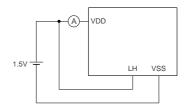
$$T_a = 25$$
°C, $V_{SS} = 0$ V, $V_{DD} = 1.5$ V

| Parameter | Symbol | Condition | | Unit | | | |
|-------------------------------------|------------------|-------------------------------|-----------------------|------|-----------------------|--------|--|
| Parameter | 3 9 111 10 1 | Colluition | min | typ | max | Uiiit | |
| Operating voltage | V _{DD} | | 1.2 | 1.5 | 3.6 | V | |
| Current consumption ¹ | I _{DD1} | Non-play mode | - | 0.01 | 0.3 | μA | |
| Current consumption ² | I _{DD2} | Melody modes: OUT, OUT open | - | 25 | 50 | μA | |
| LH, OSH LOW-level input voltage | V _{IL} | | - | - | V _{SS} + 0.2 | ٧ | |
| LH, OSH HIGH-level input voltage | V _{IH} | | V _{DD} - 0.2 | - | - | ٧ | |
| LH, OSH LOW-level input current | I _{IL} | V _{IL} = 0.4V | 0.7 | 1.5 | 3.0 | μA | |
| LH, OSH HIGH-level input current | I _{IH} | V _{IH} = 1.5V | 0.7 | 1.5 | 3.0 | μA | |
| OUT, OUT LOW-level output current | I _{OL} | V _{OL} = 0.75V | 2.0 | - | - | m A | |
| OUT, OUT HIGH-level output current | I _{OH} | V _{OH} = 0.75V | 2.0 | - | - | m A | |
| TO oscillator frequency | fosc | | 35 | 50 | 65 | kHz | |
| TO frequency stability (relative to | $\Delta f/f_1$ | V _{DD} = 1.2 to 2.0V | - | 1 | - | %/0.1V | |
| voltage) | $\Delta f/f_2$ | V _{DD} > 2.0V | - | 2 | - | %/1V | |
| TO oscillator start voltage | V _{DOB} | | - | - | 1.2 | ٧ | |
| TO oscillator stop voltage | V _{DOS} | | - | - | 1.2 | V | |

1. Measurement circuit

A VDD

2. Measurement circuit



FUNCTIONAL DESCRIPTION

Melody Modes

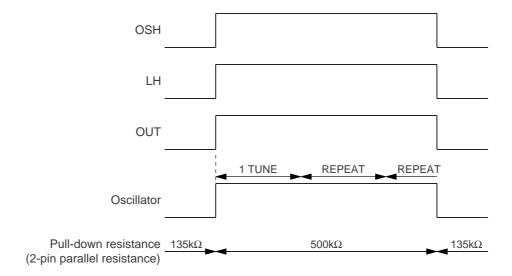
The CF1155 series melody mode is determined by the bonding options and levels on the mode control inputs OSH and LH. When either OSH or LH or both go HIGH (V_{DD}), the corresponding melody mode is selected as shown in the following table.

| OSH | LH | Melody mode |
|------|------|--------------|
| HIGH | HIGH | Level hold 1 |
| LOW | HIGH | Level hold 2 |
| HIGH | LOW | One-shot |
| LOW | LOW | Non-play |

Level hold 1

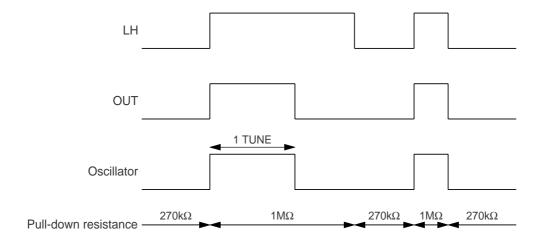
When both OSH and LH go HIGH, melody play starts and continues while both are held HIGH. When both inputs go open circuit or LOW, melody play stops, even if during mid melody.

Note that both OSH and LH inputs should be switched simultaneously using a single switch. If there is timing difference between the two inputs, then LH has priority.



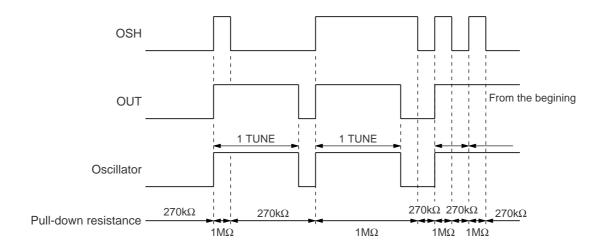
Level hold 2

When LH goes HIGH, a single melody play starts. Melody play continues until the single melody ends or LH goes open circuit or LOW, whichever occurs first.



One-shot

When OSH goes HIGH, a single melody play starts. Melody play continues until the melody ends, even if OSH goes open circuit or LOW mid melody. However, if the OSH input goes HIGH again during melody play, the melody play restarts from the beginning.



Power-save Function

As shown in the preceding timing diagrams, the oscillator stops during non-play mode and the pull-down resistance value changes in response to both HIGH-level and LOW-level inputs (power-save pull-down resistor) to reduce power consumption and extend battery life.

Non-play oscillator stop function

When melody play ends, regardless of the state of either LH or OSH, the internal oscillator stops and is placed in a standby state. In this state, the current consumption, including input pin pull-down resistor current (I_{IH} max), does not exceed 3.3 μ A.

Power-save pull-down resistor

The resistance of the OSH and LH input pull-down resistor changes in response to the input voltage. The pull-down resistance is $1M\Omega$ when the input is HIGH, and the pull-down resistance is $270k\Omega$ when the input is LOW.

Furthermore, if a light-dependent resistor (CdS) cell is employed as a switch (the pull-down resistance is maximum when the CdS resistance is minimum (light) and the pull-down resistance is minimum when the CdS resistance is maximum (dark)), the combined resistance can be increased, decreasing current consumption.

Musical Specifications

Maximum program steps

A maximum of 64 steps can be programmed into internal mask-programmable ROM. Each step represents either a note (sound pitch and length), a rest note, or a jump. The maximum number of jumps is 7.

Note length (including rests)

Eight rhythm values, from sixteenth note to half note, for notes and rests can be programmed, as shown in the following table.

| Typo | Code | | | | | | | | | |
|------|------|----------|----|----|--------|----|----------|---|--|--|
| Туре | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | | |
| Note | A |) | ♪. | J | J. B | J. | J | J | | |
| Rest | 7 | 7 | ٧٠ | \$ | \$ + 4 | ٤. | ٤ | | | |

Also, notes and rests longer than half notes and rests can be created using a tie to connect the notes. This requires 2 program steps in ROM.

Tempo

The tempo for the standard quarter note can be selected from the following 16 options.

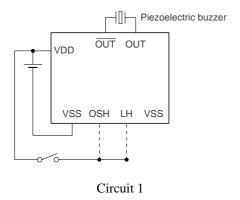
| Code | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | Α | В | С | D | E | F |
|------------|-----|-----|-----|-----|-----|-----|------|----|------|------|------|----|------|------|------|------|
| J = | 697 | 348 | 232 | 174 | 139 | 116 | 99.4 | 87 | 77.3 | 69.6 | 63.3 | 58 | 53.5 | 49.7 | 46.4 | 43.5 |

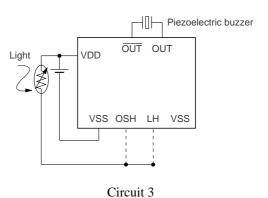
Pitch and scale

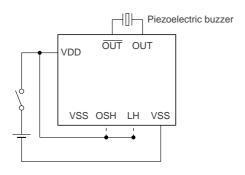
The CF1155 series devices support 3.5 octaves ranging from G_3 to D_7 , from which 15 notes can be selected to form a scale.

TYPICAL APPLICATION

The circuits below represent the standard connections for CF1155 series devices.







Circuit 2

Note 1: There are 2 V_{SS} pads, and either pad can be used.

Note 2: Circuit 1 is for one-shot mode, circuit 2 for level hold 1/2 modes, and circuit 3 for CdS connection.

The bonding options for the above circuits is shown in the following table.

| OSH | LH | Melody mode |
|-----|-----|--------------|
| Yes | Yes | Level hold 1 |
| | Yes | Level hold 2 |
| Yes | | One-shot |

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