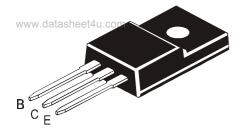


## PNP SILICON PLANAR POWER TRANSISTOR



CFA1046

TO-220FP Fully Isolated Plastic Package

## **Complementary CFC2026**

#### **ABSOLUTE MAXIMUM RATINGS**

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector Base Voltage	V <sub>CBO</sub>	60	V
Collector Emitter Voltage	V <sub>CEO</sub>	60	V
Emitter Base Voltage	V <sub>EBO</sub>	7.0	V
RMS Isolation Voltage (for 1sec, R.H.	** V <sub>ISOL</sub> (a)	3500	V <sub>RMS</sub>
<30%, T <sub>a</sub> = 25°C)	(b)	1500	V <sub>RMS</sub>
Collector Current	I <sub>C</sub>	3.0	A
Base Current	I <sub>B</sub>	0.5	A
Collector Power Dissipation @T <sub>a</sub> =25°C	P <sub>C</sub>	2.0	W
@T <sub>c</sub> =25°C		25	W
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature Range	T <sub>stg</sub>	- 55 to 150	°C

\*\* RMS Isolation Voltage: (a) 3500 V<sub>RMS</sub> with Package in Clip Mounting Position (b) 1500 V<sub>RMS</sub> with Package in Screw Mounting Position (for 1sec, R.H.<30%, T<sub>a</sub>=25°C; Pulse Test: Pulse Width <a href="mailto:signal-

#### ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector Cut Off Current	Сво	$V_{CB} = 60V, I_{E} = 0$			100	μΑ
Emitter Cut Off Current	I <sub>EBO</sub>	V <sub>EB</sub> = 7V, I <sub>C</sub> =0			100	μΑ
Collector Emitter Voltage	V <sub>CEO</sub>	I <sub>C</sub> = 50mA, I <sub>B</sub> =0	60			V
DC Current Gain	*h <sub>FE</sub>	$V_{CE} = 5V, I_{C} = 0.5A$	100		300	
	h <sub>FE</sub>	$V_{CE} = 5V, I_C = 3A$	20			
Collector Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_{\rm C} = 2A, I_{\rm B} = 0.2A$			1	V
Base Emitter On Voltage	V <sub>BE(on)</sub>	$V_{CE} = 5V, I_{C} = 0.5A$			1	V
Transition Frequency	f <sub>T</sub>	$V_{CE} = 5V, I_{C} = 0.5A$		30		MHz
Collector Output Capacitance	C <sub>ob</sub>	$V_{CB}$ = 10V, $I_{E}$ =0, f=1MHz		45		pF

Switching Time

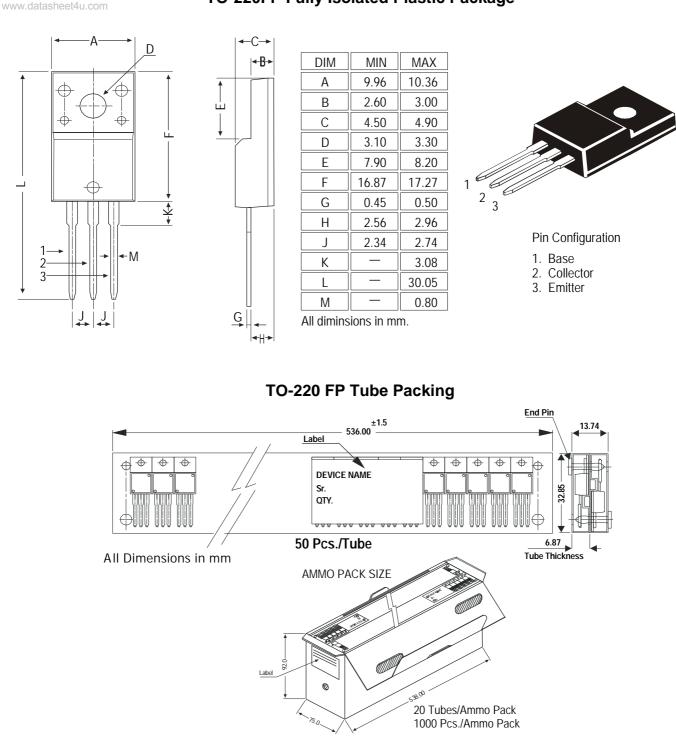
Turn On Time	t <sub>on</sub>	$I_{B1} = I_{B2} = 0.2A$	0.4	μs
Storage Time	t <sub>stg</sub>	$V_{CC} = 30V$	1.7	μs
Fall Time	t <sub>f</sub>	Duty Cycle <u>&lt;</u> 1%	0.5	μs

\*h<sub>FE</sub> Classification

Y : 100 - 200 GR : 1

GR : 150 - 300

## TO-220FP Fully Isolated Plastic Package



# **TO-220FP Fully Isolated Plastic Package**

## **Packing Detail**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-220FP	200 pcs/polybag 50 pcs/tube	396 gm/200 pcs 135 gm/50 pcs	3" x 7.5" x 7.5" 3.5" x 3.7" x 21.5"		17" x 15" x 13.5" 19" x 19" x 19"	16K 10K	36 kgs 28 kgs

### CFA1046

TO-220FP Fully Isolated Plastic Package

www.datasheet4u.com

### Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of **Continental Device India Limited** C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119 email@cdil.com www.cdisemi.com

CFA1046Rev130302E