

# **CGHV14800**

## 800 W, 1200 - 1400 MHz, 50 V, GaN HEMT for L-Band Radar Systems

Cree's CGHV14800 is a gallium nitride (GaN) high electron mobility transistor (HEMT) designed specifically with high efficiency, high gain and wide bandwidth capabilities, which makes the CGHV14800 ideal for 1.2 - 1.4 GHz pulsed L-Band radar amplifier applications, such as air traffic control (ATC) radar, weather radar, penetration radars, antimissile system radars, target tracking radars and long range survelliance radars. The GaN HEMT typically operates at 50 V, typically deliverying >65% drain efficiency. The package options are ceramic/metal flange package.



Package Type: 440117 PN: CGHV14800F

### Typical Performance Over 1.2-1.4 GHz (T<sub>c</sub> = 25°C) of Demonstration Amplifier

Parameter	1.2 GHz	1.25 GHz	1.3 GHz	1.35 GHz	1.4 GHz	Units
Output Power	900	900	870	870	920	W
Power Gain	14.5	14.5	14.0	14.0	14.0	dB
Drain Efficiency	68	67	67	63	62	%

#### Note:

Measured in the CGHV14800-AMP amplifier circuit, under 3  $\mu$ s pulse width, 3% duty cycle,  $P_{IN}$  = 45 dBm.

#### **Features**

- Reference design amplifier 1.2 1.4 GHz Operation
- 800 W Minimum Output Power
- 14 dB Power Gain
- 69% Typical Drain Efficiency
- <0.3 dB Pulsed Amplitude Droop</li>
- Internally input and output matched

Large Signal Models Available for ADS and MWO



### **Absolute Maximum Ratings (not simultaneous)**

Parameter	Symbol	Rating	Units	Conditions
Drain-Source Voltage	V <sub>DSS</sub>	125	Volts	25°C
Gate-to-Source Voltage	V <sub>GS</sub>	-10, +2	Volts	25°C
Storage Temperature	T <sub>STG</sub>	-65, +150	°C	
Operating Junction Temperature	T <sub>J</sub>	225	°C	
Maximum Forward Gate Current	I <sub>GMAX</sub>	132	mA	25°C
Maximum DC Current <sup>1</sup>	I <sub>DCMAX</sub>	24	А	25°C
Soldering Temperature <sup>2</sup>	$T_s$	245	°C	
Screw Torque	τ	40	in-oz	
CW Thermal Resistance, Junction to Case <sup>3</sup>	$R_{\scriptscriptstyle{\theta JC}}$	0.44	°C/W	P <sub>DISS</sub> = 398 W, 50°C
Pulsed Thermal Resistance, Junction to Case <sup>3</sup>	$R_{\scriptscriptstyle{\theta JC}}$	0.10	°C/W	P <sub>DISS</sub> = 664 W, 3 μsec, 3%, 85°C
Case Operating Temperature <sup>4</sup>	T <sub>c</sub>	-40, +100	°C	P <sub>DISS</sub> = 664 W, 100 µsec, 10%

#### Note:

#### **Electrical Characteristics**

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions	
DC Characteristics¹ (T <sub>c</sub> = 25°C)							
Gate Threshold Voltage	$V_{\rm GS(th)}$	-3.8	-3.0	-2.3	V <sub>DC</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 83.6 mA	
Gate Quiescent Voltage	$V_{GS(Q)}$	-	-2.7	-	$V_{DC}$	$V_{DS} = 50 \text{ V, I}_{D} = 500 \text{ mA}$	
Saturated Drain Current <sup>2</sup>	I <sub>DS</sub>	80.3	123.5	-	Α	$V_{DS} = 6.0 \text{ V, } V_{GS} = 2.0 \text{ V}$	
Drain-Source Breakdown Voltage	$V_{\rm BR}$	150	-	-	V <sub>DC</sub>	V <sub>GS</sub> = -8 V, I <sub>D</sub> = 83.6 mA	
RF Characteristics <sup>3</sup> (T <sub>c</sub> = 25°C, F <sub>0</sub> = 1.3 c	GHz unless oth	erwise noted)					
Output Power	P <sub>out</sub>	-	900	-	W	$V_{DD} = 50 \text{ V, } I_{DQ} = 800 \text{ mA, } P_{IN} = 45 \text{ dBm}$	
Drain Efficiency	D <sub>E</sub>	-	68	-	%	$V_{DD} = 50 \text{ V, } I_{DQ} = 800 \text{ mA, } P_{IN} = 45 \text{ dBm}$	
Power Gain	$G_{p}$	-	14.5	-	dB	$V_{DD} = 50 \text{ V, } I_{DQ} = 800 \text{ mA, } P_{IN} = 45 \text{ dBm}$	
Pulsed Amplitude Droop	D	-	-0.3	-	dB	V <sub>DD</sub> = 50 V, I <sub>DQ</sub> = 800 mA	
Output Mismatch Stress	VSWR	-	5:1	-	Ψ	No damage at all phase angles, $V_{DD}$ = 50 V, $I_{DQ}$ = 800 mA, $P_{IN}$ = 45 dBm Pulsed	
Dynamic Characteristics	Dynamic Characteristics						
Input Capacitance	$C_{GS}$	-	326	-	pF	$V_{DS} = 50 \text{ V, } V_{gs} = -8 \text{ V, f} = 1 \text{ MHz}$	
Output Capacitance	C <sub>DS</sub>	-	643	-	pF	$V_{DS} = 50 \text{ V, } V_{gs} = -8 \text{ V, f} = 1 \text{ MHz}$	
Feedback Capacitance	$C_{GD}$	-	3.9	-	pF	$V_{DS} = 50 \text{ V, } V_{gs} = -8 \text{ V, f} = 1 \text{ MHz}$	

#### Notes:

<sup>&</sup>lt;sup>1</sup> Current limit for long term, reliable operation

 $<sup>^2\,\</sup>text{Refer to the Application Note on soldering at}\,\,\underline{\text{http://www.cree.com/rf/document-library}}$ 

<sup>&</sup>lt;sup>3</sup> Measured for the CGHV14800F

<sup>&</sup>lt;sup>4</sup>See also, the Power Dissipation De-rating Curve on Page x

<sup>&</sup>lt;sup>1</sup> Measured on wafer prior to packaging.

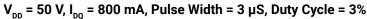
<sup>&</sup>lt;sup>2</sup> Scaled from PCM data.

 $<sup>^3</sup>$  Measured in CGHV14800-AMP. Pulse Width = 3  $\mu$ S, Duty Cycle = 3%.



### **Typical Pulsed Performance**

Figure 1. - Saturated Output Power and Drain Efficiency vs Frequency of the CGHV14800F in the CGHV14800F-AMP



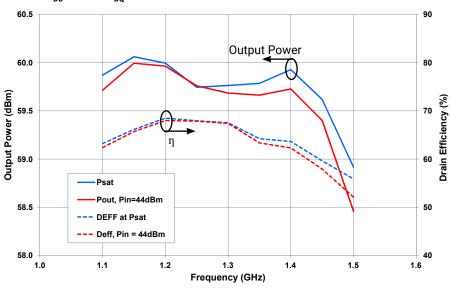
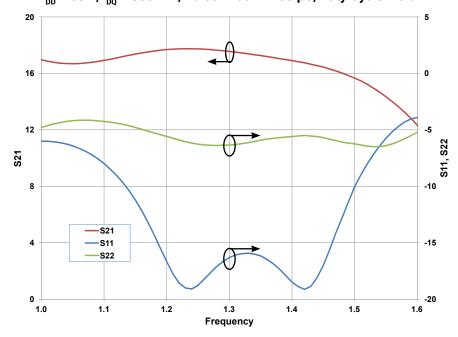


Figure 2. - Small Signal Gain and Return Losses vs Frequency of the CGHV14800F in the CGHV14800F-AMP  $V_{DD}$  = 50 V,  $I_{DO}$  = 800 mA, Pulse Width = 100  $\mu$ S, Duty Cycle = 5%





# **CGHV14800F-AMP Demonstration Amplifier Circuit Bill of Materials**

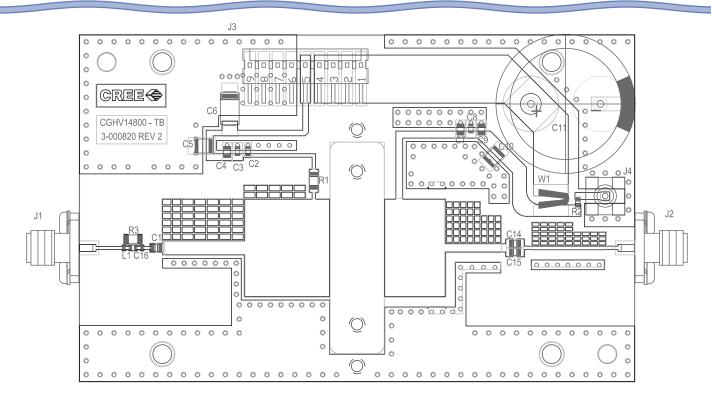
Designator	Description	Qty
R1	RES, 5.1,OHM, +/- 1%, 0.25W, 1206	1
R2	RES,1/16W,0603,1%,4.99K OHMS	1
R3	RES 5360HM +/- 1%, 0.25W,1206	1
L1	INDUCTOR,CHIP,6.8nH,0603 SMT	1
C1, C14, C15	CAP, 100 PF +/- 5%,, 250V, 0805, ATC 600F	3
C16	CAP, 2.0pF, +/-0.1pF, 0603, ATC	1
C2	CAP, 33pF, +/-5%, 0603, ATC	1
C3, C8	CAP, 470PF, 5%, 100V, 0603, X7R	2
C4, C9	CAP,33000PF, 0805,100V, X7R	2
C5, C10	CAP, 1.0UF, 100V, 10%, X7R, 1210	2
C6	CAP 10UF 16V TANTALUM	1
C7	CAP, 33 PF +/- 5%,, 250V, 0805, ATC 600F	1
C11	CAP, 3300 UF, +/-20%, 100V, ELECTROLYTIC	1
C14	CAP 10uF 16V TANTALUM	1
J1,J2	CONN, SMA, PANEL MOUNT JACK, FLANGE, 4-HOLE, BLUNT POST	2
J3	HEADER RT>PLZ .1CEN LK 9POS	1
J4	CONNECTOR; SMB, Straight, JACK,SMD	1
W1	CABLE ,18 AWG, 4.2	1
	PCB, TMM10i, 0.025" THK, CGHV14800 1.2-1.4GHZ	1
	2-56 SOC HD SCREW 1/4 SS	4
	#2 SPLIT LOCKWASHER SS	4
Q1	CGHV14800F	1

#### **CGHV14800F-AMP Demonstration Amplifier Circuit**

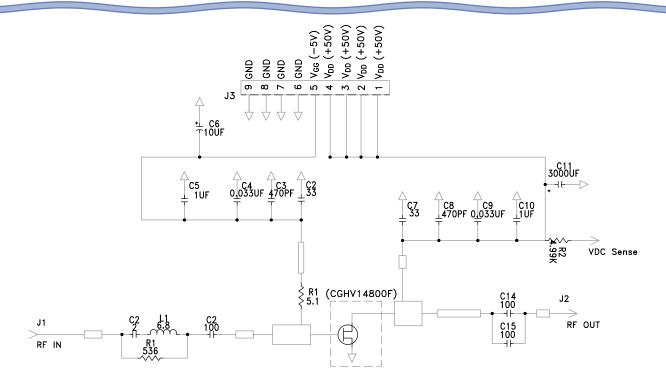




### **CGHV14800-AMP Demonstration Amplifier Circuit Outline**

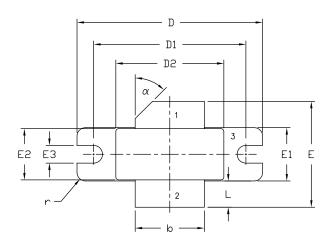


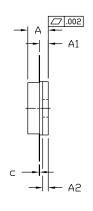
### **CGHV14800-AMP Demonstration Amplifier Circuit Schematic**





# Product Dimensions CGHV14800F (Package Type - 440117)





PIN 1. GATE

DRAIN
SOURCE

NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M - 1994.

2. CONTROLLING DIMENSION: INCH.

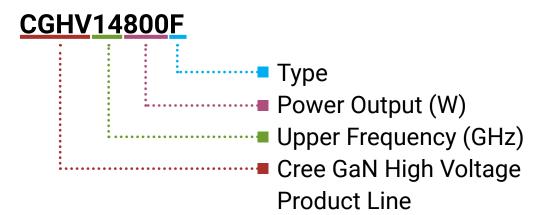
3. ADHESIVE FROM LID MAY EXTEND A MAXIMUM OF 0.020" BEYOND EDGE OF LID.

4. LID MAY BE MISALIGNED TO THE BODY OF PACKAGE BY A MAXIMUM OF 0.008' IN ANY DIRECTION.

	INCHES		MILLIMETERS		NOTES
DIM	MIN	MAX	MIN	MAX	
Α	0.138	0.158	3.51	4.01	
A1	0.057	0.067	1.45	1.70	
A2	0.035	0.045	0.89	1.14	
b	0.495	0.505	12.57	12.83	2×
С	0.003	0.006	0.08	0.15	
D	1.335	1.345	33.91	34.16	
D1	1.095	1.105	27.81	28.07	
D2	0.773	0.787	19.63	20.00	
Е	0.745	0.785	18.92	19.94	
E1	0.380	0.390	9.65	9.91	
E2	0.365	0.375	9.72	9.53	
E3	0.123	0.133	3.12	3.38	
L	0.170	0.210	4.32	5.33	2×
r	0.06 TYP		0.06 TYP		4x
α	45° REF		45* REF		



### **Part Number System**



Parameter	Value	Units	
Upper Frequency <sup>1</sup>	1.4	GHz	
Power Output	800	W	
Туре	F = Flanged P = Package	-	

Table 1.

**Note**<sup>1</sup>: Alpha characters used in frequency code indicate a value greater than 9.9 GHz. See Table 2 for value.

Character Code	Code Value	
A	0	
В	1	
С	2	
D	3	
E	4	
F	5	
G	6	
Н	7	
J	8	
K	9	
Examples:	1A = 10.0 GHz 2H = 27.0 GHz	

Table 2.



# **Product Ordering Information**

Order Number	Description	Unit of Measure	lmage
CGHV14800F	GaN HEMT	Each	CREE-O- CONTRICTORY
CGHV14800-TB	Test board without GaN HEMT	Each	
CGHV14800F-AMP	Test board with GaN HEMT installed	Each	



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