



CHENMKO ENTERPRISE CO.,LTD

SURFACE MOUNT

N-Channel Enhancement Mode Field Effect Transistor

VOLTAGE 30 Volts CURRENT 15.5 Ampere

CHM8809JPT

Lead free devices

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

FEATURE

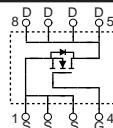
- * Small flat package. (SO-8)
- * High density cell design for extremely low R_{DSON}.
- * Rugged and reliable.
- * High saturation current capability.

CONSTRUCTION

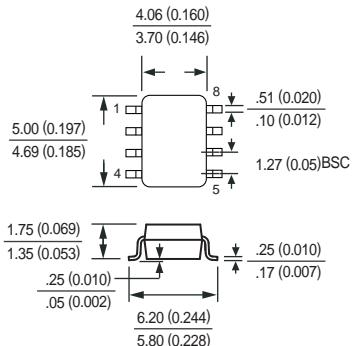
- * N-Channel Enhancement

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CIRCUIT



SO-8



Dimensions in millimeters

SO-8

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

| Symbol | Parameter | CHM8809JPT | Units |
|------------------|------------------------------------|------------|-------|
| V _{DSS} | Drain-Source Voltage | 30 | V |
| V _{GSS} | Gate-Source Voltage | ±16 | V |
| I _D | Maximum Drain Current - Continuous | 15.5 | A |
| | - Pulsed (Note 3) | 50 | |
| P _D | Maximum Power Dissipation | 2500 | mW |
| T _J | Operating Temperature Range | -55 to 150 | °C |
| T _{STG} | Storage Temperature Range | -55 to 150 | °C |

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

Thermal characteristics

| | | | |
|------------------|--|----|------|
| R _{θJA} | Thermal Resistance, Junction-to-Ambient (Note 1) | 50 | °C/W |
| 2006-01 | | | |

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RATING CHARACTERISTIC CURVES (CHM8809JPT)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Conditions | Min | Typ | Max | Units |
|--------|-----------|------------|-----|-----|-----|-------|
|--------|-----------|------------|-----|-----|-----|-------|

OFF CHARACTERISTICS

| | | | | | | |
|--------------------------|---------------------------------|--|----|--|------|---------------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$ | 30 | | | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{\text{DS}} = 24 \text{ V}, V_{\text{GS}} = 0 \text{ V}$ | | | 1 | μA |
| I_{GSSF} | Gate-Body Leakage | $V_{\text{GS}} = 16 \text{ V}, V_{\text{DS}} = 0 \text{ V}$ | | | +100 | nA |
| I_{GSSR} | Gate-Body Leakage | $V_{\text{GS}} = -16 \text{ V}, V_{\text{DS}} = 0 \text{ V}$ | | | -100 | nA |

ON CHARACTERISTICS (Note 2)

| | | | | | | |
|---------------------|-----------------------------------|--|---|-----|-----|------------------|
| $V_{\text{GS(th)}}$ | Gate Threshold Voltage | $V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$ | 1 | | 3 | V |
| $R_{\text{DS(ON)}}$ | Static Drain-Source On-Resistance | $V_{\text{GS}}=10 \text{ V}, I_D=15.5 \text{ A}$ | | 5.5 | 6.6 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=4.5 \text{ V}, I_D=15 \text{ A}$ | | 7.5 | 9.5 | |
| g_{FS} | Forward Transconductance | $V_{\text{DS}}=5 \text{ V}, I_D = 16 \text{ A}$ | | 34 | | S |

SWITCHING CHARACTERISTICS (Note 4)

| | | | | | | |
|------------------|--------------------|--|--|-----|-----|----|
| Q_g | Total Gate Charge | $V_{\text{DS}}=15 \text{ V}, I_D=16 \text{ A}$ $V_{\text{GS}}=5 \text{ V}$ | | 46 | 55 | nC |
| Q_{gs} | Gate-Source Charge | | | 15 | | |
| Q_{gd} | Gate-Drain Charge | | | 15 | | |
| t_{on} | Turn-On Time | $V_{\text{DD}}=15 \text{ V}$ $I_D = 1.0 \text{ A}, V_{\text{GS}} = 10 \text{ V}$ $R_{\text{GEN}}=6 \Omega$ | | 24 | 48 | nS |
| t_r | Rise Time | | | 14 | 28 | |
| t_{off} | Turn-Off Time | | | 100 | 200 | |
| t_f | Fall Time | | | 40 | 80 | |

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

| | | | | | | |
|-----------------|------------------------------------|---|--|--|-----|---|
| I_s | Drain-Source Diode Forward Current | (Note 1) | | | 2.1 | A |
| V_{SD} | Drain-Source Diode Forward Voltage | $I_s = 2.1 \text{ A}, V_{\text{GS}} = 0 \text{ V}$ (Note 2) | | | 1.3 | V |

RATING CHARACTERISTIC CURVES (CHM8809JPT)

Typical Electrical Characteristics

Figure 1. Output Characteristics

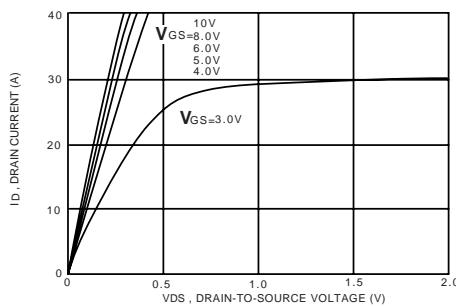


Figure 2. Transfer Characteristics

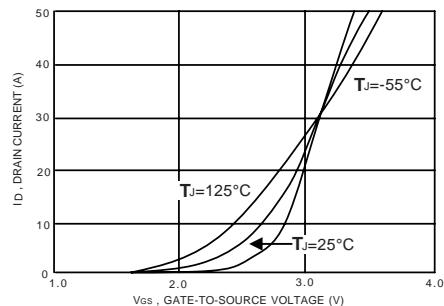


Figure 3. Gate Charge

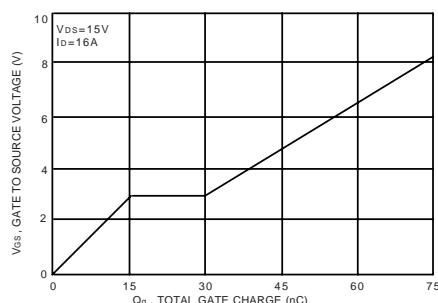


Figure 4. On-Resistance Variation with Temperature

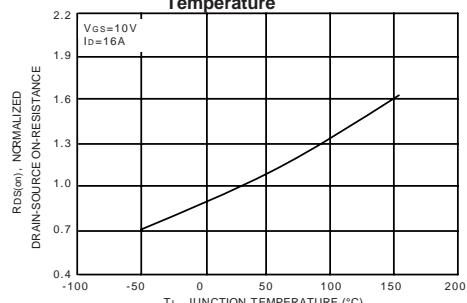


Figure 5. Gate Threshold Variation with Temperature

