

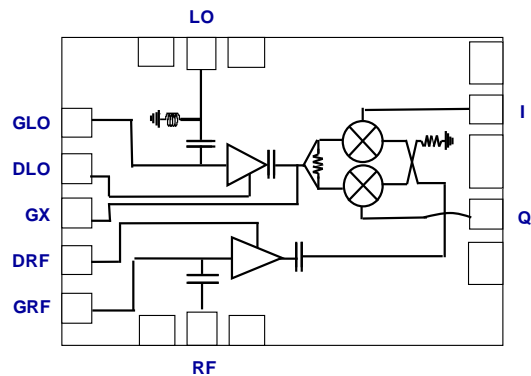
71-86GHz Down-converter GaAs Monolithic Microwave IC

Description

The CHR1080a98F is a multifunction monolithic receiver, which integrates a balanced sub-harmonic cold FET mixer, a LO buffer, and a RF low noise amplifier.

It is designed for the E-band telecommunication application, particularly well suited for the new generation of high capacity backhaul.

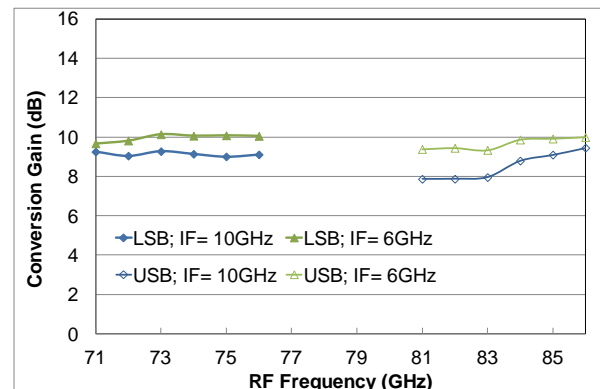
The circuit is manufactured with a pHEMT process, 0.10µm gate length. It is available in chip form.



Main Features

- Broadband RF performances: 71-86GHz
- 8dB Conversion Gain
- 5dB Noise Figure
- -10dBm Input Power at 1dB compression
- DC bias: Vd=3.5V @Id=175mA
- Chip size 3.43x2.24x0.07mm

Conversion Gain



Main Electrical Characteristics

Tamb.= +25°C

Symbol	Parameter	Min	Typ	Max	Unit
F _{RF}	RF Frequency	71		86	GHz
F _{IF}	IF frequency	DC		12	GHz
G	Conversion gain		8		dB
NF	Noise Figure		5		dB

Electrical Characteristics

Tamb.= +25°C, Vd = 3.5V

Symbol	Parameter	Min	Typ	Max	Unit
FRF	RF Frequency range	71		86	GHz
FLO	LO Frequency range	34.5		44	GHz
FIF	IF output Frequency	DC	10	12	GHz
PLO	LO input power	0	1		dBm
Gc	Conversion gain ⁽¹⁾		8		dB
R_LO	LO input return loss		8		dB
R_RF	RF input return loss		10		dB
NF	Noise Figure		5		dB
Im_rej	Image rejection ⁽¹⁾		16		dBc
2LO Leak.	2LO Leakage to RF port		-38		dBm
RFin P1dB	RF Input power @1dB compression		-10		dBm
Idt	Drain current (Id LO Buffer +Id LNA) ⁽²⁾		175		mA
DLO, DRF	DC drain voltage (LO Buffer, LNA)		3.5		V
GLO, GX, GRF	LO Buffer, Mixer, LNA DC gate voltage		-2		V

⁽¹⁾ An external combiner 90° is required on I / Q.

⁽²⁾ LO drain quiescent current 85mA, LNA drain quiescent current 90mA.

These values are representative of on-wafer measurements made without bonding wires at the RF & LO ports.

A ribbon (75 µm wide) connection at the RF and LO inputs (see chapter recommended chip assembly) could improve the results.

Absolute Maximum Ratings ⁽¹⁾T_{amb.} = +25°C

Symbol	Parameter	Values	Unit
V _d	Drain bias voltage	4V	V
I _{dt}	Drain bias current	240	mA
V _g	Gate bias voltage	-3.0 to -1.4	V
Pin_LO	Maximum LO peak input power overdrive ⁽²⁾	+10	dBm
T _j	Junction temperature	175	°C
T _a	Operating temperature range	-40 to +85	°C
T _{stg}	Storage temperature range	-55 to +150	°C

⁽¹⁾ Operation of this device above anyone of these parameters may cause permanent damage.

⁽²⁾ Duration < 1s.

Typical Bias ConditionsT_{amb.} = +25°C

Pad name	Pad N°	Parameter	Typical Values	Unit
GRF	6	LNA DC gate voltage ⁽¹⁾	-2	V
DRF	7	LNA DC drain voltage (90mA)	3.5	V
GX	8	Mixer DC gate voltage	-2	V
DLO	10	LO Buffer DC drain voltage (85mA)	3.5	V
GLO	12	LO Buffer DC gate voltage	-2.2	V
	5, 9, 11	Not connected		

⁽¹⁾ LNA gate voltage could be adjusted between -3.0V and -1.4V to perform gain control

Typical on wafer Measurements

Tamb.= +25°C, Vd = +3.5V

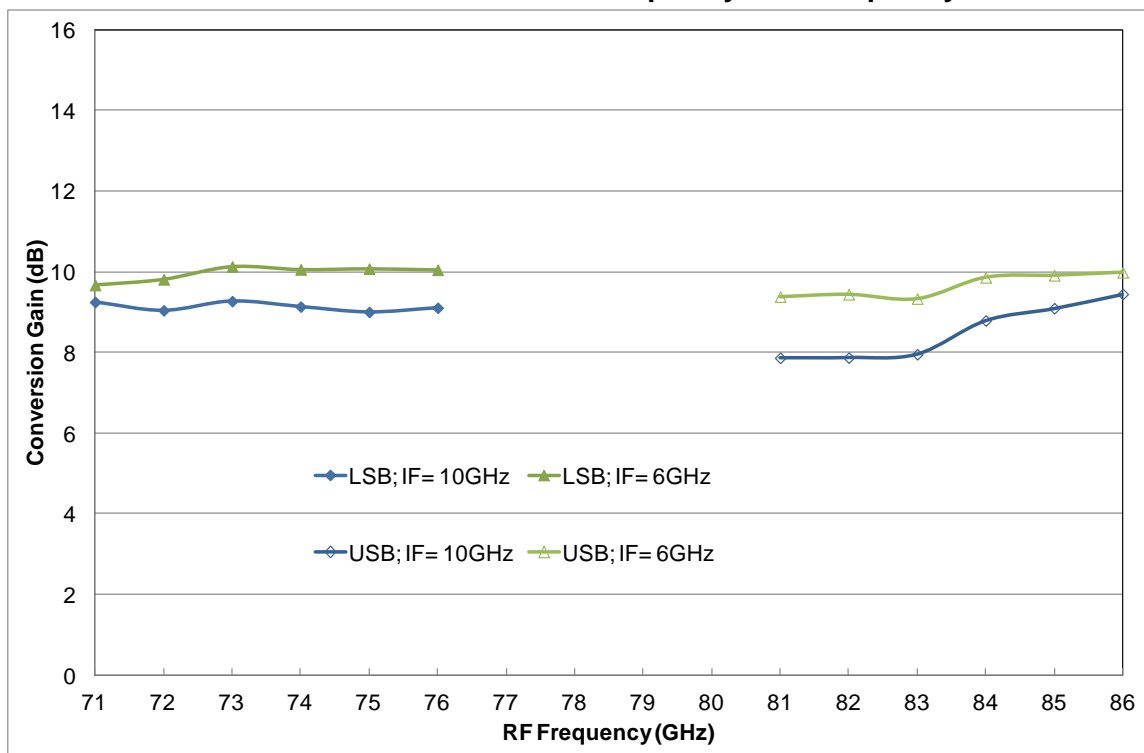
Without specific comment:

GLO= -2.2V, GX= -2V, Pin_RF= -30dBm, Pin_LO= 1dBm, IF frequency = 10GHz

GRF to be tuned for I_DRF= 90mA (GRF close to -2V)

LSB: RF= 2LO- IF; USB: RF= 2LO+ IF

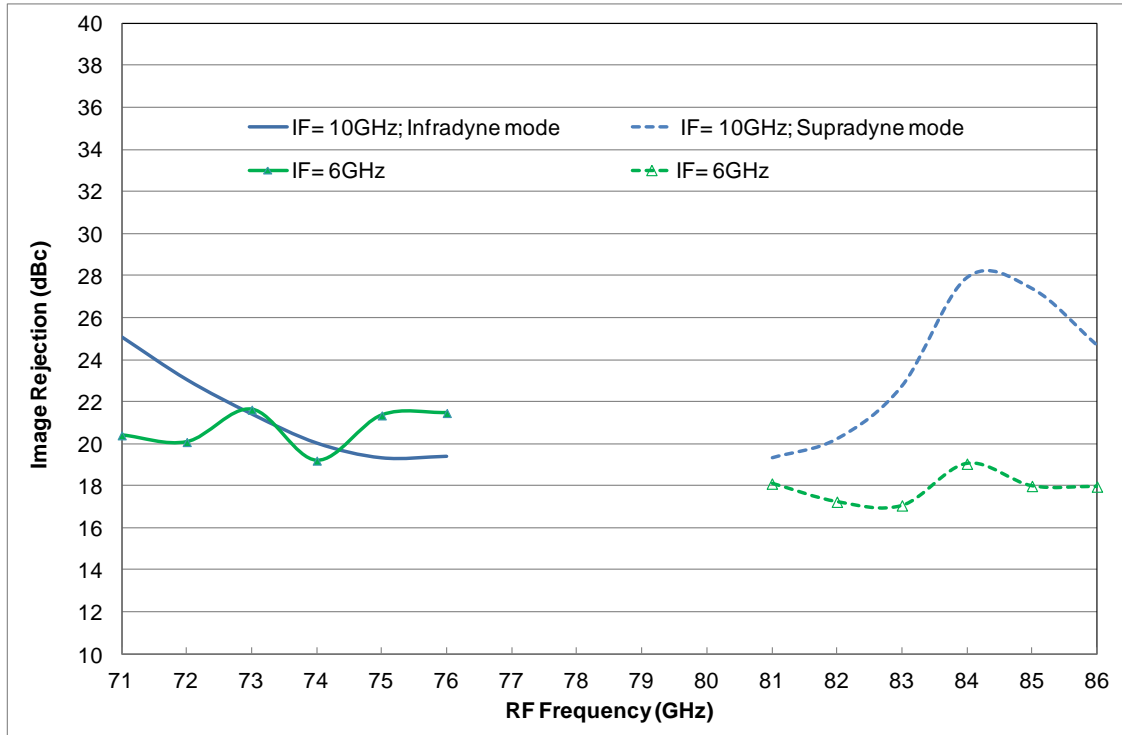
Conversion Gain versus RF Frequency & IF Frequency



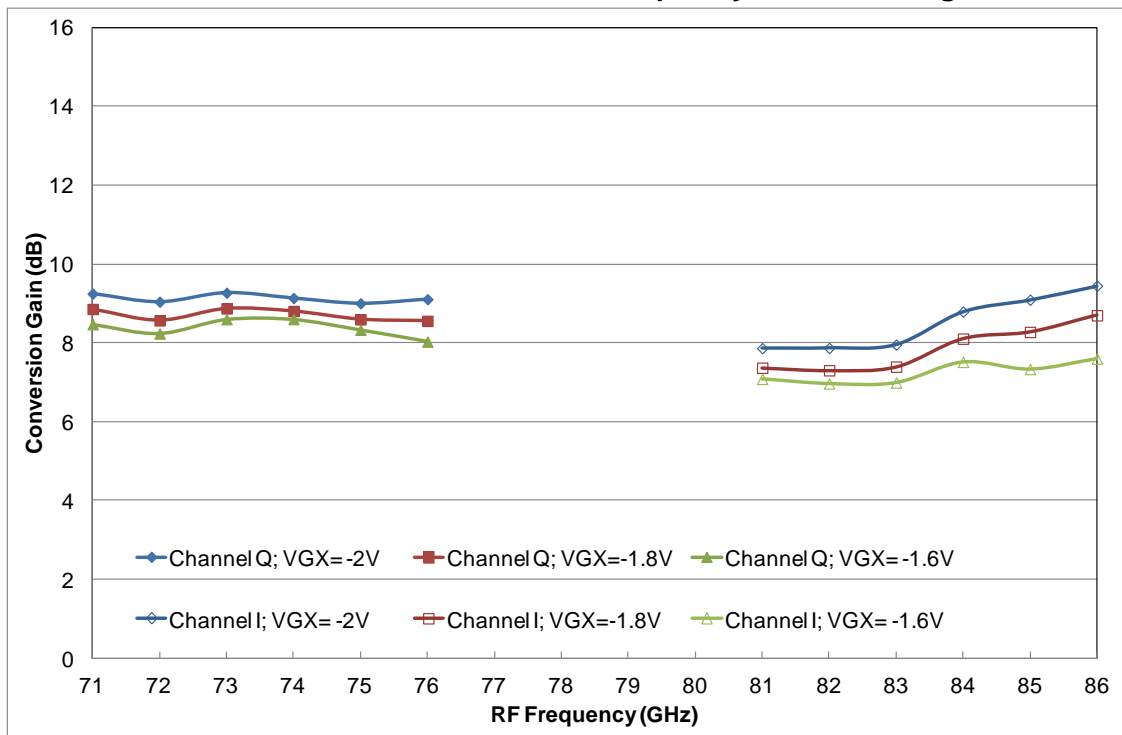
Typical on wafer Measurements

Tamb.= +25°C, Vd = +3.5V

Image rejection versus RF Frequency & IF Frequency



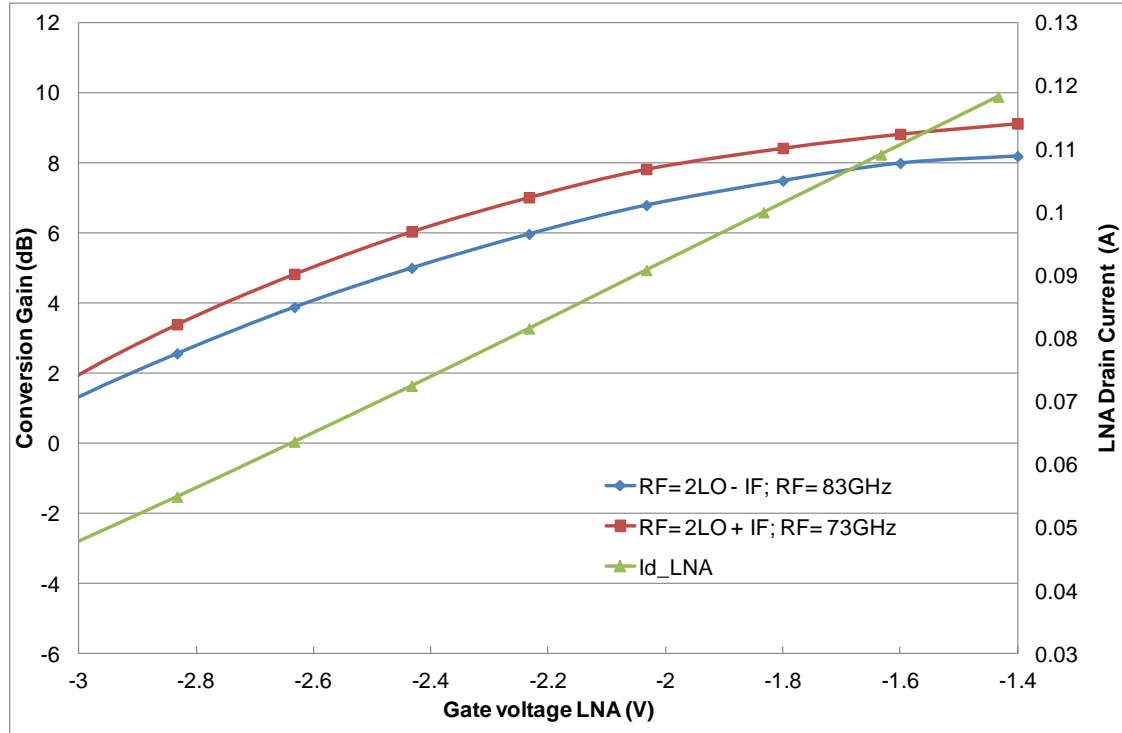
Conversion Gain versus RF Frequency & Mixer voltage



Typical on wafer Measurements

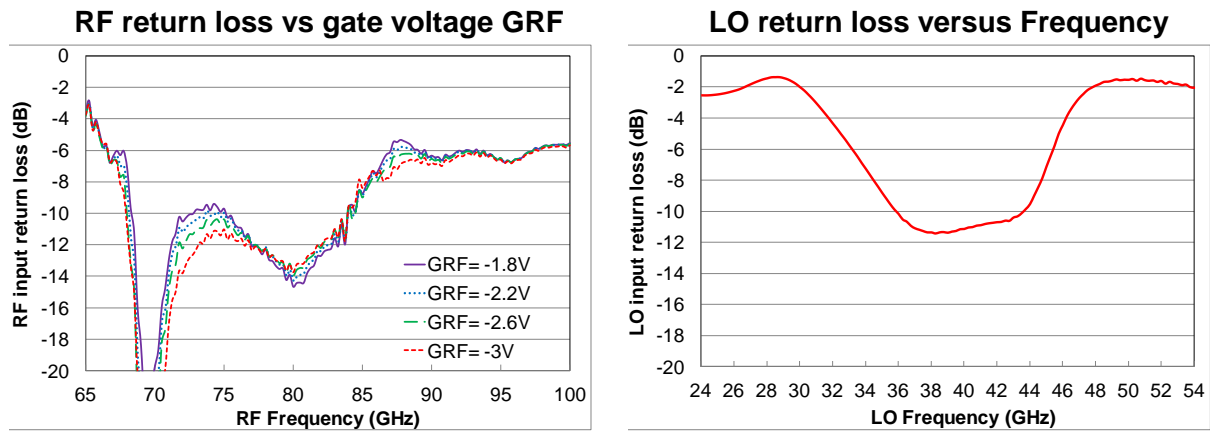
Tamb.= +25°C, Vd = +3.5V

Conversion Gain control versus LNA gate voltage GRF

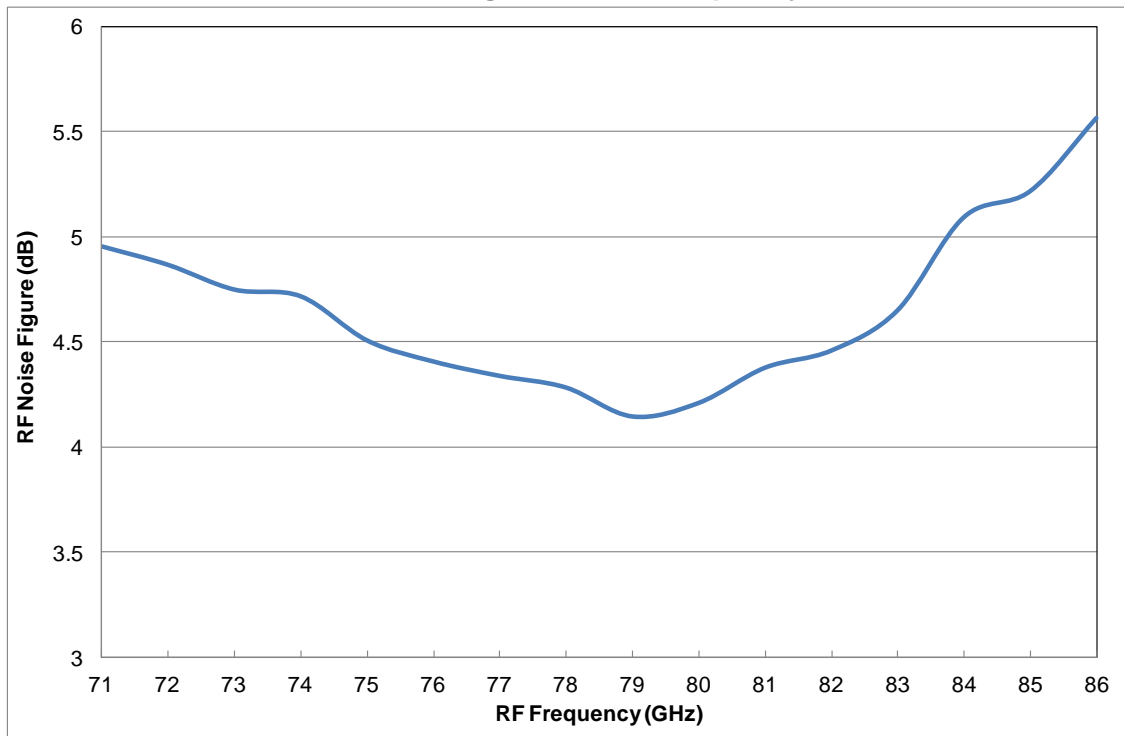


Typical on wafer Measurements

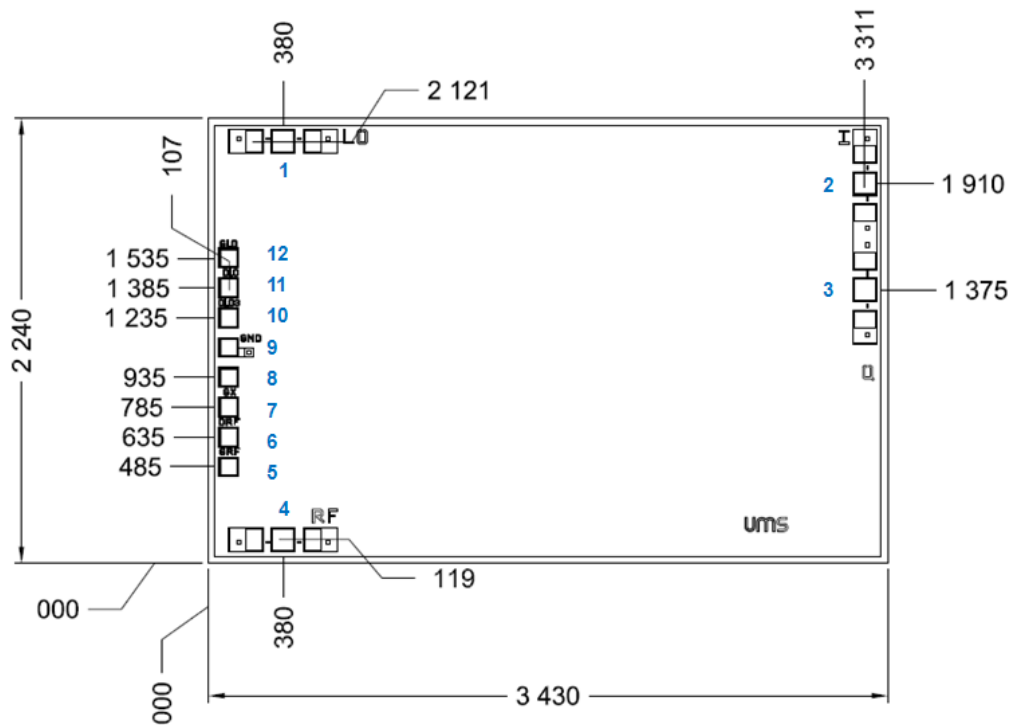
Tamb.= +25°C, Vd = +3.5V



Noise figure versus Frequency



Mechanical data



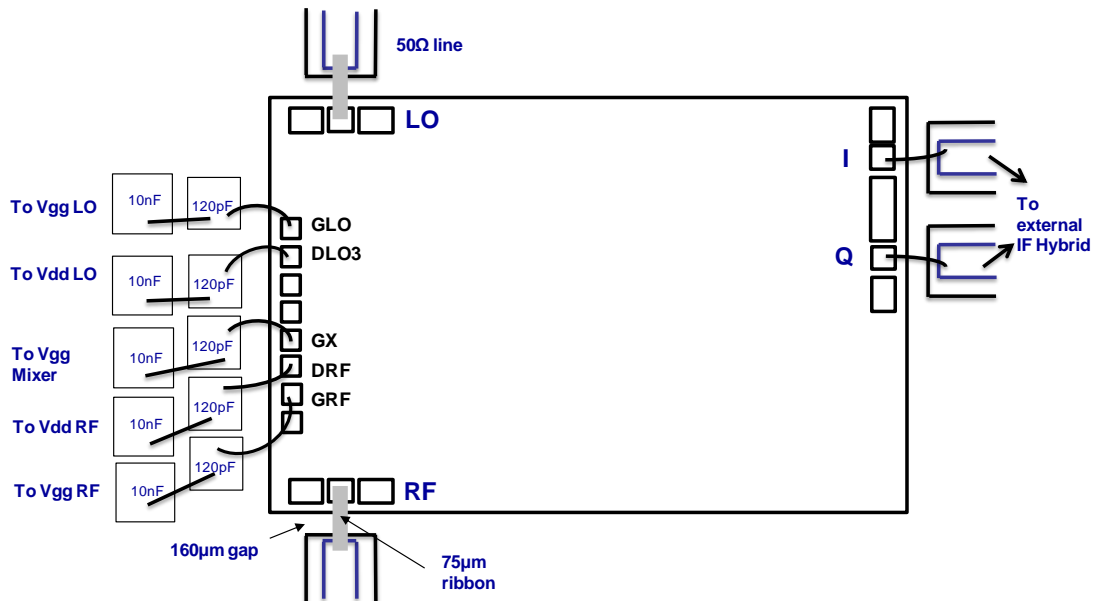
Chip thickness: 70µm.
 Chip size: 3430x2240 ±35µm
 All dimensions are in micrometers

RF Pads = 108 x 106 (BCB opening)
 DC Pads = 86 x 83 (BCB opening)

Recommended circuit bonding table

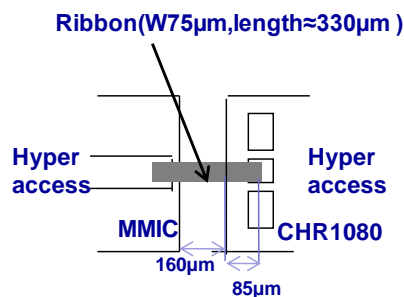
Pin number	Pin name	Description	Decoupling
1	LO	LO in	
2	I	IF (I)	
3	Q	IF (Q)	
4	RF	RF in	
6	GRF	DC LNA Gate Voltage (-2V)	120pF, 10nF
7	DRF	DC LNA drain Voltage (3.5V)	120pF, 10nF
8	GX	DC Mixer Gate Voltage (-2V)	120pF, 10nF
11	DLO3	DC LO Buffer drain Voltage (3.5V)	120pF, 10nF
12	GLO	DC LO buffer Gate Voltage (-2.2V)	120pF, 10nF
5, 9, 10		Not connected	

Recommended assembly plan



The design integrates a half ribbon (75μm wide) connection at the RF and LO input of the MMIC amplifier compliant with a 50Ω line on GaAs MMIC.

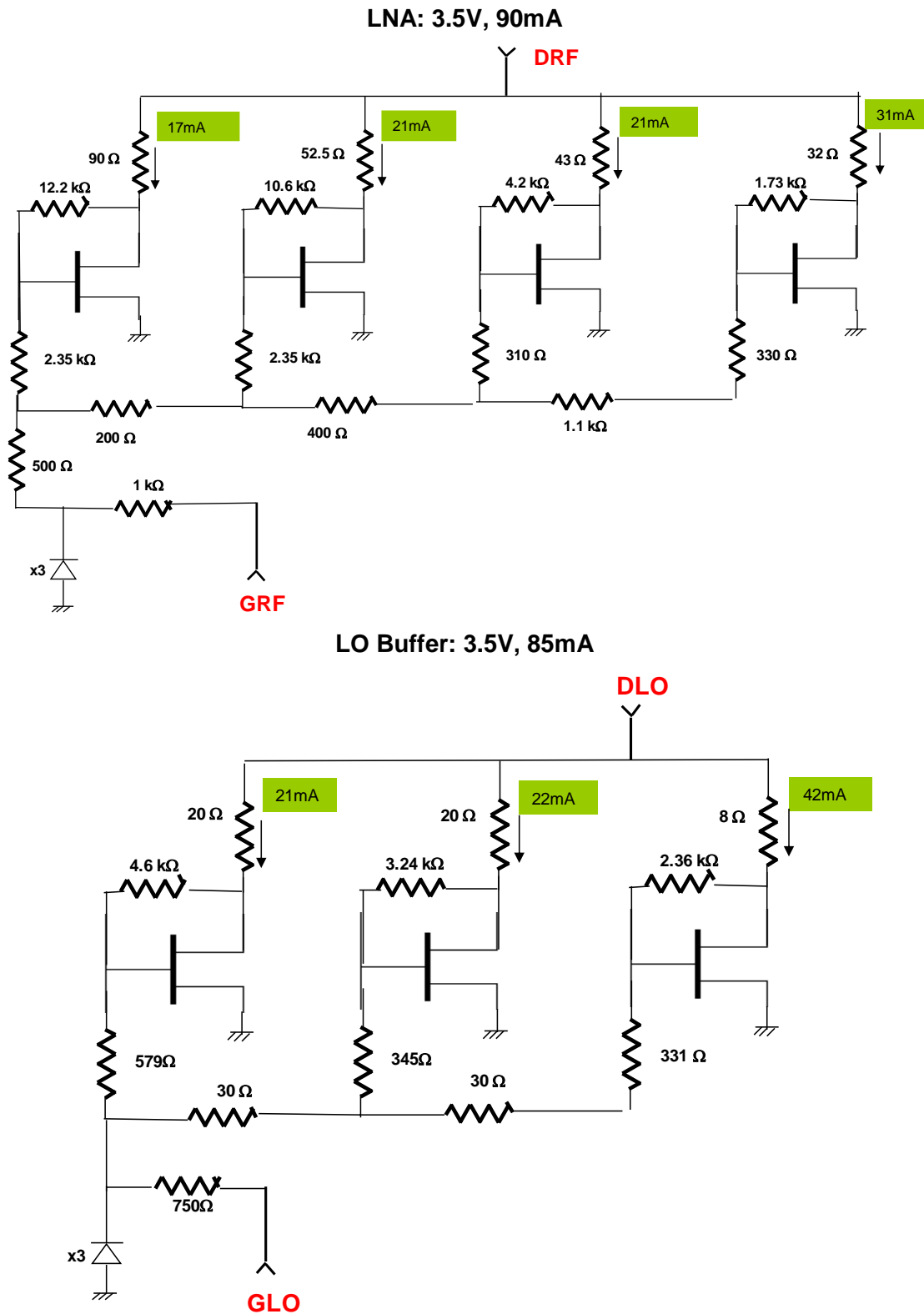
Circuits having to be as close as possible to each other, the ribbon length must be reduced to the achievable minimum (160μm gap between two chips is considered) and the loop height must also be the smallest realizable (80μm).



A second solution is the use of double wires (Ø 25μm). In this case, a minimum of two wires and the same chip to chip distance as ribbon solution is necessary to reduce the inductance effect. Nevertheless, simulations have demonstrated an improvement of RF performance for E-band frequency range with the use of ribbon connection instead of wire.

Regarding the connection of the DC pads, a 25μm wedge bonding is preferred.

DC Schematic



Notes



Recommended ESD management

Refer to the application note AN0020 available at <https://www.ums-rf.com> for ESD sensitivity and handling recommendations for the UMS products.

Recommended environmental management

UMS products are compliant with the regulation in particular with the directives RoHS N°2011/65 and REACH N°1907/2006. More environmental data are available in the application note AN0019 also available at <https://www.ums-rf.com>.

Ordering Information

Chip form:

CHR1080a98F/00

Information furnished is believed to be accurate and reliable. However **United Monolithic Semiconductors S.A.S.** assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of **United Monolithic Semiconductors S.A.S.** Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. **United Monolithic Semiconductors S.A.S.** products are not authorised for use as critical components in life support devices or systems without express written approval from **United Monolithic Semiconductors S.A.S.**