



CHENMKO ENTERPRISE CO.,LTD

Lead free devices

**SURFACE MOUNT
PNP&NPN Multi-Chip General Purpose Transistor**

VOLTAGE 40 Volts CURRENT 600 mAmpere

CHT4413UPNPT

APPLICATION

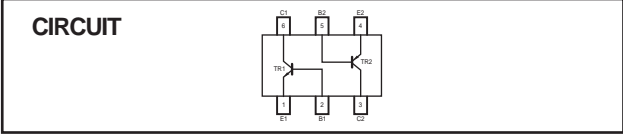
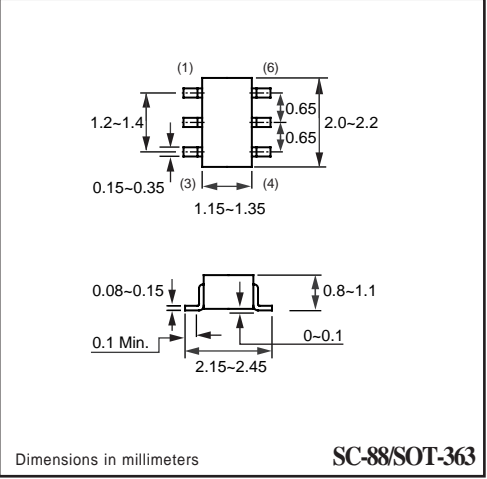
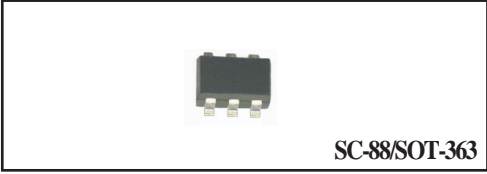
- * AF input stages and driver applicationon equipment.
- * Other general purpose applications.

FEATURE

- * Small surface mounting type. (SC-88/SOT-363)
- * High current gain.
- * Suitable for high packing density.
- * Low collector-emitter saturation.
- * High saturation current capability.
- * Two internal isolated PNP and NPN transistors in one package.

CONSTRUCTION

- * PNP and NPN transistors in one package.



TR1 CHT4401 LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	60	V
V _{CEO}	collector-emitter voltage	open base	-	40	V
V _{EBO}	emitter-base voltage	open collector	-	6	V
I _C	collector current (DC)		-	600	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	-	200	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		-	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

2004-8

1. Transistor mounted on an FR4 printed-circuit board.

TR2 CHT4403 LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	-40	V
V_{CEO}	collector-emitter voltage	open base	–	-40	V
V_{EBO}	emitter-base voltage	open collector	–	-5	V
I_C	collector current (DC)		–	-600	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ }^\circ\text{C}$; note 2	–	200	mW
T_{stg}	storage temperature		-65	+150	$^\circ\text{C}$
T_j	junction temperature		–	150	$^\circ\text{C}$
T_{amb}	operating ambient temperature		-65	+150	$^\circ\text{C}$

Note

2. Transistor mounted on an FR4 printed-circuit board.

TR1 CHT4401 CHARACTERISTICS

$T_{amb} = 25\text{ }^\circ\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100\mu\text{A}$; $I_E = 0\text{A}$	60	–	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 1\text{mA}$; $I_B = 0\text{A}$	40	–	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	$I_E = 100\mu\text{A}$; $I_C = 0\text{A}$	6	–	V
I_{CEX}	collector cut-off current	$V_{EB(OFF)} = 0.4\text{V}$; $V_{CE} = 35\text{V}$	–	100	nA
I_{BL}	base cut-off current	$V_{EB(OFF)} = 0.4\text{V}$; $V_{CE} = 35\text{V}$	–	100	nA
h_{FE}	DC current gain	$I_C = 100\mu\text{A}$; $V_{CE} = 1\text{V}$	20	–	
		$I_C = 1\text{mA}$; $V_{CE} = 1\text{V}$	40	–	
		$I_C = 10\text{mA}$; $V_{CE} = 1\text{V}$	80	–	
		$I_C = 150\text{mA}$; $V_{CE} = 1\text{V}$	100	300	
		$I_C = 500\text{mA}$; $V_{CE} = 2\text{V}$	40	–	
V_{CEsat}	collector-emitter saturation	$I_C = 150\text{mA}$; $I_B = 15\text{mA}$	–	400	mV
		$I_C = 500\text{mA}$; $I_B = 50\text{mA}$	–	750	mV
V_{BEsat}	base-emitter saturation voltage	$I_C = 150\text{mA}$; $I_B = 15\text{mA}$	750	950	mV
		$I_C = 500\text{mA}$; $I_B = 50\text{mA}$	–	1200	mV
C_{cb}	output capacitance	$V_{CB} = 5.0\text{V}$; $f = 1.0\text{MHz}$; $I_E = 0$	–	6.5	pF
C_{eb}	input capacitance	$V_{EB} = 0.5\text{V}$; $f = 1.0\text{MHz}$; $I_C = 0$	–	30	pF
h_{ie}	input impedance	$V_{CE} = 10\text{V}$; $f = 1.0\text{KHz}$; $I_C = 1.0\text{mA}$	1.0	15	$\text{K}\Omega$
h_{re}	voltage feedback ratio		0.1	8.0	$\times 10^{-4}$
h_{fe}	small signal current gain		40	500	
h_{oe}	output impedance		1.0	30	μS
f_T	transition frequency	$I_C = 20\text{mA}$; $V_{CE} = 10\text{V}$; $f = 100\text{MHz}$	250	–	MHz
t_d	delay time	$V_{CC} = 30\text{V}$; $I_C = 150\text{mA}$	–	15	nS
t_r	rise time	$V_{BE(off)} = 2.0\text{V}$; $I_{B1} = 15\text{mA}$	–	20	nS
t_s	storage time	$V_{CC} = 30\text{V}$; $I_C = 150\text{mA}$	–	225	nS
t_f	fall time	$I_{B1} = I_{B2} = 15\text{mA}$	–	30	nS

TR2 CHT4403 CHARACTERISTICS

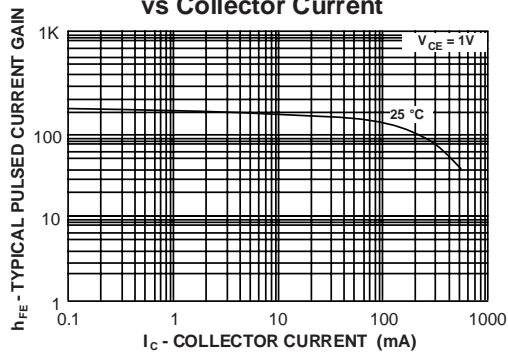
T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100uA ; I _E = 0A	-40	–	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = -1mA ; I _B = 0A	-40	–	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = -100uA ; I _C = 0A	-6	–	V
I _C EX	collector cut-off current	V _{EB(OFF)} = -0.4V ; V _{CE} = -35 V	–	-100	nA
I _{BL}	base cut-off current	V _{EB(OFF)} = -0.4V ; V _{CE} = -35 V	–	-100	nA
h _{FE}	DC current gain	I _C = -100uA; V _{CE} = -1V	30	–	
		I _C = -1 mA; V _{CE} = -1V	60	–	
		I _C = -10 mA; V _{CE} = -1V	100	–	
		I _C = -150 mA; V _{CE} = -2V	100	300	
		I _C = -500 mA; V _{CE} = -2V	20	–	
V _{CEsat}	collector-emitter saturation	I _C = -150 mA; I _B = -15 mA	–	-400	mV
		I _C = -500 mA; I _B = -50 mA	–	-750	mV
V _{BEsat}	base-emitter saturation voltage	I _C = -150 mA; I _B = -15 mA	-750	-950	mV
		I _C = -500 mA; I _B = -50 mA	–	-1300	mV
C _{cb}	output capacitance	V _{CB} =-10V; f=1.0MHZ; I _E =0	–	8.5	pF
C _{eb}	input capacitance	V _{EB} =-0.5V; f=1.0MHZ; I _C =0	–	30	pF
h _{ie}	input impedance	V _{CE} =-10V; f=1.0KHZ; I _C =-1.0mA	1.5	15	KΩ
h _{re}	voltage feedback ratio		0.1	8.0	x10 ⁻⁴
h _{fe}	small signal current gain		60	500	
h _{oe}	output impedance		1.0	100	μS
f _T	transition frequency		I _C = -20 mA; V _{CE} = - 10 V f = 100 MHz	200	–
t _d	delay time	V _{CC} =-30V; I _C =-150mA	–	15	nS
t _r	rise time	V _{BE(off)} =-2.0V; I _{B1} =-15mA	–	20	nS
t _s	storage time	V _{CC} =-30V; I _C =-150mA	–	225	nS
t _f	fall time	I _{B1} =I _{B2} =-15mA	–	30	nS

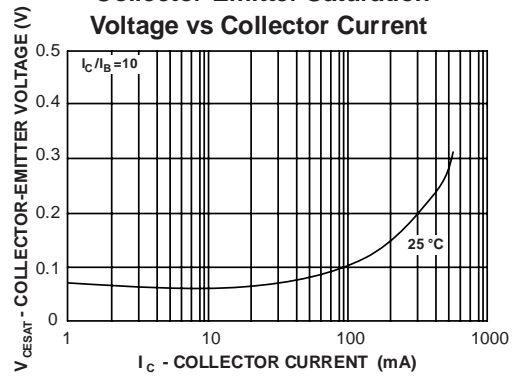
RATING CHARACTERISTIC CURVES (CHT4413UPNPT)

TR1 CHT4401 Typical Characteristics

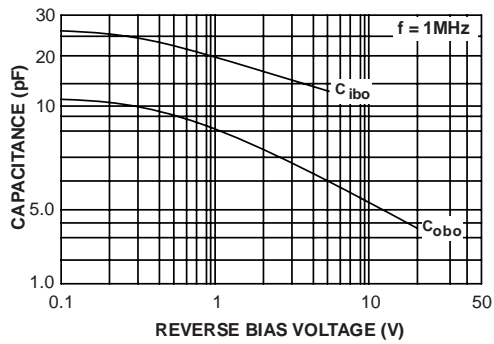
Typical DC Current Gain vs Collector Current



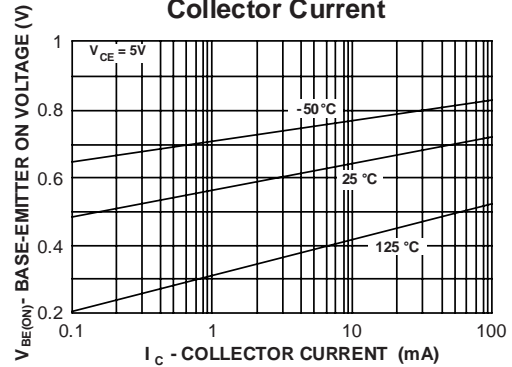
Collector-Emitter Saturation Voltage vs Collector Current



Typical Capacitance



Base-Emitter ON Voltage vs Collector Current



RATING CHARACTERISTIC CURVES (CHT4413UPNPT)

TR1 CHT4403 Typical Characteristics

