## L, S-band Middle Power SPDT Switch



#### **Features**

Control voltage :

VC(H) = 1.8 to 5.0 V (3.0 V TYP.)

VC(L) = -0.2 to 0.2 V (0 V TYP.)

Low Insertion Loss:

 $L_{ins}1 = 0.30 \text{ dB TYP.} @ f = 0.05 \text{ to } 0.5 \text{ GHz}$ 

 $L_{ins}2 = 0.30 \text{ dB TYP.} @ f = 0.5 \text{ to } 1.0 \text{ GHz}$ 

 $L_{ins}3$  = 0.30 dB TYP. @ f = 1.0 to 2.0 GHz

 $L_{ins}4 = 0.35 \text{ dB TYP.}$  @ f = 2.0 to 2.5 GHz

 $L_{ins}5 = 0.35 \text{ dB TYP.} @ f = 2.5 \text{ to } 3.0 \text{ GHz}$ 

High Isolation :

ISL1= 38 dB TYP. @ f = 0.05 to 0.5 GHz

ISL2= 32 dB TYP. @ f = 0.5 to 1.0 GHz

ISL3= 27 dB TYP. @ f = 1.0 to 2.0 GHz

ISL4= 25 dB TYP. @ f = 2.0 to 2.5 GHz

ISL5= 23 dB TYP. @ f = 2.5 to 3.0 GHz

Handling power :

 $P_{in(0.5dB)} = +32 \text{ dBm TYP.} @ f = 3.0 \text{ GHz},$ 

VC(H) = 3.0 V, VC(L) = 0 V

## **Applications**

- Wireless LAN (IEEE 802.11 b/g)
- Bluetooth

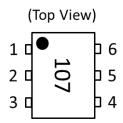
#### **Package**

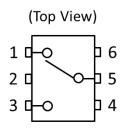
 6-pin lead-less mini mold package (1.5mm x 1.1mm x 0.55mm)

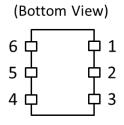
## **Description**

 The CKRF2214MM66 is a pHEMT GaAs SPDT (Single Pole Double Throw) switch. This device can operate frequency from 0.05 to 3.0GHz, having the low insertion loss and high isolation.

# Pin Configuration And Internal Block Diagram







Pin No.	Pin Name
FIII NO.	FILLINGILLE
1	RF1
2	GND
3	RF2
4	VC2
5	RFC
6	VC1

# **Ordering Information**

Part Number	Order Number	Package	Marking	Supplying Form
CKRF2214MM66-C2	CKRF2214MM66-C2 ·6-pin lead-less		107	•Embossed tape 8 mm wide
		mini mold package		•Pin 1, 6 face the perforation
		(Pb-Free)		side of the tape
				·Qty 10 Kpcs/reel





# **Absolute Maximum Ratings**

 $(T_A=+25^{\circ}C, \text{ unless otherwise specified})$ 

Parameter	Symbol	Rating	Unit
Control Voltage	VC	6.0 <sup>Note 1</sup>	V
Input Power	P <sub>in</sub>	+33 <sup>Note 2</sup>	dBm
Operating Ambient Temperature	T <sub>A</sub>	-45~+85	$^{\circ}$
Storage Temperature	$T_{stg}$	-55~+150	$^{\circ}$

- Note 1. |VC1 VC2|≤6.0V
  - 2. 3.0V≦|VC1 VC2|≦5.0V, f≥0.5GHz

# **Recommended Operating Range**

 $(T_A=+25^{\circ}C, \text{ unless otherwise specified})$ 

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operating Frequency	f	0.05	-	3.0	GHz
Switch Control Voltage (H)	VC(H)	+1.8	+3.0	+5.0	V
Switch Control Voltage (L)	VC(L)	-0.2	0	+0.2	V

#### **Truth Table**

VC1	VC2	RFC-RF1	RFC-RF2
Low	High	ON	OFF
High	Low	OFF	ON

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#### **Electrical Characteristics**

 $(T_A=+25^{\circ}C, VC(H)=3.0V, VC(L)=0V, Zo=50\Omega, DC Block Capacitance=56pF, unless otherwise specified)$ 

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Insertion Loss	L <sub>INS</sub> 1	f=0.05 to 0.5GHz Note 1		0.30	0.50	dB
	L <sub>INS</sub> 2	f=0.5 to 1.0GHz		0.30	0.50	dB
	L <sub>INS</sub> 3	f=1.0 to 2.0GHz		0.30	0.50	dB
	L <sub>INS</sub> 4	f=2.0 to 2.5GHz		0.35	0.55	dB
	L <sub>INS</sub> 5	f=2.5 to 3.0GHz		0.35	0.55	dB
Isolation	ISL1	f=0.05 to 0.5GHz Note 1	35	38		dB
	ISL2	f=0.5 to 1.0GHz	29	32		dB
	ISL3	f=1.0 to 2.0GHz	24	27		dB
	ISL4	f=2.0 to 2.5GHz	22	25		dB
	ISL5	f=2.5 to 3.0GHz	20	23		dB
Input Return Loss	RL <sub>in</sub> 1	f=0.05 to 0.5GHz Note 1	15	20		dB
	RL <sub>in</sub> 2	f=0.5 to 3.0GHz	15	20		dB
Output Return Loss	RL <sub>out</sub> 1	f=0.05 to 0.5GHz Note 1	15	20		dB
	RL <sub>out</sub> 2	f=0.5 to 3.0GHz	15	20		dB
0.1dB Loss Compression	P <sub>in(0.1dB)</sub>	f=3.0GHz, VC(H)=1.8V, VC(L)=0V		+26		dBm
Input Power Note 2		f=3.0GHz, VC(H)=3.0V, VC(L)=0V		+30		dBm
0.5dB Loss Compression	P <sub>in(0.5dB)</sub>	f=3.0GHz, VC(H)=1.8V, VC(L)=0V		+29		dBm
Input Power Note 3		f=3.0GHz, VC(H)=3.0V, VC(L)=0V		+32		dBm
2nd Harmonics	2f0	f=3.0GHz, P <sub>in</sub> =+20dBm		-85		dBc
3rd Harmonics	3f0	f=3.0GHz, P <sub>in</sub> =+20dBm		-85		dBc
3rd Order Input Intercept	IIP <sub>3</sub>	f=2.5GHz, 2-tone		. 50		dD.vo
Point		1MHz Spacing		+58		dBm
Switch Control Current	$I_{CONT}$	RF none		1	10	uA
Switching Speed	$T_SW$	50% CTL to 90/10% RF		50		ns

Note 1. DC block capacitance = 1000pF at f=0.05 to 0.5GHz

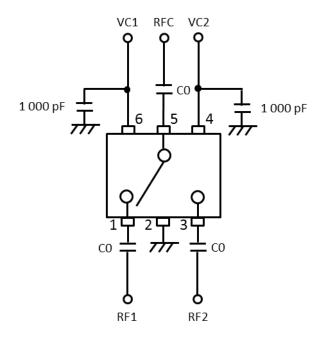
- 2.  $P_{in(0.1dB)}$  is the measured input power level when the insertion loss increases 0.1dB more than that of the linear range.
- 3.  $P_{in(0.5dB)}$  is the measured input power level when the insertion loss increases 0.5dB more than that of the linear range.

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## **Evaluation Circuit**

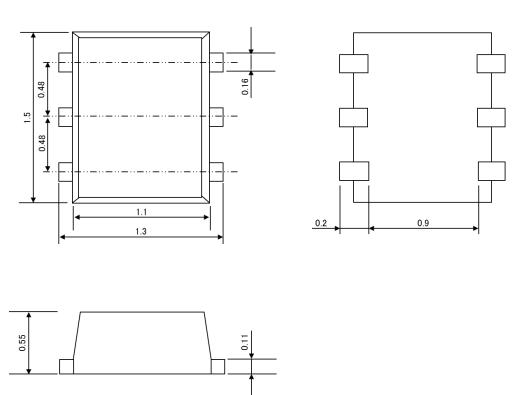


**Note** C0: 0.05 to 0.5 GHz 1000pF

: 0.5 to 3.0 GHz 56pF

The application circuits and their parameters are for reference only and are not intended for use in actual design-ins. This device is used it is necessary to use DC Block Capacitance.

## **Package Dimensions**



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#### L, S-band Middle Power SPDT Switch



[Caution in the gallium arsenide (GaAs) product handling]

This product uses gallium arsenide (GaAs) of the toxic substance appointed in laws and ordinances. GaAs vapor and powder are hazardous to human health if inhaled or ingested.

- Do not dispose in fire or break up this product.
- Do not chemically make gas or powder with this product.
- When discard this product, please obey the law of your country.
- Do not lick the product or in any way allow it to enter the mouth.

#### [CAUTION]

Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

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