

SPECIFICATION

- Supplier : Samsung electro-mechanics
- Product : Multi-layer Ceramic Capacitor
- Samsung P/N : **CL21B106KQQNNNE**
- Description : **CAP, 10 μ F, 6.3V, \pm 10%, X7R, 0805**

A. Samsung Part Number

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 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪

① Series	Samsung Multi-layer Ceramic Capacitor		
② Size	0805 (inch code)	L: 2.0 \pm 0.15 mm	W: 1.25 \pm 0.15 mm
③ Dielectric	X7R	⑧ Inner electrode	Ni
④ Capacitance	10 μ F	Termination	Cu
⑤ Capacitance tolerance	\pm 10 %	Plating	Sn 100% (Pb Free)
⑥ Rated Voltage	6.3 V	⑨ Product	Normal
⑦ Thickness	1.25 \pm 0.15 mm	⑩ Special	Reserved for future use
		⑪ Packaging	Embossed Type, 7" reel

B. Samsung Reliability Test and Judgement condition

	Performance	Test condition
Capacitance	Within specified tolerance	1kHz \pm 10% 1.0 \pm 0.2Vrms
Tan δ (DF)	0.1 max.	
Insulation Resistance	10,000Mohm or 100Mohm $\cdot\mu$ F Whichever is Smaller	Rated Voltage 60~120 sec.
Appearance	No abnormal exterior appearance	Microscope (\times 10)
Withstanding Voltage	No dielectric breakdown or mechanical breakdown	250% of the rated voltage
Temperature Characterisitcs	X7R (From -55 $^{\circ}$ C to 125 $^{\circ}$ C, Capacitance change should be within \pm 15%)	
Adhesive Strength of Termination	No peeling shall be occur on the terminal electrode	500g-F, for 10 \pm 1 sec.
Bending Strength	Capacitance change : within \pm 12.5%	Bending to the limit (1mm) with 1.0mm/sec.
Solderability	More than 75% of terminal surface is to be soldered newly	SnAg3.0Cu0.5 solder 245 \pm 5 $^{\circ}$ C, 3 \pm 0.3sec. (preheating : 80~120 $^{\circ}$ C for 10~30sec.)
Resistance to Soldering heat	Capacitance change : within \pm 7.5% Tan δ , IR : initial spec.	Solder pot : 270 \pm 5 $^{\circ}$ C, 10 \pm 1sec.

	Performance	Test condition
Vibration Test	Capacitance change : within $\pm 5\%$ Tan δ , IR : initial spec.	Amplitude : 1.5mm From 10Hz to 55Hz (return : 1min.) 2hours \times 3 direction (x, y, z)
Moisture Resistance	Capacitance change : within $\pm 12.5\%$ Tan δ : 0.125 max IR : 12.5M $\Omega \cdot \mu F$ or Over	With rated voltage 40 $\pm 2^\circ C$, 90~95%RH, 500+12/-0hrs
High Temperature Resistance	Capacitance change : within $\pm 12.5\%$ Tan δ : 0.125 max IR : 25M $\Omega \cdot \mu F$ or Over	With 150% of the rated voltage Max. operating temperature 1000+48/-0hrs
Temperature Cycling	Capacitance change : within $\pm 7.5\%$ Tan δ , IR : initial spec.	1 cycle condition Min. operating temperature $\rightarrow 25^\circ C$ \rightarrow Max. operating temperature $\rightarrow 25^\circ C$ 5 cycle test

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : 260+0/-5 $^\circ C$, 10sec. Max)

* For the more detail Specification, Please refer to the Samsung MLCC catalogue.

Multi Layer Ceramic Capacitor (MLCC)



1. Model : CL21B106KQQNNNE

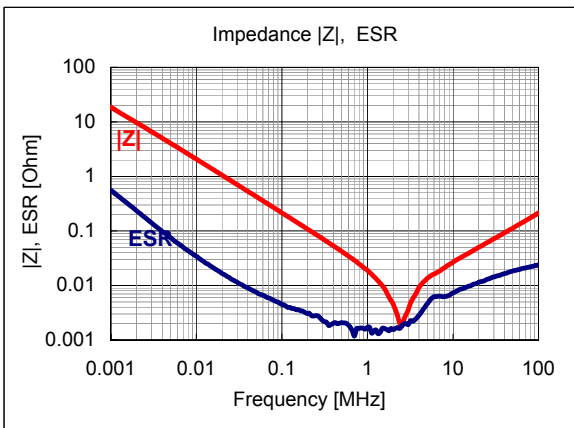
2. Description

Part no.	Size (inch(mm))	Thickness (mm)	Temperature characteristics	Capacitance value	Capacitance tolerance(%)	Voltage (V)
CL21B106KQQNNNE	0805/2012	1.25mm	X7R	10uF	± 10 %	6.3

3. Characteristics data

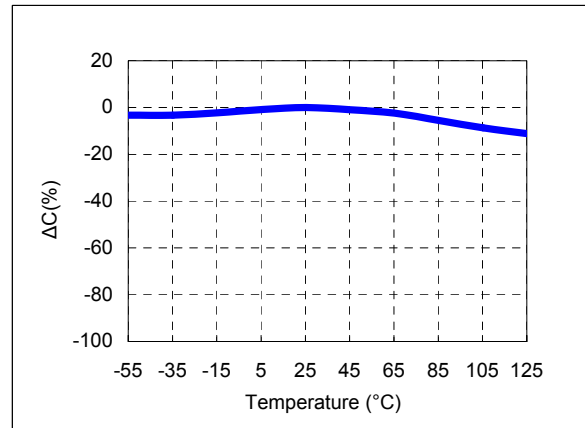
1) Frequency characteristics

Agilent E4294A, 0.5Vrms, 1KHz to 100MHz

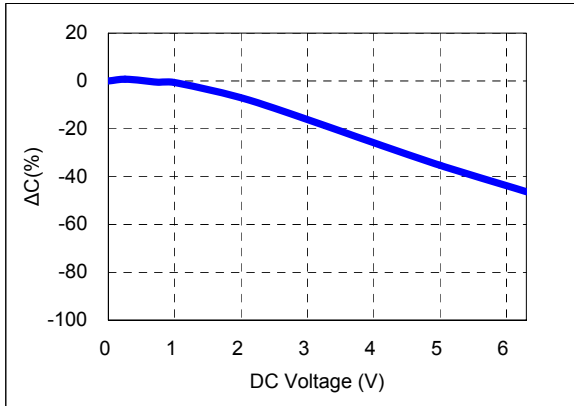


2) Temperature characteristics of capacitance(TCC)

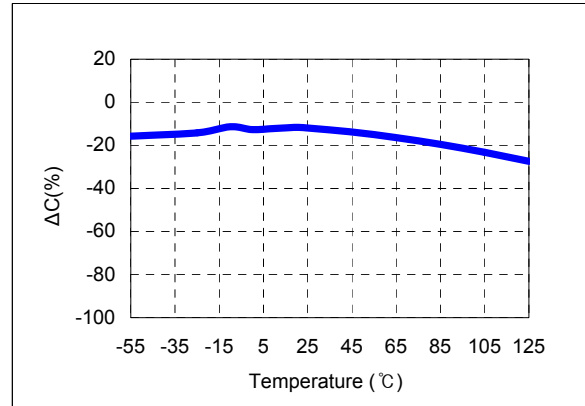
Agilent 4284A, 1kHz, 1Vrms



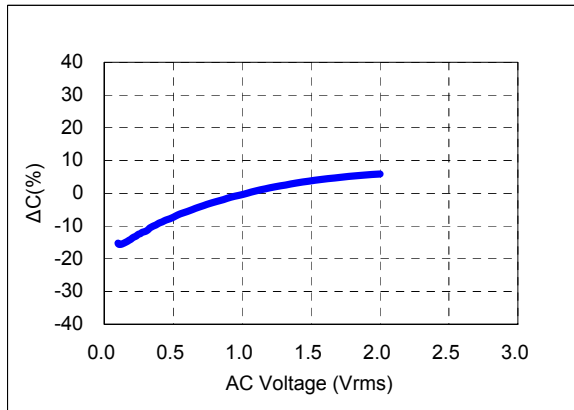
3) DC Bias characteristics (Agilent 4284A, 1kHz, 1Vrms)



4) Bias TCC characteristics (Agilent 4284A, 1kHz, 1Vrms, 3.15Vdc)



5) AC voltage characteristics (Agilent 4284A, 1kHz)



6) Ripple Current

