Chunghwa Picture Tubes, Ltd.
Technical Specification

To : **SCL**
Date : **2010 / 06 / 01**

TFT LCD
CLAA140WB01A

ACCEPTED BY :

Tentative

APPROVED BY	CHECKED BY	PREPARED BY
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Doc.No:	CLAA140WB01A-SCL-T1-2010/06/01	Issue Date:	2010/06/01

RECORD OF REVISIONS

Revision No.	Date	Description
T1	2010/4/8	Tentative version

CONTENTS

1. OVERVIEW

CLAA140WB01A is 14" color (16：9) TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, LVDS driver ICs, control circuit and backlight. By applying 6 bit digital data, 1366×RGB (3) ×768, 262K-color images are displayed on the 14" diagonal screen. General specifications are summarized in the following table：

ITEM	SPECIFICATION
Display Area	309.4(H) x 173.95(V) (mm) (14-inch diagonal)
Number of Pixels	1366 x 3 (RGB) x 768
Pixel Pitch	0.2265(H) x 0.2265(V) (mm)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	Normally white
Number of Colors	262,144(6bits) (LVDS)
Gamut	42%(min)/45% (Typ)
Optimum Viewing Angle	6 o'clock
Response Time	8ms (Typ)
Surface Treatment	Glare
Viewing Angle	40°、-40° /15°、-30° (MIN.)
Brightness	200 cd/m^2 (5point) (Typ)
Uniformity	13point：(65%)
Consumption of Power	(4.5)W (Max)
Module Size	320.9(W)×205.6(H)×3.6(D) (mm)（Max）
Module Weight (g)	(320 g) (Max)

The LCD Products listed on this document are not suitable for use of aerospace equipment, submarine cable, and nuclear reactor control system and life support systems. If customers intend to use these LCD products for applications listed above or those not included in the "Standard" list as follows, please contact our sales in advance.

Standard：Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tool, Industrial robot, Audio and Visual equipment, Other consumer products.

2. ABSOLUTE MAXIMUM RATINGS

The following are maximum value, which if exceeded, may cause faulty operation or damage to the unit.

ITEM	SYMBOL	MIN	MAX	UNIT	NOTE
LCD Power Voltage	VCC	0	4.0	V	
LED Driver Input Voltage	VBL+	7	21	V	
Operation Temperature	Top	0	50	℃	*1).*2).*3).*4).
Storage Temperature	Tstg	-25	65	℃	*1).*2).*3).

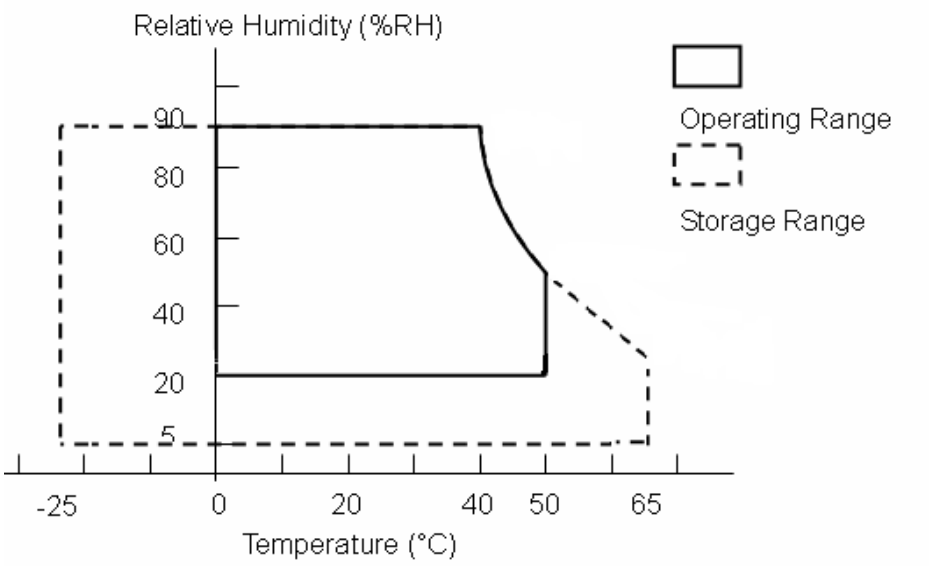
【Note】

*1) The relative temperature and humidity range are as below sketch, 90%RH Max. (Ta≦40℃)

*2) The maximum wet bulb temperature ≦39℃(Ta＞40℃) and without dewing.

*3) If product in environment which over the definition of the relative temperature and humidity out of range too long, it will affect visual of LCD.

*4) If you operate LCD in normal temperature range, the center surface of panel should be under 50℃.



3. ELECTRICAL CHARACTERISTICS

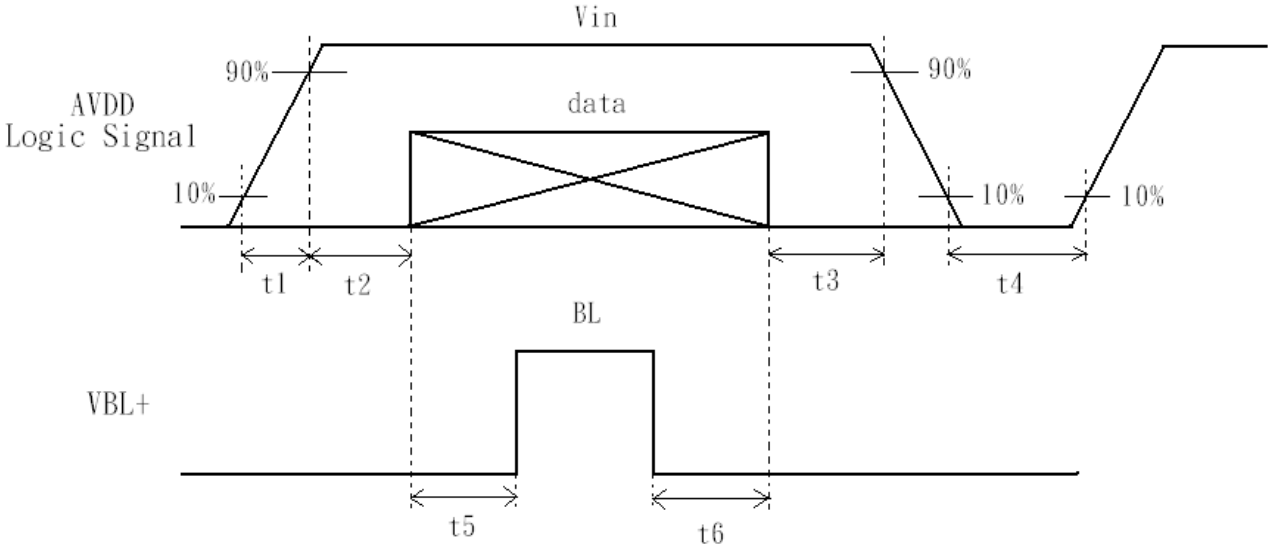
(A) TFT LCD

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
LCD Power Voltage		VCC	3.0	3.3	3.6	V	*1)
LCD Power Current		ICC	-	(303)	(363)	mA	*2)
Rush Current		Irush	-	-	3	A	*4)
Logic Input Voltage (LVDS: IN+,IN-)	Common Voltage	VCM	1.125	1.25	1.375	V	*3)
	Differential Input Voltage	| VID |	250	350	450	mV	*3)
	Threshold Voltage (HIGH)	VTH	-	-	100	mV	*3) When VCM = +1.2V
	Threshold Voltage (LOW)	VTL	-100	-	-	mV	

【Note】

*1) Power Sequence：

0.50 ms≦t1 ≦10 ms　　500 ms≦t4
0.01 ms＜t2 ≦50 ms　　200 ms≦t5
0.01 ms＜t3 ≦50 ms　　200 ms≦t6

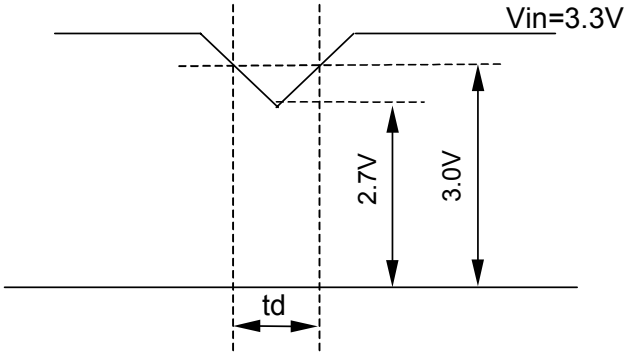


data:RGB DATA, DCLK, HD, VD, DENA

VCC-dip state

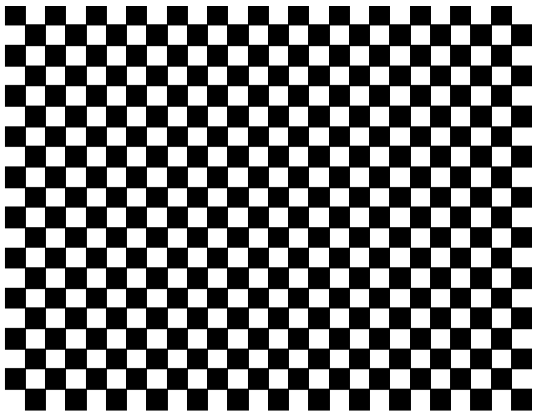
(1)when 3.0＞VCC≧2.7V & td≦10 ms.

(2)when VCC＜2.7V，VCC-dip condition should be the same as the VCC-turn-off condition.



【Note 2】Typical value is Mosaic (32*36 Checker board) Pattern：768 line mode。

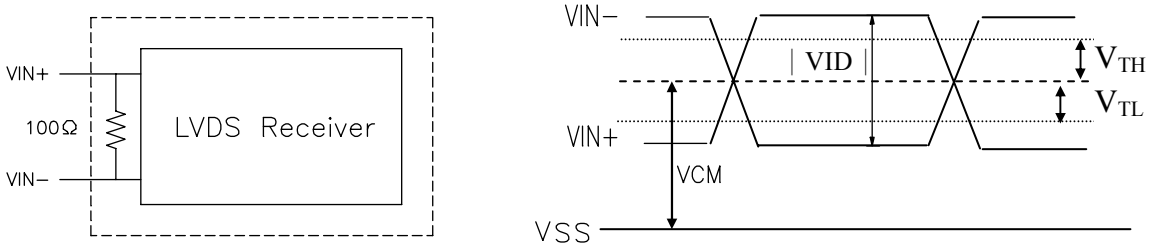
Circuit condition (typ)：VCC=3.3 V，fV=60 Hz fH=48.36 kHz，fCLK=75.44 MHz



Max value is Black Pattern：768 line mode。

Circuit condition (max)：VCC=3.3 V，fV=60 Hz fH=48.36 kHz，fCLK=75.44 MHz

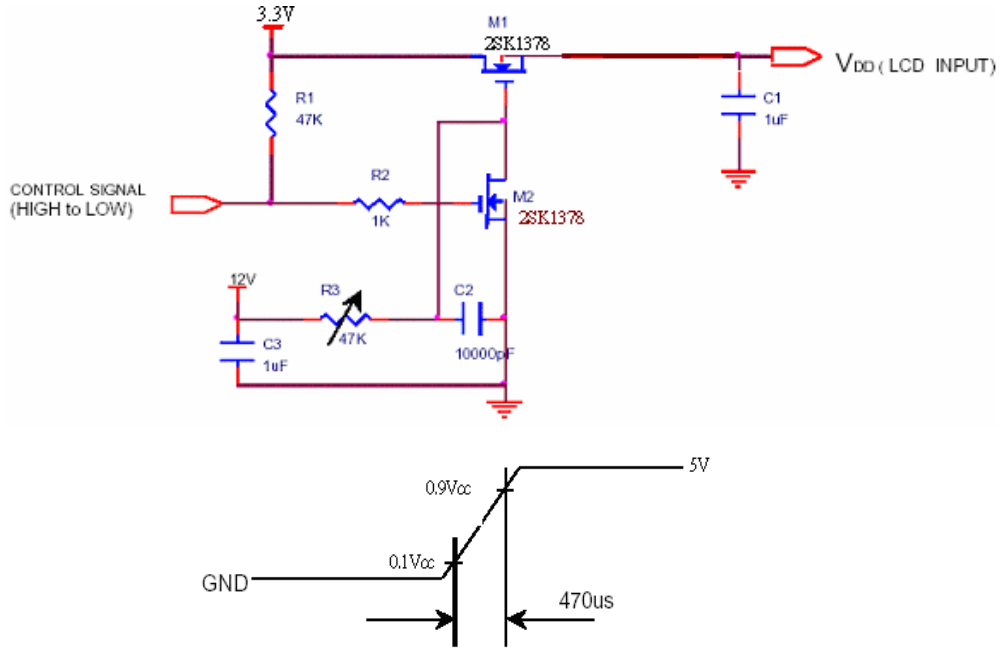
【Note 3】LVDS Signal Definite：



VIN+： Positive differential DATA & CLK Input

VIN-： Negative differential DATA & CLK Input

【Note 4】Irush measure condition



(2) BACK LIGHT

(a.) ELECTRICAL CHARACTERISTICS

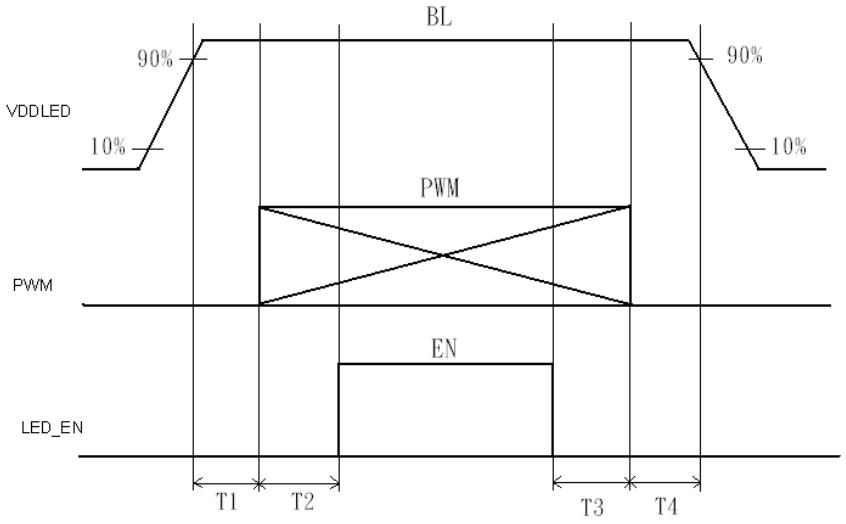
Ta=25℃

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
LED Driver Input Voltage	VBL+	7	12	21	V	
LED Driver Input Current	IBL+			(650)	mA	*1)
Forward Voltage	V_F	2.9	3.2	3.5	V	*2) I_F=20mA
Forward Current	I_F	19.5	20	20.5	mA	*2)
Power Consumption	PLED	2.46	2.71	2.96	W	*2)*3) I_F=20mA
PWM Frequency	PWM_BL	180	200	1k	Hz	*2)I_F=20mA
Duty ratio	Dim	10		100	%	

(b.) LED LIFE – TIME

ITEM	Condition	min	typ	max	UNIT	REMARK
LIFE TIME	IF=20mA、Ta=25℃	15000			hrs	*2)

(c.)LED ON/OFF Sequence：



10ms ≦ T1　　0ms ≦ T3

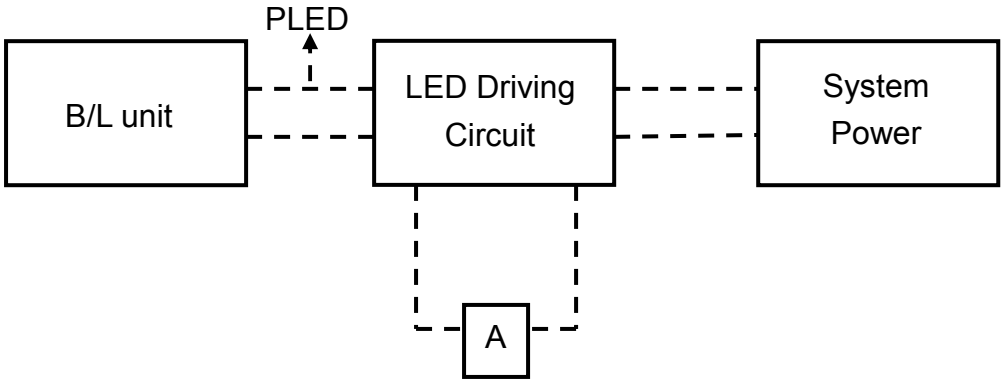
10ms ≦ T2　　10ms ≦ T4

Note：The duty of LED dimming signal should be more than 20% in T2 and T3

*1) Maximum LED Driver Input Current at 7V Input Voltage/PWM Duty 100%.

*2) Measure method：a. LED current is measured by utilizing a current meter as show below.

b. System power PLED is measured at input voltage 12V.



*3) Calculator value for reference $I_F \times V_F \times N = PLED$

*4) Life time means that estimated time to 50% degradation of initial luminous intensity.

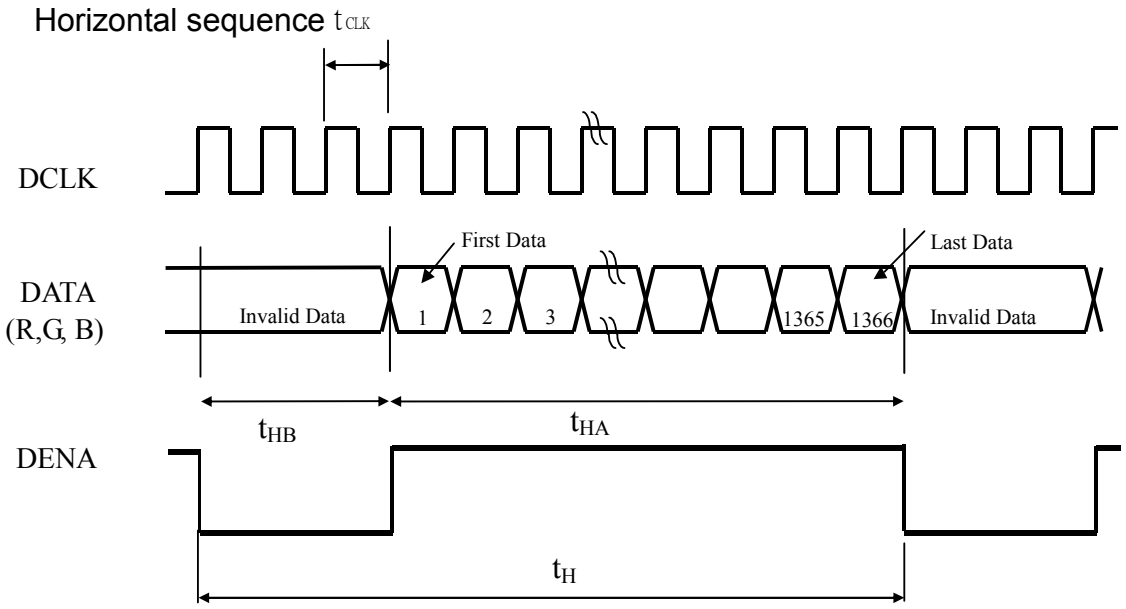
4. Connector Interface PIN & Function

CN (Interface signal) Outlet connector: LCEDI 5-2069716-3 (Tyco)

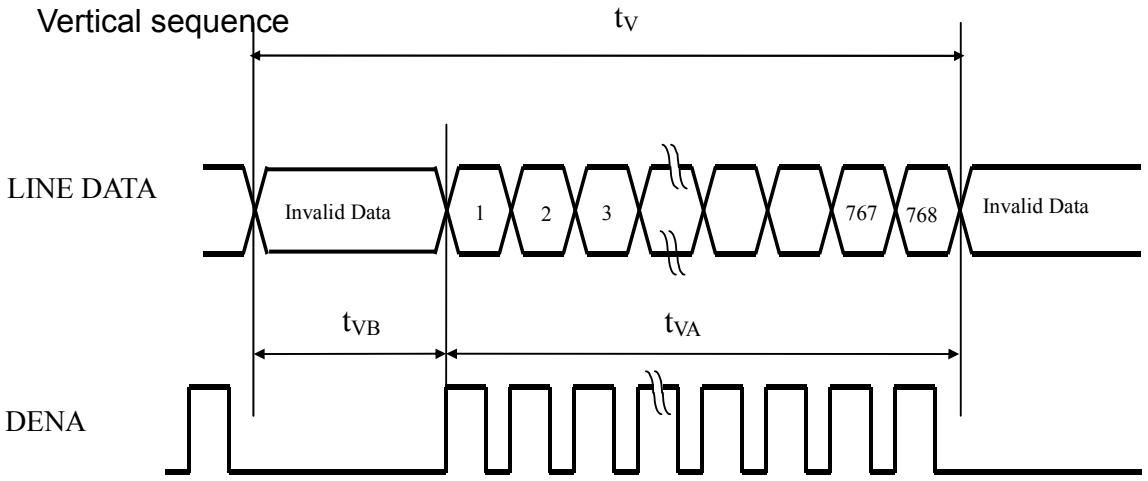
Pin No.	SYMBOL	FUNCTION
1	NC	No Connect
2	AVDD	Power Supply, 3.3 V (typical)
3	AVDD	Power Supply, 3.3 V (typical)
4	DVDD	DDC 3.3V power
5	NC	No Connect
6	SCL	DDC Clock
7	SDA	DDC Data
8	Rin0-	- LVDS differential data input (R0-R5, G0)
9	Rin0+	+ LVDS differential data input (R0-R5, G0)
10	GND	Ground
11	Rin1-	- LVDS differential data input (G1-G5, B0-B1)
12	Rin1+	+ LVDS differential data input (G1-G5, B0-B1)
13	GND	Ground
14	Rin2-	- LVDS differential data input (B2-B5, HS, VS, DE)
15	Rin2+	+ LVDS differential data input (B2-B5, HS, VS, DE)
16	GND	Ground
17	ClkIN-	- LVDS differential clock input
18	ClkIN+	+ LVDS differential clock input
19	NC	No Connect
20	NC	No Connect
21	NC	No Connect
22	GND	Ground
23	NC	No Connect
24	NC	No Connect
25	GND	Ground
26	NC	No Connect
27	NC	No Connect
28	GND	Ground
29	NC	No Connect
30	NC	No Connect
31	VBL-	Ground – LED
32	VBL-	Ground – LED
33	VBL-	Ground – LED
34	NC	No Connect
35	BLIM	System PWM Signal Input (+3.3V Swing)
36	BL_Enable	LED enable pin (+3.3V Input)
37	NC	No Connect
38	VBL+	7V – 21V LED power
39	VBL+	7V – 21V LED power
40	VBL+	7V – 21V LED power

5. INTERFACE TIMING CHART

(1)(a) LVDS input time sequence



(b) LCD input time sequence



(2) Timing Chart

ITEM				SYNBOL	MIN	TYP	MAX	UNIT
LCD Timing	Frame Rate			-	(55)	60	(60)	Hz
	DCLK		Frequency	f_{CLK}	(65.93)	75.44	(80)	MHz
			Period	t_{CLK}	(11.26)	13.25	(15.17)	ns
	DENA	Horizontal	Horizontal total time	t_H	(1498)	1560	(1667)	t_{CLK}
			Horizontal Active time	t_{HA}	1366	1366	1366	t_{CLK}
			Horizontal Blank time	t_{HB}	(132)	194	(301)	t_{CLK}
		Vertical	Vertical total time	t_V	(800)	806	(870)	t_H
			Vertical Active time	t_{VA}	768	768	768	t_H
			Vertical Blank time	t_{VB}	(32)	38	(102)	t_H
LVDS spread spectrum range *3)					-2		2	%

【Note】

*1) DENA (DATA ENABLE) usually is positive.

*2) During the whole blank period, DCLK should keep input.

*3) LVDS input clock is 85MHz and modulation rate is fixed 100KHz

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(3) DATA mapping

Color	Input Data	R DATA						G DATA						B DATA					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Green(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

【Note】

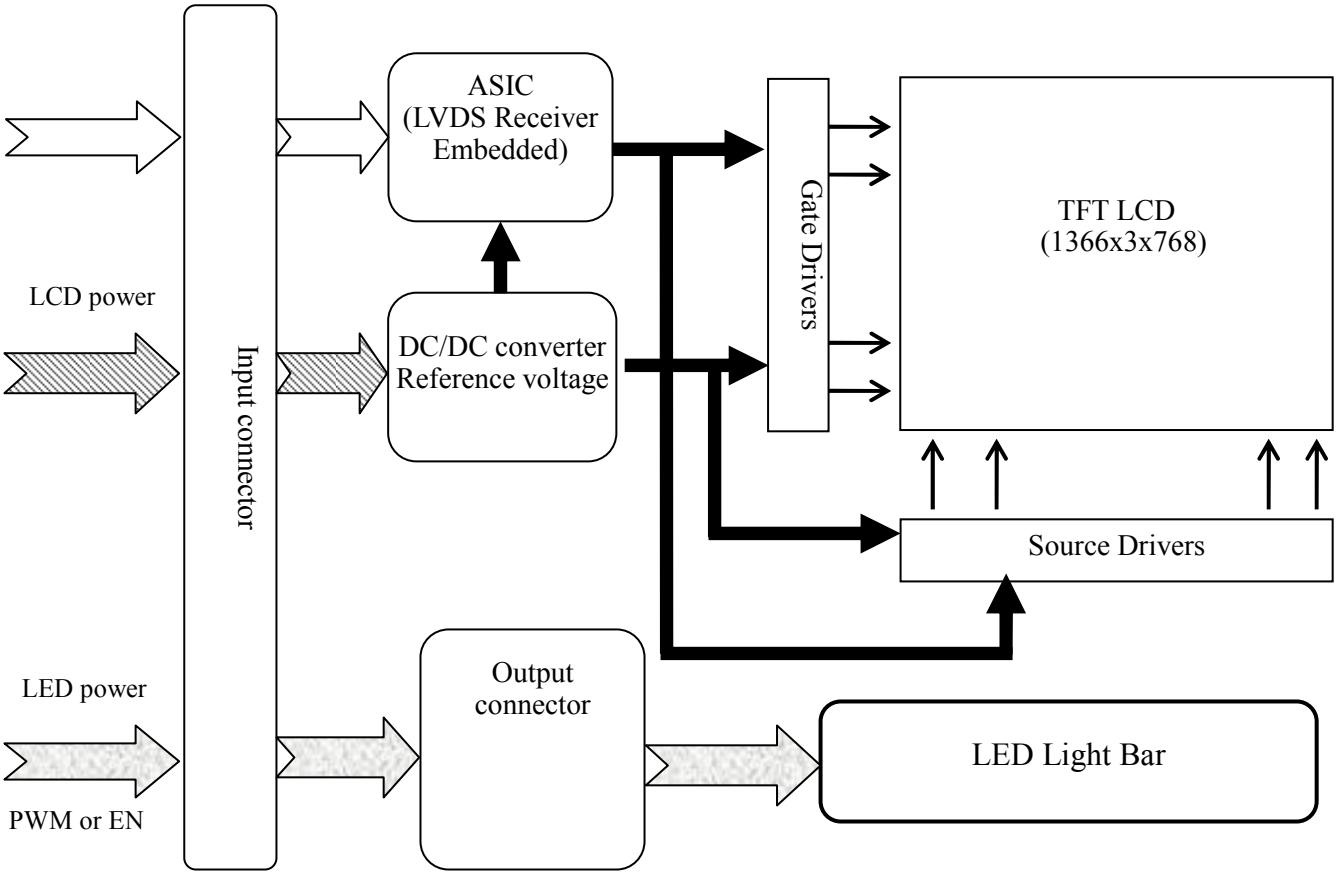
1) Gray level:

Color(n)：n is level order; higher n means brighter level.

2) DATA:

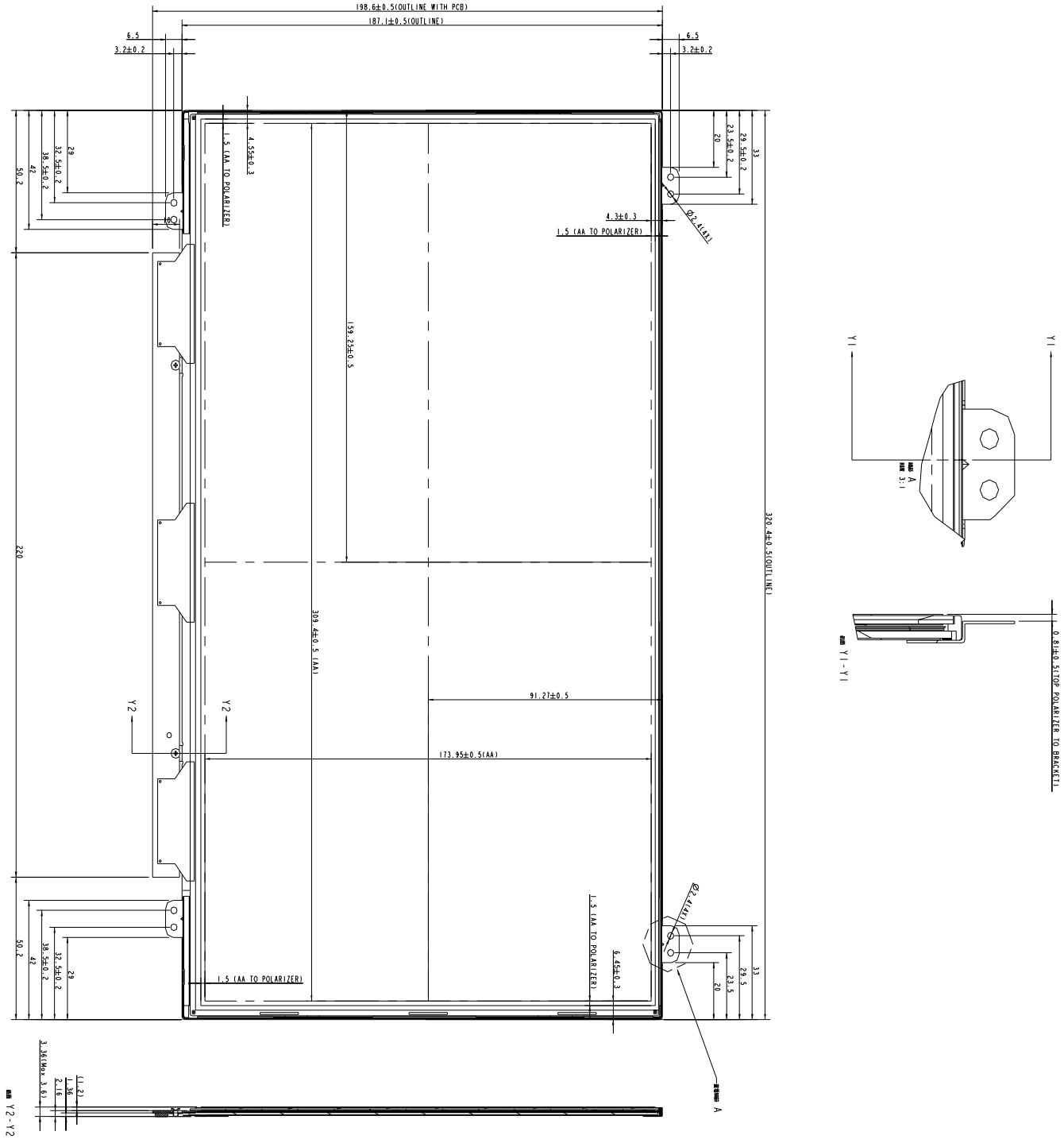
1: high，0: low

6. BLOCK DIAGRAM



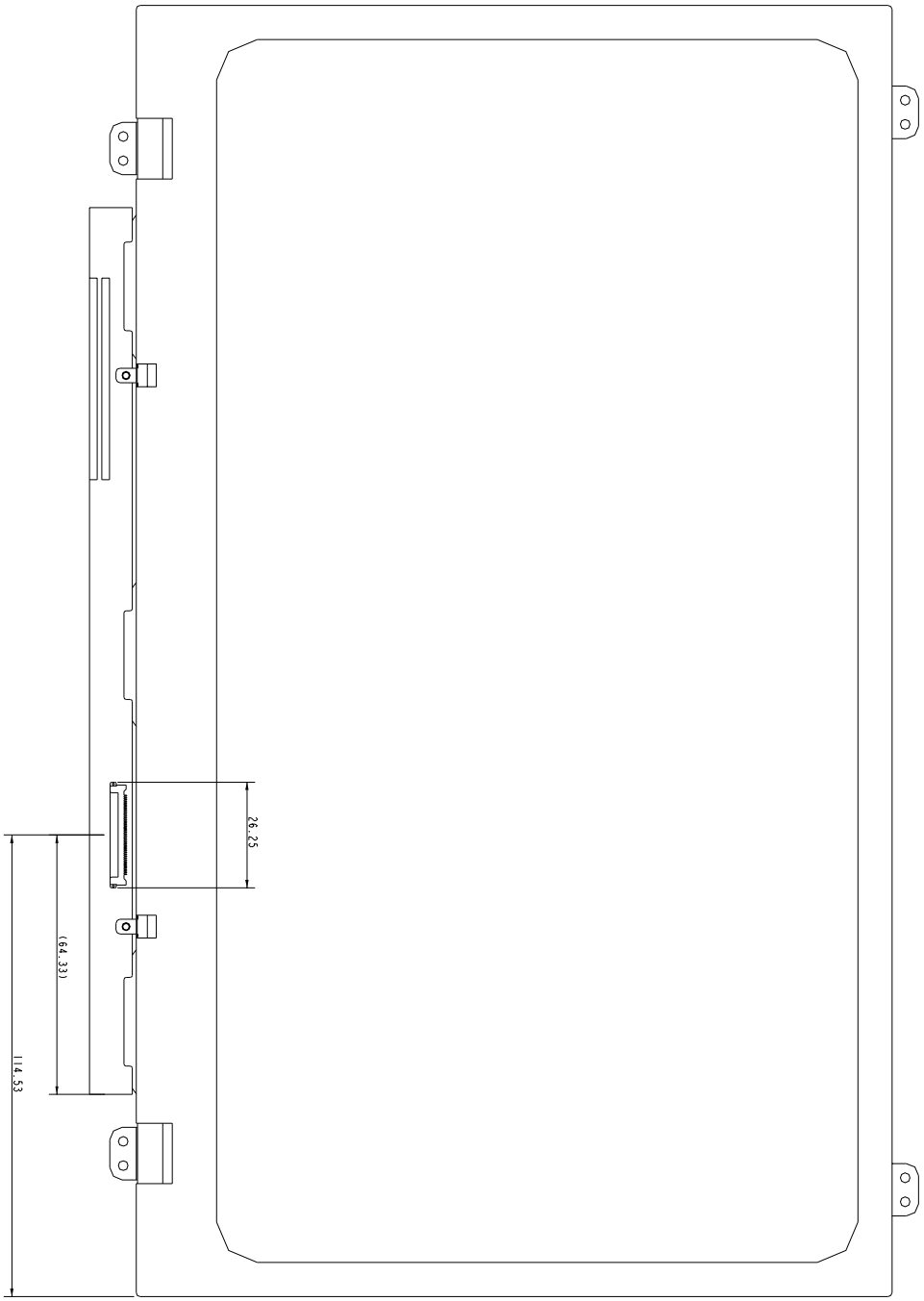
7. MECHANICAL SPECIFICATION

(1) The tolerance, not show in the figure, is ±0.5 mm. **[Unit：mm]**



(2) Rear side

The tolerance, not show in the figure, is ±0.5 mm. **[Unit：mm]**



8. OPTICAL CHARACTERISTICS

Ta=25℃，VDD=3.3V

ITEM		SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	NOTE
Contrast Ratio		CR	θ=ψ= 0°	500	600	-	--	*1) 2)
Luminance (5P)		L	θ=ψ= 0°	170	200	-	cd/m^2	*1) 3)
Response Time		Tr	θ=ψ= 0°	-	3	6	ms	*5)
		Tf	θ=ψ= 0°	-	5	10	ms	*5)
Cross Talk		CT	θ=ψ= 0°	-	-	1	%	*6)
View Angle	**Horizontal**	ψ	CR≧10	40/-40	-	-	°	*4)
	Vertical	θ		15/-30	-	-	°	
Color Temperature Coordinate	**W**	X	θ=ψ= 0°	(0.283)	0.313	(0.343)		*3)
		Y		(0.299)	0.329	(0.359)		
	R	X		(0.550)	(0.580)	(0.610)		
		Y		(0.310)	(0.340)	(0.370)		
	G	X		(0.280)	(0.310)	(0.340)		
		Y		(0.520)	(0.550)	(0.580)		
	B	X		(0.125)	(0.155)	(0.185)		
		Y		(0.095)	(0.125)	(0.155)		
Gamut			θ=ψ= 0°	42%	45%	-		
Gamma		γ	GL	2.0	2.2	2.4		*7)

Color coordinate and color gamut are measured by SRUL1R, response time is measured by TRD-100, and all the other items are measured by BM-5A (TOPCON). All these items are measured under the dark room condition (no ambient light).

Measurement Condition: IL= 20mA (each LED)

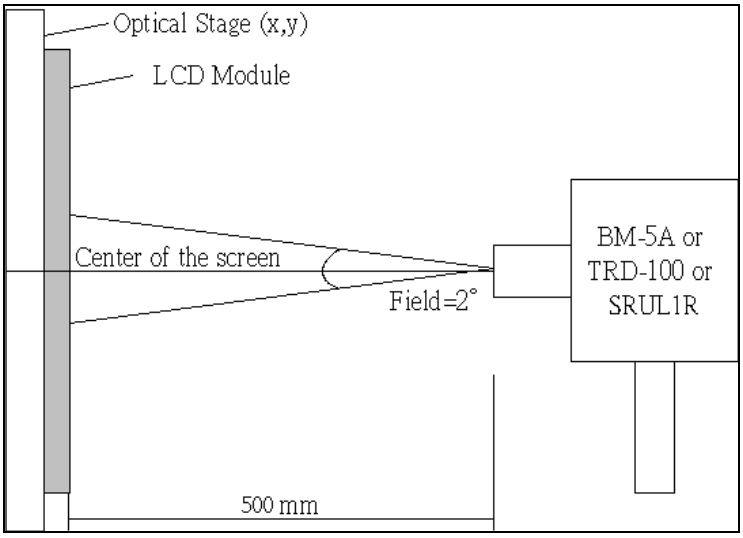
Definition of these measurement items is as follows:

***1) Setup of Measurement Equipment**

The LCD module should be turn-on to a stable luminance level to be reached. The measurement should be executed after lighting Backlight for 20 minutes and in a dark room.

***2) Definition of Contrast Ratio**

CR=ON (White) Luminance/OFF (Black) Luminance



***3) Definition of Luminance and Luminance uniformity**

Central luminance: The white luminance is measured at the center position “5” on the screen, see Fig.1 below.

5P Luminance (AVG): The white luminance is measured at measuring points 5、10、11、12、13, see Fig.1 below.

5P Uniformity: Δ L = (Lmin / Lmax) ×100%

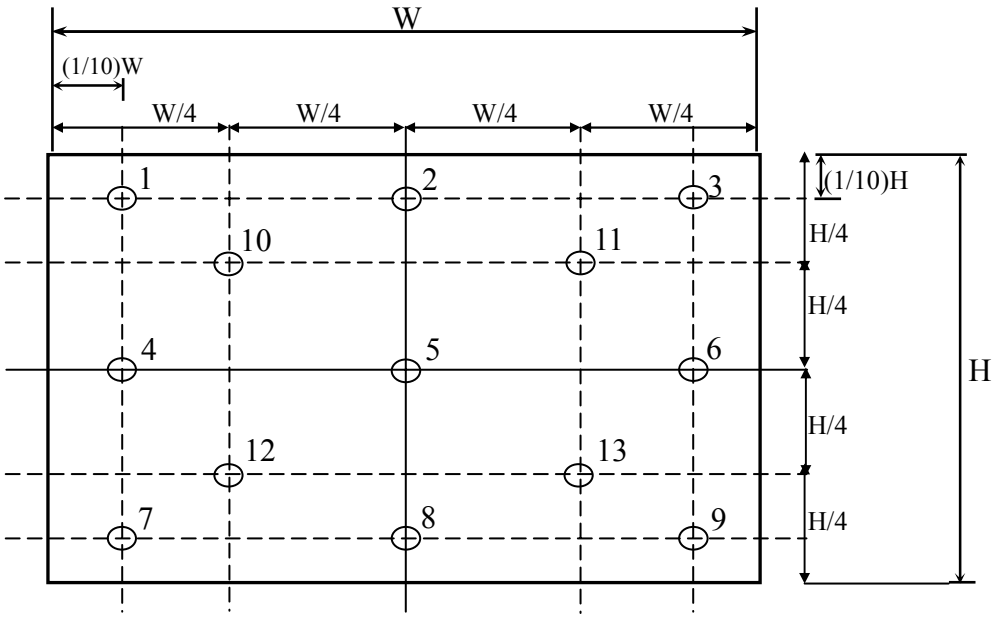
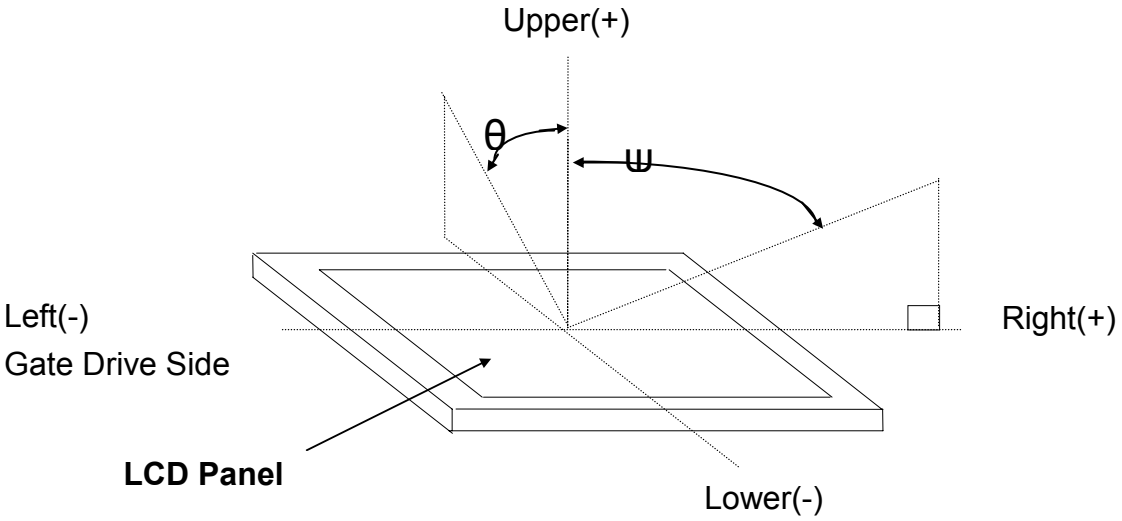
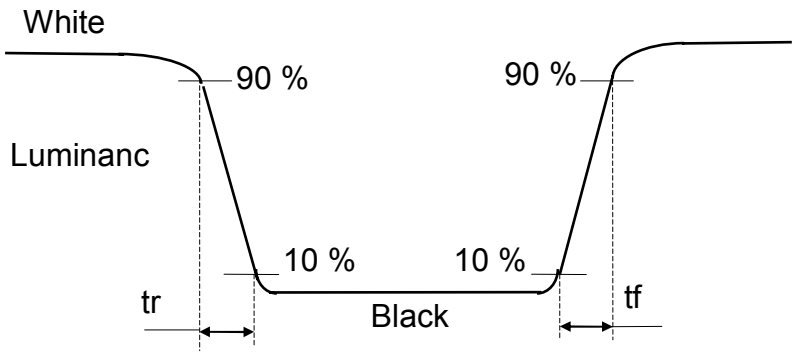


Fig.1 Measure point (Active area)

***4) Definition of view angle(θ，ψ)**



***5) Definition of response time**



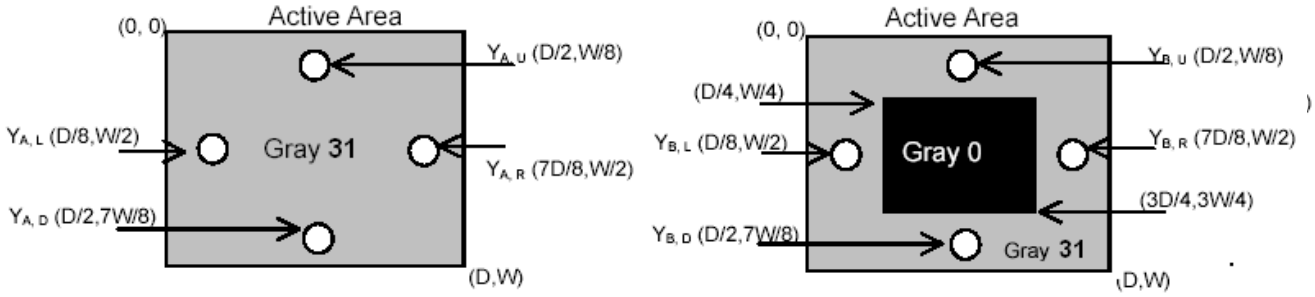
***6) Crosstalk Modulation Ratio:**

CT= | Y_B-Y_A | / Y_A× 100%

Y_A 、Y_B measure position and definition

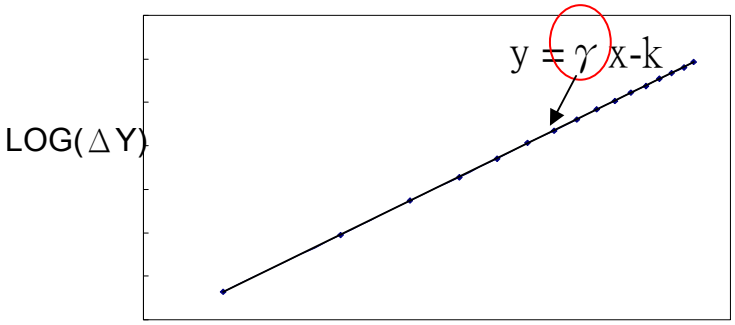
Y_A means luminance at gray level 32(exclude gray level 0 pattern)

Y_B means luminance at gray level 32(include gray level 0 pattern)



***7) Definition Gamma (VESA)**

Based on Customer Sample, take the average value as a standard center value and the variation range of gamma value caused by loop voltage error should be between +/- 0.2. the bellow figure shows how to obtain the gamma curve and γ (from gray level: 0、4、8-----60、63)..



LOG（Gray Level）

9. RELIABILITY TEST CONDITIONS

9.1 Temperature and Humidity:

(LCD also need to meet Sony LCD Note PC LCD Reliability Standard)

TEST ITEMS	CONDITIONS
High Temperature Operation	50°C ; 240Hrs
High Temperature Storage	60°C ; 240Hrs
High Temperature High Humidity Operation	50°C ; 90% RH ; 240Hrs
High Temperature High Humidity Storage	60°C ; 90% RH ; 48 Hrs
Low Temperature Operation	0°C ; 240 Hrs
Low Temperature Storage	-20°C ; 240 Hrs
Thermal Shock	-20°C (0.5 hr)～60°C (0.5 hr)， Ramp<20℃，100 CYCLE
Temperature & Pressure Storage	25°C ; 260hPa(about 10000m)，24 Hrs

9.2 Shock & Vibration:

TEST ITEMS	CONDITIONS
Shock (Operating)	120G 3msec Half sin wave ±X, ±Y, ±Z, 1 time each
Shock (Non-Operating)	210G 3msec Half sin wave ±X, ±Y, ±Z, 1 time each
Vibration (Operating)	Random 1.1Grms 5-50Hz 0.024G2/Hz 50-100Hz -36dB/oct X,Y,Z 20min total 60 min
Vibration (Non-Operating)	Random 2.3Grms 5-50Hz 0.011G2/Hz 50-100Hz -36dB/oct X,Y,Z 20min total 60 min

9.3 ESD:

ITEMS	CONDITIONS
Module	Air ±15KV,150pF，330Ω， 1.Under test conditions, by using air-mode to discharge each test point 25 times continueously and then check the module frame. 2. Under test conditions, by using contact-mode to discharge each test point of panel frame 25 times continueously and then check the module frame.
Connector	200 pF，0 Ω，±250 V By using contact-mode to discharge each pin one time and then check the module frame.

9.4 MTBF without B/L : 200,000 Hrs(min) life time.

9.5 Judgment standard

The judgment of the above test should be made as follow:

Pass：Normal display image with no obvious non-uniformity and no line defect.

Partial transformation of the module parts should be ignored.

Fail：No display image, obvious non-uniformity, or line defects.

10. HANDLING PRECAUTIONS FOR TFT-LCD MODULE

Please pay attention to the followings in handling- TFT-LCD products.

10.1 ASSEMBLY PRECAUTION

(1) Please use the mounting hole on the module side in installing and do not beading or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.

(2) Please design display housing in accordance with the following guidelines.

- Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
- Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. Approximately 1.0 mm of the clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
- When some parts, such as, FPC cable and ferrite plate, are installed underneath the LCD module, still sufficient clearance is required, such as 0.5mm. This clearance is, especially, to be reconsidered when the additional parts are implemented for EMI countermeasure.
- Keep sufficient clearance between LCD module and the others parts, such as speaker so as not to interface the LCD module. Approximately 1.0mm of the clearance in the design is recommended.

(3) Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)

(4) Please do not press any parts on the rear side such as source TCP, gate TCP, control circuit board and FPCs during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.

(5) Please wipe out LCD panel surface with absorbent cotton or soft clothe in case of it being soiled.

(6) Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change.

(7) Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.

(8) Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.

10.2 OPERATING PRECAUTIONS

(1) Please be sure to turn off the power supply before connecting and disconnecting signal input cable.

(2) Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification.

(3) Please consider that LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.

(4) A condensation might happen on the surface and inside of LCD module in case of sudden change of ambient temperature.

(5) Please pay attention to displaying the same pattern for very long time. Image might stick on LCD. If then, time going on can make LCD work well.

(6) Please obey the same caution descriptions as ones that need to pay attention to ordinary electronic parts.

10.3 PRECAUTIONS WITH ELECTROSTATICS

(1) This LCD module use CMOS-IC on circuit board and TFT-LCD panel, and so it is easy to be affected by electrostatics. Please be careful with electrostatics by the way of your body connecting to the ground and so on.

(2) Please remove protection film very slowly on the surface of LCD module to prevent from electrostatics occurrence.

10.4 STORAGE PRECAUTIONS

(1) When you store LCDs for a long time, it is recommended to keep the temperature between 0℃ ~40℃ without the exposure of sunlight and to keep the humidity less than 90%RH.

(2) Please do not leave the LCDs in the environment of high humidity and high temperature such as 60℃ 90%RH.

(3) Please do not leave the LCDs in the environment of low temperature; below -20℃.

10.5 SAFETY PRECAUTIONS

(1) When you waste LCDs, it is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.

(2) If any liquid leaks out of a damaged-glass cell and comes in contact with the hands, wash off throughly with soap and water.

10.6 OTHERS

(1) A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight Land strong UV rays.

(2) Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.

(3) For the packaging box, please pay attention to the followings:

- Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
- Please do not pile them up more than 3 boxes. (They are not designed so.) And please do not turn over.
- Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.

Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)