

CLAMP2504N

Low Capacitance Array for Surge & ESD Protection

The CLAMP2504N has a low typical capacitance of 2.4pF and operates with virtually no insertion loss to 1GHz. This makes the device ideal for protection of high-speed data lines such as Firewire, DVI, and gigabit Ethernet interfaces. The low capacitance array configuration allows the user to protect four high-speed data or transmission lines. The low inductance construction minimizes voltage overshoot during high current surges. It may be used to meet the ESD immunity requirements of IEC61000-4-2, Level 4 (±15kV air, ±8kV contact discharge).

It has been specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from overvoltage caused by ESD(electrostatic discharge), CDE (Cable Discharge Events), and lightning.

Features

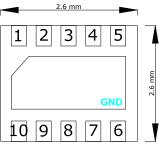
- Protects four I/O lines and one Vcc line
- Low capacitance
- Working voltages: 2.5V
- Low leakage current
- Response Time is < 1 ns
- Low capacitance (<5.0pF) for high-speed interfaces
- Meets MSL 3 Requirements
- Solid-state silicon avalanche technology
- ROHS compliant
- WeiPan technology

Main applications

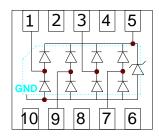
- Digital Visual Interface (DVI)
- 10/100/1000 Ethernet
- IEEE 1394 Firewire Ports
- T1/E1 Secondary Protection
- T3/E3 Secondary Protection
- Projection TV Monitors and Flat Panel Displays
- Notebook Computers
- Projection TV

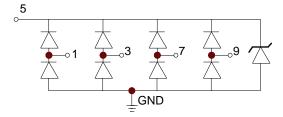
Protection solution to meet

- IEC61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
- IEC61000-4-4 (EFT) 40A (5/50ns)
- IEC61000-4-5 (Lightning) 17A (8/20 μs)



DFN2626-10L





Ordering Information

Device	Qty per Reel	Reel Size
CLAMP2504N	3000	7 Inch

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CLAMP2504N Low Capacitance Array for Surge & ESD Protection

Maximum ratings (Temp=25℃ Unless Otherwise Specified)						
Parameter	Symbol	Value	Unit			
Peak Pulse Power (tp=8/20μs waveform)	Ррр	300	Watts			
Peak Pulse Current(tp=8/20μs waveform)	IPP	17	A			
ESD Rating per IEC61000-4-2: Contact		8	WW			
Air		15	KV			
Lead Soldering Temperature	$T_{\rm L}$	260 (10 sec.)	${\mathbb C}$			
Operating Temperature Range	ΤJ	-55 ~ 150	${\mathbb C}$			
Storage Temperature Range	Tstg	-55 ~ 150	$^{\circ}$			

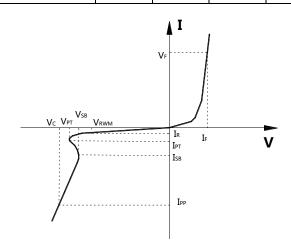
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

^{1.} Non-repetitive current pulse, per Figure 1.

Electrical characteristics (Temp=25°C Unless Otherwise Specified)							
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units	
V _{RWM}	Reverse Working Voltage	Any I/O to Ground			2.5	V	
V_{PT}	Punch-Through Voltage	IPT = 2uA, Any I/O to Ground				V	
VsB	Snap-Back Voltage	Isb = 50mA, Any I/O to Ground	2.0			V	
IR	Reverse Leakage Current	$V_{RWM} = 2.5V,$ Any I/O to Ground			0.5	μΑ	
Vc		$I_{PP} = 1A$, tp =8/20 μ s, any I/O pin to Ground			5.5	V	
	Clamping Voltage	$I_{PP} = 15A$, tp =8/20 μ s, any I/O pin to Ground			15	V	
I_{PP}	Peak Pulse Current	tp =8/20μs			17	A	
C _J	Junction Capacitance	$V_R = 0V$, $f = 1MHz$, between I/O pins		1.3	2.7	pF	
		$V_R = 0V$, $f = 1MHz$, any I/O pin to Ground		2.4	5	pF	

Junction capacitance is measured in VR=0V, F=1MHz

Symbol	Parameter		
V_{RWM}	Working Peak Reverse Voltage		
V_{PT}	Punch-Through Voltage@ I _{PT}		
$ m V_{SB}$	Snap-Back Voltage@ I _{SB}		
$V_{\rm C}$	Clamping Voltage @ I _{PP}		
I_{T}	Test Current		
I_{RM}	Leakage current at V _{RWM}		
I_{PP}	Peak pulse current		
C_J	Junction Capacitance		

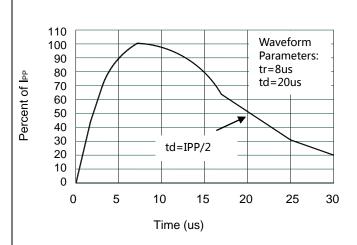


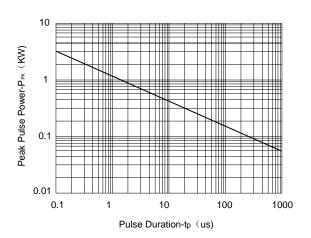
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^{*}Other voltages may be available upon request.

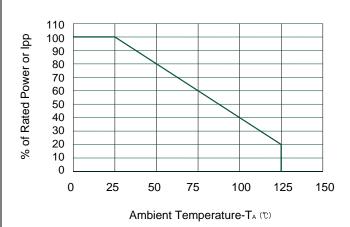
Typical electrical characteristic applications

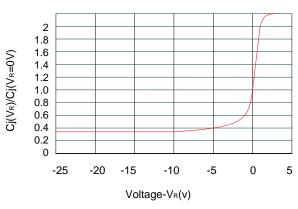




Pulse Waveform

Non-Repetitive Peak Pulse Power vs. Pulse Time





Power Derating Curve

1, -0.7540 dB 900MHz
2, -1.9718 dB 1.7GHz
3, -2.8512 dB 2.5GHz
4, -4.3316 dB 2.7GHz

Junction Capacitances. Reverse Voltage

Insertion Loss S21

1 MHz 10 MHz

0

-6 -12 -18 -24 -30 -36

Insertion Loss (dB)

3 GHz

1 GHz

CLAWII 2504IV

Typical applications

10/100/1000 **Ethernet PHY** TX+ **RJ45 Connector** Unused 1 2 3 4 5 TX-Unused TX+ TX-10 9 8 7 6 RX+ RX-RX+ Unused Unused 75 75 RX-

10/100/1000 Ethernet Protection

Considerations:

VCC

GND

- Some Ethernet ports only need be protected for ESD and not for lightning induced transients
 - These are sometimes referred to as "2M" ports or 2 Meter ports that have very short CAT5 cable installations
- Parasitic capacitance should be taken into account especially for 1GbE
- The 4 data lines below (Tx± and Rx±) are being protected against ESD by a low capacitance CLAMP2504N which is suitable for all Ethernet data rates
 - In fact, any low capacitance CLAMP2504N device is suitable for any "ESD only" Ethernet application
- 1000Mbps Ethernet (or 1GbE) will require 8 channels of protection for the 4 differential pair so the below scheme should be replicated for the remaining 2 data pair

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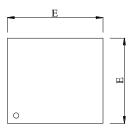
Package Information

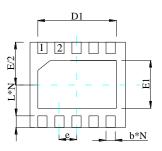
DFN2626-10L

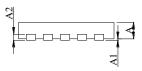
Mechanical Data

Case:DFN2626-10L

Case Material: Molded Plastic. UL Flammability

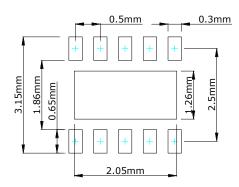




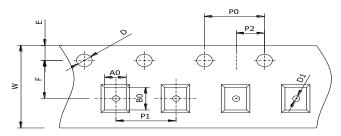


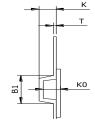
DIM	Millimeters		Inches		
	Min	Max	Min	Max	
A	0.5	0.60	0.020	0.024	
A1	0.05		0.002		
A2	0.15REF		0.00)6REF	
b	0.20	0.30	0.008	0.012	
D1	2.00	2.25	0.079	0.089	
E	2.55	2.60	0.100	0.102	
E1	1.11	1.36	0.044	0.054	
e	0.50BSC		0.020BSC		
L	0.25	0.45	0.010	0.018	
N	10		10		

Recommended Pad outline



DFN2626-10L Reel Dim





Package	Chip Size (mm)	Pocket Size B0×A0×K0(mm)	Tape Width	Reel Diameter	Quantity Per Reel	P0	P1
DFN2626-10L	2.6×2.6×0.65	2.80×2.80×0.85	8mm	178mm(7")	3000	4mm	4mm
D0	D1	Е	F	K	Т	W	
1.5mm	0.5mm	1.75mm	3.5mm	0.65mm	0.2mm	8mm	

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