

<IGBT Modules>

# CM1000DX-24T/CM1000DXP-24T

HIGH POWER SWITCHING USE **INSULATED TYPE** 

|     | the should be             | Collector current I <sub>c</sub> <b>1 0 0 0</b> A      |  |  |  |  |
|-----|---------------------------|--|--|--|--|--|
|     |                           | Collector-emitter voltage V <sub>CES</sub> 1 2 0 0 V   |  |  |  |  |
|     | •                         | Maximum junction temperature Tvjmax 175°C              |  |  |  |  |
| DX  |                           | ●Flat base type  |  |  |  |  |
|     |                           | <ul> <li>Copper base plate (Nickel-plating)</li> </ul> |  |  |  |  |
|     |                           | <ul> <li>RoHS Directive compliant</li> </ul>           |  |  |  |  |
|     |                           | <ul> <li>Tin-plating pin terminals</li> </ul>          |  |  |  |  |
|     |                           | Collector current Ic 1000A                             |  |  |  |  |
|     |                           | Collector-emitter voltage Vces 1 2 0 0 V               |  |  |  |  |
|     | -                         | Maximum junction temperature T <sub>vjmax</sub> 175 °C |  |  |  |  |
| DXP |                           | ●Flat base type  |  |  |  |  |
|     |                           | <ul> <li>Copper base plate (Nickel-plating)</li> </ul> |  |  |  |  |
|     |                           | <ul> <li>RoHS Directive compliant</li> </ul>           |  |  |  |  |
|     |                           | <ul> <li>Tin-plating pressfit terminals</li> </ul>     |  |  |  |  |
|     | dual switch (half-bridge) | •UL Recognized under UL1557, File No. E323585          |  |  |  |  |

#### APPLICATION

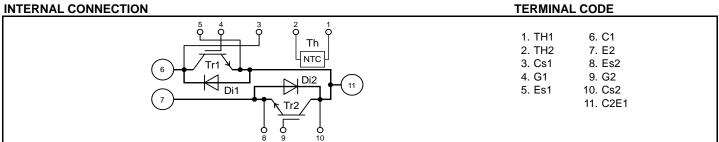
AC Motor Control, Motion/Servo Control, Power supply, etc.

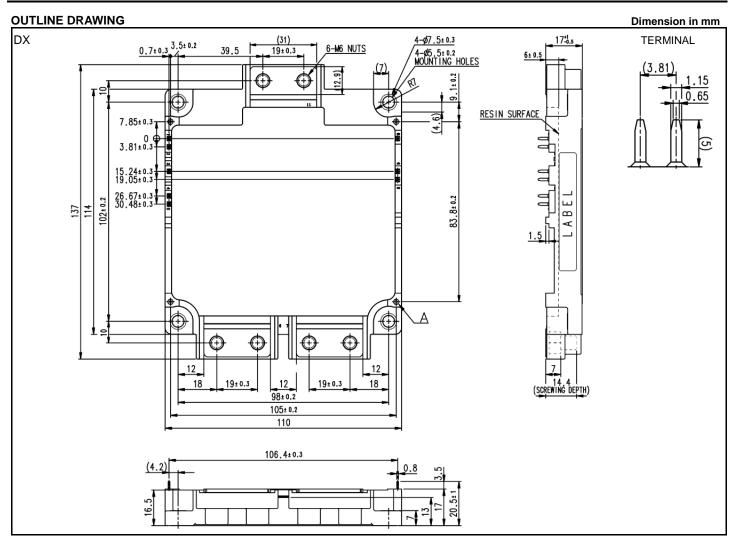
#### **OPTION (Below options are available.)**

•PC-TIM (Phase Change Thermal Interface Material) pre-apply

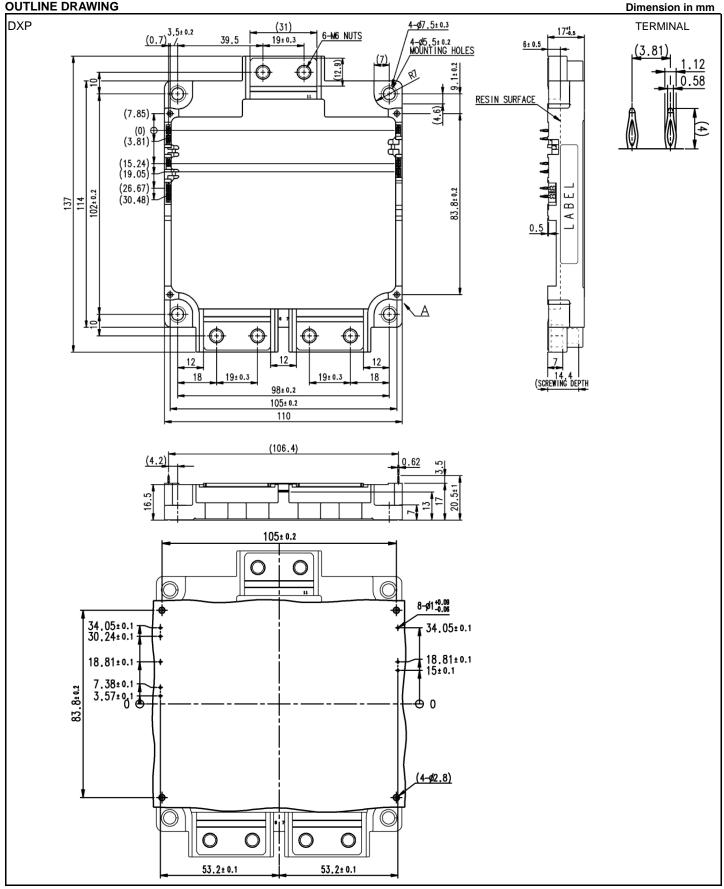
#### •V<sub>CEsat</sub> selection for parallel connection

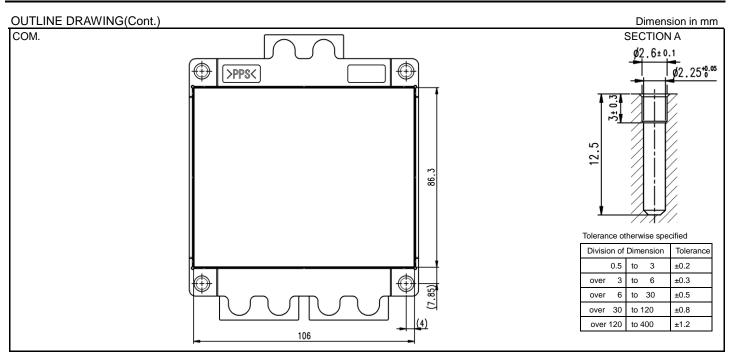
#### INTERNAL CONNECTION











# MAXIMUM RATINGS (T $_{vj}$ =25 °C, unless otherwise specified) INVERTER PART IGBT/FWD

| Symbol           | Item   | Conditions                            | Rating | Unit |  |
|------------------|--|---------------------------------------|--------|------|--|
| V <sub>CES</sub> | Collector-emitter voltage                                | G-E short-circuited                   | 1200   | V    |  |
| V <sub>GES</sub> | Gate-emitter voltage                                     | C-E short-circuited                   | ± 20   | V    |  |
| I <sub>C</sub>   | Collector current  | DC, T <sub>C</sub> =116 °C (Note2, 4) | 1000   | A    |  |
| I <sub>CRM</sub> |  | Pulse, Repetitive (Note3)             | 2000   |      |  |
| P <sub>tot</sub> | Total power dissipation T <sub>C</sub> =25 °C (Note2, 4) |                                       | 5355   | W    |  |
| IE (Note1)       | Emitter current  | DC (Note2)                            | 1000   | ^    |  |
| IERM (Note1)     | Emitter current  | Pulse, Repetitive (Note3)             | 2000   | A    |  |

MODULE

| MODULE             |                                |   |            |      |
|--------------------|--------------------------------|---|------------|------|
| Symbol             | Item                           | Conditions                                      | Rating     | Unit |
| Visol              | Isolation voltage              | Terminals to base plate, RMS, f=60 Hz, AC 1 min | 2500       | V    |
| T <sub>vjmax</sub> | Maximum junction temperature   | Instantaneous event (overload)                  | 175        | °C   |
| T <sub>Cmax</sub>  | Maximum case temperature       | (Note4)   | 125        |      |
| T <sub>vjop</sub>  | Operating junction temperature | Continuous operation (under switching)          | -40 ~ +150 | °C   |
| T <sub>stg</sub>   | Storage temperature            | -   | -40 ~ +125 | C    |

# ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified) INVERTER PART IGBT/FWD

| Symbol                             | ltem                                 | Conditions   |   |      | Limits |       | Unit   |
|------------------------------------|--------------------------------------|--|---|------|--------|-------|--|
| Symbol                             | nem                                  | Conditions   |   | Min. | Тур.   | Max.  | Unit   |
| I <sub>CES</sub>                   | Collector-emitter cut-off current    | $V_{CE}=V_{CES}$ , G-E short-circuited   |   | -    | -      | 1.0   | mA   |
| I <sub>GES</sub>                   | Gate-emitter leakage current         | V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited  |   | -    | -      | 0.5   | μA   |
| $V_{\text{GE(th)}}$                | Gate-emitter threshold voltage       | I <sub>C</sub> =100 mA, V <sub>CE</sub> =10 V  | I <sub>C</sub> =100 mA, V <sub>CE</sub> =10 V |      | 6.0    | 6.6   | V  |
|                                    |                                      | I <sub>C</sub> =1000 A, V <sub>GE</sub> =15 V,   | T <sub>vj</sub> =25 °C                        | -    | 1.55   | 1.95  |  |
| V <sub>CEsat</sub>                 |                                      | Refer to the figure of test circuit  | T <sub>vj</sub> =125 °C                       | -    | 1.70   | -     | V  |
| (Terminal)                         |                                      | (Note5)  | T <sub>vj</sub> =150 °C                       | -    | 1.75   | -     | 1  |
|                                    | Collector-emitter saturation voltage | I <sub>C</sub> =1000 A,  | T <sub>vj</sub> =25 °C                        | -    | 1.50   | 1.75  |  |
| V <sub>CEsat</sub>                 |                                      | V <sub>GE</sub> =15 V,   | T <sub>vj</sub> =125 °C                       | -    | 1.70   | -     | V  |
| (Chip)                             |                                      | (Note5)  | T <sub>vj</sub> =150 °C                       | -    | 1.75   | -     | 1  |
| Cies                               | Input capacitance                    |  |   | -    | -      | 242.5 |  |
| C <sub>oes</sub>                   | Output capacitance                   | V <sub>CE</sub> =10 V, G-E short-circuited   |   | -    | -      | 6.8   | nF   |
| Cres                               | Reverse transfer capacitance         |  |   | -    | -      | 3.0   |  |
| Q <sub>G</sub>                     | Gate charge                          | V <sub>CC</sub> =600 V, I <sub>C</sub> =1000 A, V <sub>GE</sub> =15 V                              |   | -    | 7.5    | -     | μC   |
| t <sub>d(on)</sub>                 | Turn-on delay time                   | $V_{cc}$ =600 V, I <sub>c</sub> =1000 A, $V_{GE}$ =±15 V,<br>R <sub>G</sub> =2.0 Ω, Inductive load |   | -    | -      | 800   | - ns   |
| tr                                 | Rise time                            |  |   | -    | -      | 400   |  |
| t <sub>d(off)</sub>                | Turn-off delay time                  |  |   | -    | -      | 1300  |  |
| t <sub>f</sub>                     | Fall time                            |  |   | -    | -      | 400   |  |
|                                    |                                      | I <sub>E</sub> =1000 A, G-E short-circuited,   | T <sub>vi</sub> =25 °C                        | -    | 1.65   | 2.15  |  |
| V <sub>EC</sub> <sup>(Note1)</sup> |                                      | Refer to the figure of test circuit  | T <sub>vi</sub> =125 °C                       | -    | 1.75   | -     | v  |
| (Terminal)                         |                                      | (Note5)  | T <sub>vi</sub> =150 °C                       | -    | 1.80   | -     | 1  |
|                                    | Emitter-collector voltage            | I <sub>E</sub> =1000 A,  | T <sub>vi</sub> =25 °C                        | -    | 1.60   | 1.95  |  |
| V <sub>EC</sub> <sup>(Note1)</sup> |                                      | G-E short-circuited,   | T <sub>vi</sub> =125 °C                       | -    | 1.60   | -     | v  |
| (Chip)                             |                                      | (Note5)  | T <sub>vi</sub> =150 °C                       | -    | 1.60   | -     | 1  |
| t <sub>rr</sub> (Note1)            | Reverse recovery time                | V <sub>CC</sub> =600 V, I <sub>E</sub> =1000 A, V <sub>GE</sub> =±15 V,                            | ,   | -    | -      | 500   | ns   |
| Qrr (Note1)                        | Reverse recovery charge              | $R_{G}=2.0 \Omega$ , Inductive load  |   | -    | 78     | -     | μC   |
| Eon                                | Turn-on switching energy per pulse   | V <sub>cc</sub> =600 V, I <sub>c</sub> =I <sub>E</sub> =1000 A,                                    |   | -    | 150.5  | -     |  |
| E <sub>off</sub>                   | Turn-off switching energy per pulse  | $V_{GE}=\pm 15 \text{ V}, \text{ R}_{G}=2.0 \Omega, \text{ T}_{vj}=150 \text{ °C},$                |   | -    | 128.4  | -     | mJ   |
| Err (Note1)                        | Reverse recovery energy per pulse    | Inductive load   |   | -    | 69     | -     | mJ   |
| R <sub>CC'+EE'</sub>               | Internal lead resistance             | Main terminals-chip, per switch, Tc=25 °C (Note4)  |   | -    | 0.5    | -     | mΩ   |
| r <sub>g</sub>                     | Internal gate resistance             | Per switch   |   | -    | 0.4    | -     | Ω  |
|                                    |                                      | L  |   | L    | 1      | 1     | <u>.                                    </u> |
|                                    |                                      |  |   |      | Limits |       |  |
| Symbol                             | Item                                 | Conditions   |   | Min. | Тур.   | Max.  | Unit   |

| Symbol Item          |                         | Conditions  |      |      |      | Unit |
|----------------------|-------------------------|---|------|------|------|------|
| Symbol               | nem                     | Conditions  | Min. | Тур. | Max. | Onit |
| R <sub>25</sub>      | Zero-power resistance   | T <sub>C</sub> =25 °C (Note4)                           | 4.85 | 5.00 | 5.15 | kΩ   |
| ΔR/R                 | Deviation of resistance | R <sub>100</sub> =493 Ω, T <sub>C</sub> =100 °C (Note4) | -7.3 | -    | +7.8 | %    |
| B <sub>(25/50)</sub> | B-constant              | Approximate by equation (Note6)                         | -    | 3375 | -    | К    |
| P <sub>25</sub>      | Power dissipation       | T <sub>C</sub> =25 °C (Note4)                           | -    | -    | 10   | mW   |

#### THERMAL RESISTANCE CHARACTERISTICS

| Symphol               | ltom                       | Conditions                                   |          |      | Unit |        |       |  |
|-----------------------|----------------------------|--|----------|------|------|--------|-------|--|
| Symbol Item           |                            | Conditions                                   |          | Min. | Тур. | Max.   | Unit  |  |
| R <sub>th(j-c)Q</sub> | Thermal resistance         | Junction to case, per Inverter IGBT (Note4)  |          | -    | -    | 28     | K/kW  |  |
| R <sub>th(j-c)D</sub> | mermanesistance            |  | -        | -    | 49   | rv/kvv |       |  |
| Р                     | Contact thermal registered | Case to heat sink, Thermal grease applied (N | ote4, 7) | -    | 7.1  | -      | K/kW  |  |
| R <sub>th(c-s)</sub>  | Contact thermal resistance | per 1 module, PC-TIM applied (Note4, 8)      |          | -    | 1.9  | -      | r/kvv |  |

#### **MECHANICAL CHARACTERISTICS**

| Currente e l   | lán m                  | 0.00                    | Conditions                                     |      | Limits |      |      |  |
|----------------|------------------------|-------------------------|--|------|--------|------|------|--|
| Symbol         | Item                   | Cor                     |  |      | Тур.   | Max. | Unit |  |
| M <sub>t</sub> | Mounting torque        | Main terminals          | Main terminals M 6 screw                       |      | 4.0    | 4.5  | N∙m  |  |
| Ms             | Mounting torque        | Mounting to heat sink   | M 5 screw                                      | 2.5  | 3.0    | 3.5  | N∙m  |  |
| ds             |                        |                         | Terminal to terminal                           | 17.3 | -      | -    | mm   |  |
|                | Creepage distance      | Solder pin type (DX)    | Terminal to base plate                         | 17.5 | -      | -    |      |  |
|                |                        |                         | Terminal to terminal                           | 16.5 | -      | -    | mm   |  |
|                |                        | Pressfit pin type (DXP) | Terminal to base plate                         | 18.0 | -      | -    |      |  |
|                | Clearance              |                         | Terminal to terminal                           | 10.3 | -      | -    | mm   |  |
| d              |                        | Solder pin type (DX)    | Terminal to base plate                         | 11.7 | -      | -    |      |  |
| da             |                        |                         | Terminal to terminal                           | 10.2 | -      | -    |      |  |
|                |                        | Pressfit pin type (DXP) | Pressfit pin type (DXP) Terminal to base plate |      | -      | -    | mm   |  |
| ec             | Flatness of base plate | On the centerline X, Y  | On the centerline X, Y (Note9)                 |      | -      | +200 | μm   |  |
| m              | mass                   | -                       | -  |      | 490    | -    | g    |  |

\*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

2. Junction temperature (T  $_{\nu j}$  ) should not increase beyond T  $_{\nu j\,m\,a\,x}$  rating.

3. Pulse width and repetition rate should be such that the device junction temperature  $(T_{vj})$  dose not exceed  $T_{vjmax}$  rating.

4. Case temperature (T<sub>c</sub>) and heat sink temperature (T<sub>s</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6.  $B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$ 

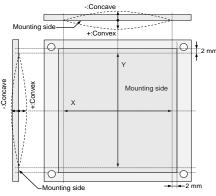
 $R_{25}\!\!:$  resistance at absolute temperature  $T_{25}$  [K];  $T_{25}\!\!=\!\!25$  [°C]+273.15=298.15 [K]

 $R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50}{=}50$  [°C]+273.15=323.15 [K]

7. Typical value is measured by using thermally conductive grease of  $\lambda=0.9$  W/(m·K)/D<sub>(C-S)</sub>=50  $\mu m.$ 

8. Typical value is measured by using PC-TIM of  $\lambda{=}3.4$  W/(m·K)/D<sub>(C-S)</sub>=50  $\mu m.$ 

9. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



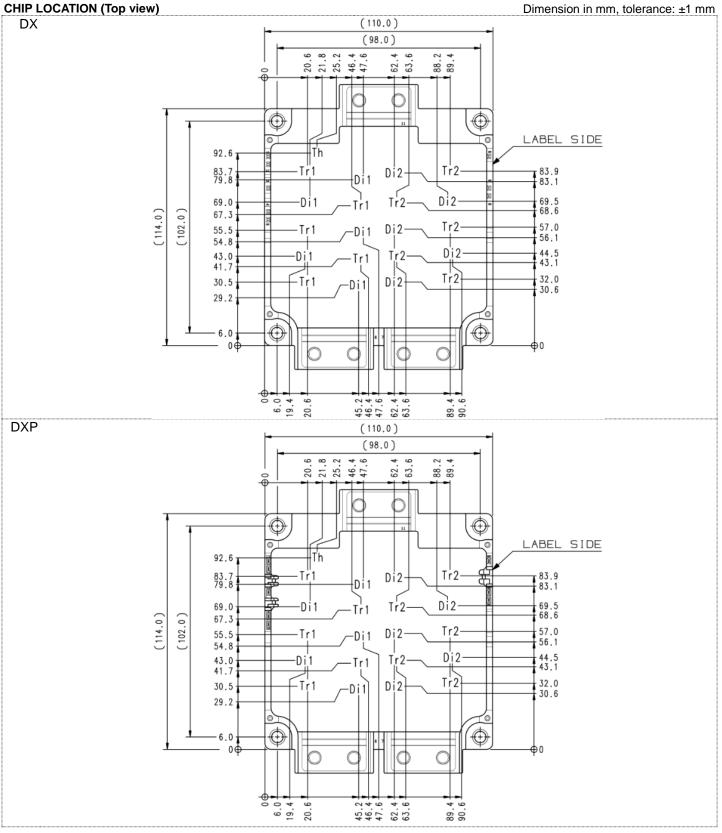
10. Use the following screws when mounting the printed circuit board (PCB) on the standoffs. PCB thickness : t1.6

|     | Туре          | Manufacturer | Size    | Tightening torque<br>(N•m) | Recommended tightening method          |
|-----|---------------|--------------|---------|----------------------------|--|
| (1) | PT®           | EJOT         | K25×8   | 0.55 ± 0.055               |  |
| (2) | PT®           | -            | K25×10  | 0.75 ± 0.075 N∙m           | by handwork (equivalent to 30 rpm      |
| (3) | DELTA PT®     | -            | 25×8    | 0.55 ± 0.055 N∙m           | by mechanical screw driver)            |
| (4) | DELTA PT®     | -            | 25×10   | 0.75 ± 0.075 N∙m           | ~ 600 rpm (by mechanical screw driver) |
| (5) | B1            | -            | φ2.6×10 | 0.75 ± 0.075 N ⋅ m         |  |
|     | tapping screw |              | φ2.6×12 | 0.75 ± 0.075 N-III         |  |

#### **RECOMMENDED OPERATING CONDITIONS**

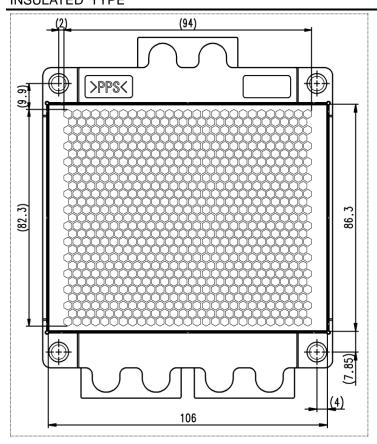
| Symbol            | Item                          | Conditions                             |      | Unit |      |       |
|-------------------|-------------------------------|--|------|------|------|-------|
|                   | nem                           | Conditions                             | Min. | Тур. | Max. | Offic |
| V <sub>cc</sub>   | (DC) Supply voltage           | Applied across C1-E2 terminals         |      | 600  | 850  | V     |
| V <sub>GEon</sub> | Gate (-emitter drive) voltage | Applied across G1-E1s/G2-E2s terminals | 13.5 | 15.0 | 16.5 | V     |
| R <sub>G</sub>    | External gate resistance      | Per switch                             | 2.0  | -    | 20   | Ω     |

#### MITSUBISHI ELECTRIC CORPORATION

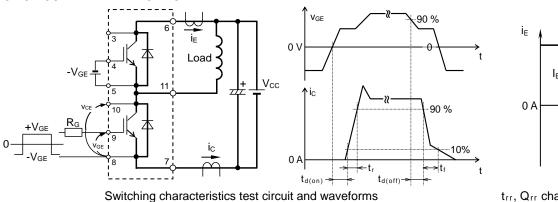


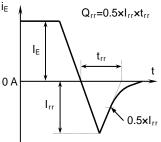
Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

**Option: PC-TIM applied baseplate outline** 



### TEST CIRCUIT AND WAVEFORMS

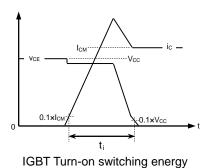


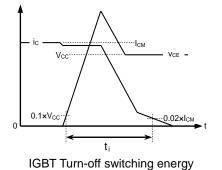


trr, Qrr characteristics test waveform

ti

VEC





FWD Reverse recovery energy

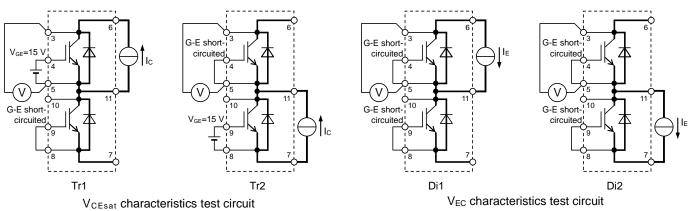
IEM

0 A

0 ۷

Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

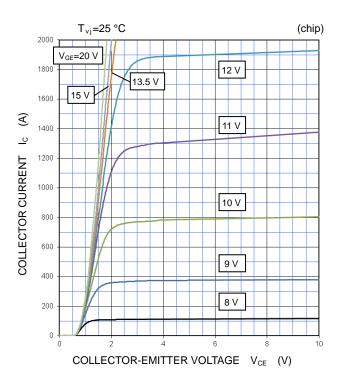
#### **TEST CIRCUIT**



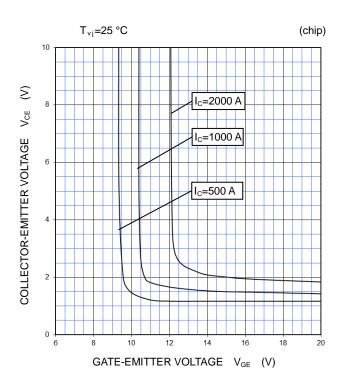
#### PERFORMANCE CURVES

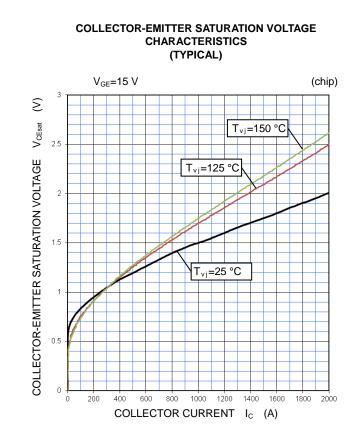
#### **INVERTER PART**



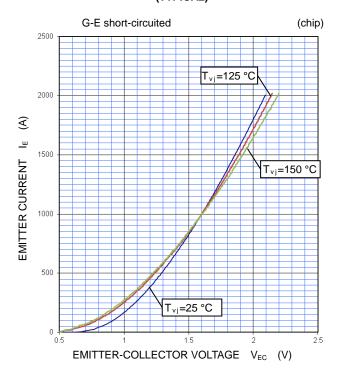


## COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)





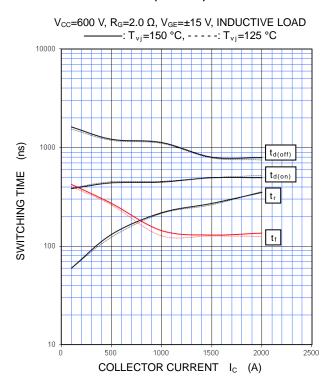
#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



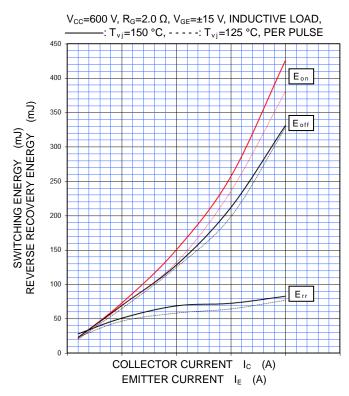
#### PERFORMANCE CURVES

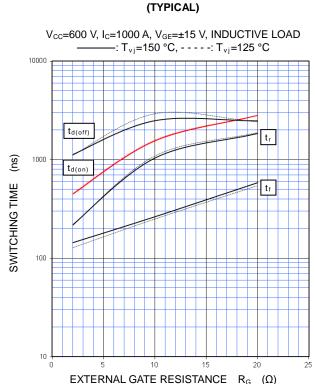
#### **INVERTER PART**

HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

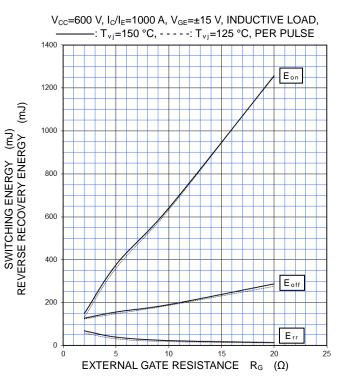


#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)





#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



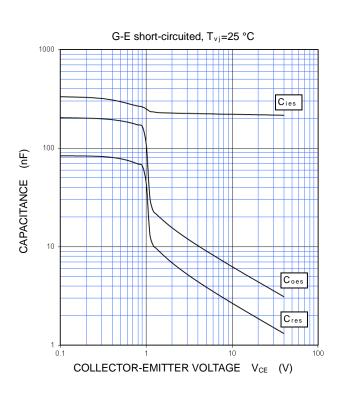
## HALF-BRIDGE SWITCHING CHARACTERISTICS

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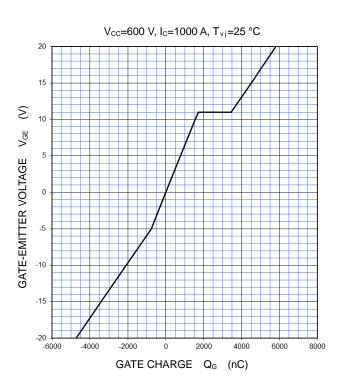
#### PERFORMANCE CURVES

#### **INVERTER PART**

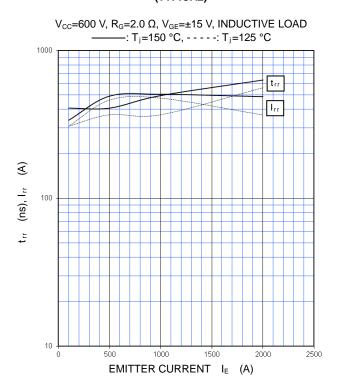
CAPACITANCE CHARACTERISTICS (TYPICAL)



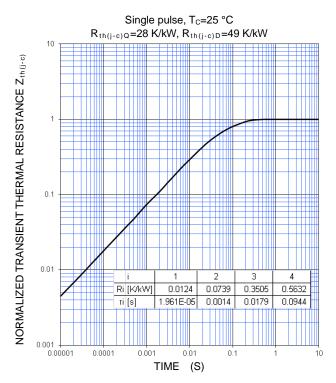
GATE CHARGE CHARACTERISTICS (TYPICAL)







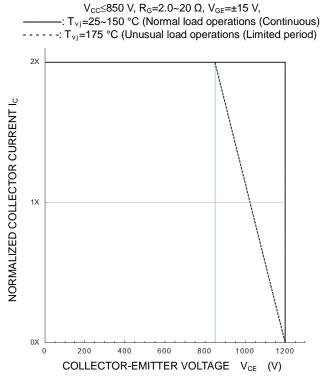
#### TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



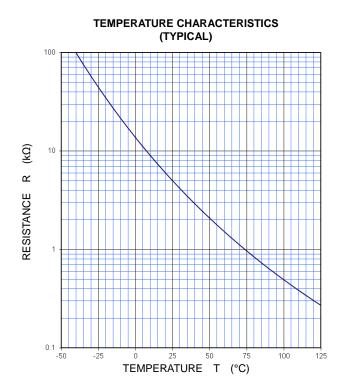
#### PERFORMANCE CURVES

#### **INVERTER PART**

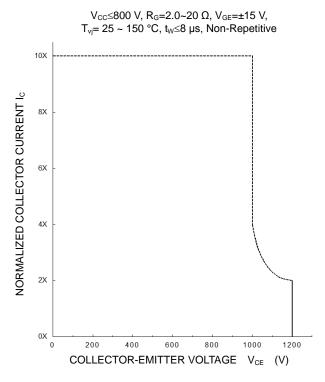
#### TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)



NTC thermistor part



SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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