

### **GENERAL DESCRIPTION**

The CM1084 series of high performance positive voltage regulators are designed for use in applications requiring low dropout performance at full rated current. Additionally, the CM1084 series provides excellent regulation over variations in line, load and temperature.

Outstanding features include low dropout performance at rated current, fast transient response, internal current limiting and thermal shutdown protection of the output device. The CM1084 series are three terminal regulators with adjustable voltage options available in popular packages.

# **APPLICATIONS**

- Power Supplies
- Computer Add-On Cards
- Other Applications Requiring Low Dropout Voltage Over Rated Current

#### **PIN CONFIGURATION**

GND/AD.

TO-220

Top View

/OUT

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# FEATURES

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- Low dropout performance
- 1.3V Typ. for CM1084
- Full current rating over line and temperature
  - Fast transient response
- ◆ ±2% total output regulation over line, load and temperature
- Adjust pin current max 120µA over temperature
- Adjustable output voltage
  - Line regulation typically 0.015%
- Load regulation typically 0.1%
- TO-220, TO-252 and TO-263 packages



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#### **BLOCK DIAGRAM**



#### ORDERING INFORMATION

Package Type				Operating	
TO-220	TO-252	TO-263	TO-263L	Temperature	Output Voltage
				Range (T <sub>A</sub> )	
CM1084KCN220	CM1084KCN252	CM1084KCN263	CM1084KCN263L	0 ~ +125	2.5V
CM1084SCN220	CM1084SCN252	CM1084SCN263	CM1084SCN263L	0 ~ +125	3.3V
CM1084CN220	CM1084CN252	CM1084CN263	CM1084CN263L	0 ~ +125	ADJ.
CM1084GKCN220*	CM1084GKCN252*	CM1084GKCN263*	CM1084GKCN263L*	0 ~ +125	2.5V
CM1084GSCN220*	CM1084GSCN252*	CM1084GSCN263*	CM1084GSCN263L*	0 ~ +125	3.3V
CM1084GCN220*	CM1084GCN252*	CM1084GCN263*	CM1084GCN263L*	0 ~ +125	ADJ.

\*Note: Add suffix "G" for Pb Free Product



# ABSOLUTE MAXIMUM RATINGS (Note 1)

Parameter	Symbol	Maximum	Units
Input Supply Voltage	V <sub>IN</sub>	7	V
Power Dissipation (Note 2)	PD	Internally	W
		Limited.	
Thermal Resistance Junction to Case			
TO-220	JC	2.5	
TO-263		2.5	/W
TO-252		2.5	
Thermal Resistance Junction to Ambient			
TO-220	JA	45	/W
TO-263/TO-263L		45	
TO-252		80	
Operating Junction Temperature Range (Note 3)	$T_J$	0 to 125	
Storage Temperature Range	T <sub>STG</sub>	-65 to 150	
Lead Temperature (Soldering) 6 Sec (TO-252)	T <sub>LEAD</sub>	260	
Lead Temperature (Soldering) 10 Sec			
(TO-263/220)			
ESD(Note 4)		2000V	



# ELECTRICAL CHARACTERISTICS

Typicals and limits appearing in normal type apply for  $T_j = 25$ . Limits appearing in **Boldface** type apply over the entire junction temperature range for operation.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Vpee	Reference Voltage	CM1084-ADJ				
* KEF	riorororioo voltago	$I_{OUT} = 10 \text{mA}$ . VIN-VOLIT = 3V	1.238	1.250	1.262	V
		$10mA \le I_{OUT} \le I_{EUU} + OAD$ , $1.5V \le (V_{IN} - V_{OUT}) \le 7V$	1.225	1.250	1.270	V
		(Note 7)	_		-	-
Vout	Output Voltage	CM1084-3.3	0.070			
	(Note 7)	$I_{OUT} = 0$ mA, $V_{IN} = 7V$	3.270	3.300	3.330	V
	,	$0mA \le I_{OUT} \le I_{FULL LOAD}$ , 4.8V $\le V_{IN} \le 7V$	3.235	3.300	3.365	V
		Cm1084-5.0	4.050	=		
		$I_{OUT} = 0$ mA, $V_{IN} = 7V$	4.950	5.000	5.050	V
		$0mA \le I_{OUT} \le I_{FULL LOAD}$ , $6.5V \le V_{IN} \le 7V$	4.900	5.000	5.100	V
Vout	Line Regulation	CM1084-ADJ		0.015	0.2	%
001	(Note 8)	$I_{OUT} = 10mA, 1.5V \le (V_{IN} - V_{OUT}) \le 7V$		0.035	0.2	96
	,	CM1084-3 3		0.5	6	mV
		$l_{\text{out}} = 0\text{m}\Delta / 8/(z - 1/w) = -7/(z - 1/w)$		10	6	mV
		$Cm1084_50$		0.5	10	mV
		$l_{\text{out}} = 0\text{mA} = 6.5 \text{V/c} = 7 \text{V}$		10	10	mV
Vour	Load Regulation	CM1084-AD1		0.1	0.2	04
V 001	(Note 8)	$V_{\rm M}$ - $V_{\rm C}$ = 3/ 10mAc-lourd-lour		0.1	0.3	90
				0.2	0.4	%
		CM1084-3.3		3	15	mv
		VIN = 7 V, U<=IOUT<=IFULL LOAD	-	1	20	mv
		Cm1084-5.0		5	20	mv
		VIN = 7 V, U<=I <sub>OUT</sub> <=I <sub>FULL LOAD</sub>	-	10	35	mv
	Dropout Voltage	CM1084-3.3/5/ADJ		1.3	1.5	V
	(Note 9)	$V_{\text{REF}} = 1\%$ , $I_{\text{OUT}} = 5V$		-	_	
LIMIT	Current Limit	CM1084-ADJ	5.5	8.0		А
		$V_{\text{IN}} - V_{\text{OUT}} = 5V$		••		
		CM1084-3.3	5.5	8.0		А
		$V_{\rm IN} = 7V$				
		Cm1084-5.0	5.5	8.0		А
		V <sub>IN</sub> = 7V				
	Minimum Load	CM1084-ADJ		5.0	10.0	mA
	Current (Note 10)	$V_{\text{IN}} - V_{\text{OUT}} = 5V$				
	Quiescent Current	CM1084-3.3		5.0	10.0	mA
		$V_{\rm IN} = 7V$	_			
		Cm1084-5.0		5.0	10.0	mA
	·	$V_{\rm IN} \leq 7V$				
	Thermal	$T_A = 25$ , 30ms Pulse		0.003	0.015	% /W
	Regulation					
	Ripple Rejection	$f_{RIPPLE} = 120Hz$ , = $C_{OUT} = 25 \mu$ F, I antalum,	60	75		dB
		I <sub>OUT</sub> = 5A				
		CM1084-ADJ, $C_{ADJ} = 25 \mu F$ , $(V_{IN}-V_{OUT}) = 3V$				
		CM1084-3.3, V <sub>IN</sub> = 6.3V	60	72		dB
		CM1084-5.0, V <sub>IN</sub> = 7V	60	68		dB
	Adjust Pin Current	CM1084		55	120	μA
	Adjust Pin Current	10mA,= I <sub>OUT</sub> <=I <sub>FULL LOAD</sub>			-	۸
	Change	1.5V<= V <sub>IN</sub> -V <sub>OUT</sub> <=7V		0.2	5	μA
	Temperature			0.5		
	Stability			0.5		%
	Long Term	$T_A = 125$ , 1000 H <sub>Is</sub>		0.0	4.0	
	Stability	,		0.3	1.0	%
	RMS Output Noise	10Hz <= f <=10kHz		0.000		
	(% of V <sub>OUT</sub> )			0.003		%
<u> </u>	Thermal Resistance	3-Lead TO-263: Control Section/Output Section			0 65/2 7	/W
	Junction-to-Case	3-Lead TO-220: Control Section/Output Section			0.65/2.7	////
					5.55/2.7	/ • •



#### ELECTRICAL CHARACTERISTICS (Continued)

Typicals and limits appearing in normal type apply for  $T_j = 25$ . Limits appearing in **Boldface** type apply over the entire junction temperature range for operation.

#### NOTES :

- (1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings Indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.
- (2) Power Dissipation is kept in a safe range by current limiting circuitry, Refer to Overload Recovery in Application Notes.
- (3) The maximum power dissipation is a function of  $T_{J(MAX)}$ ,  $_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any ambient temperature is PD = ( $T_{J(MAX} T_A)$ /  $_{JA}$ . All numbers apply for packages soldered directly into a PC board, Refer to Thermal Considerations in the Application Notes.
- (4) For testing purposes, ESD was applied using human body model, 1.5k in series with 100pF.
- (5) Typical Values represent the most likely parametric norm.
- (6) All limits are guaranteed by testing or statistical analysis.
- (7) I<sub>FULL LOAD</sub> is defined in the current limit curves. The I<sub>FULL LOAD</sub> Curve defines the current limit as a function of input-to-output voltage. Note that 30W power dissipation for the CM1084 is only achievable over a limited range of input-to-output voltage.
- (8) Load and line regulation are measured at constant junction temperature, and are guaranteed up to the maximum power dissipation of 30W. Power dissipation is determined by the input/output differential and the output current. Guaranteed maximum power dissipation will not be available over the full input/output rage.
- (9) Dropout voltage is specified over the full output current range of the device.
- (10) The minimum output current required to maintain regulation.



# **APPLICATION CIRCUIT**



Adjustable Regulator



# PACKAGE DIMENSION



2004/10/13 Preliminary Rev. 1.1



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2004/10/13 Preliminary Rev. 1.1



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