

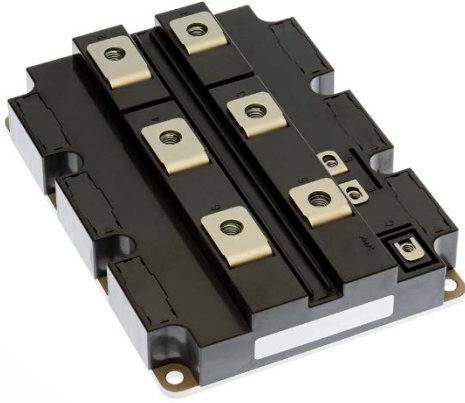
< High Voltage Insulated Gate Bipolar Transistor : HVIGBT >

# CM1800HC-66X

HIGH POWER SWITCHING USE  
INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## CM1800HC-66X



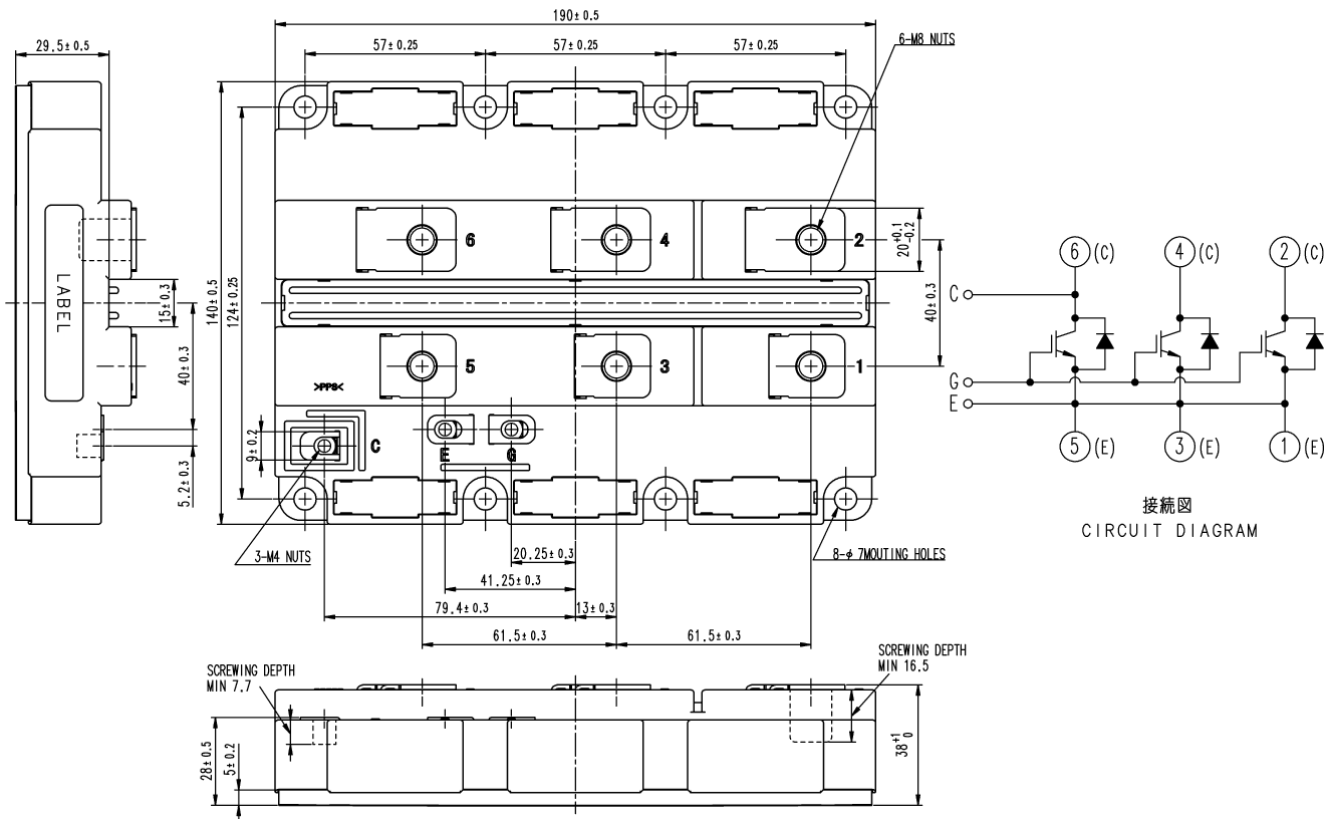
- $I_C$ .....1800A
- $V_{CES}$ .....3300V
- 1-element in a Pack
- Insulated Type
- CSTBT™(III)
- RFC Diode
- AISiC Baseplate

## APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers

## OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



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## MAXIMUM RATINGS

Symbol	Item	Conditions	Ratings	Unit
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V, T <sub>J</sub> = -40...+150°C	3300	V
		V <sub>GE</sub> = 0V, T <sub>J</sub> = -50°C	3200	
V <sub>GES</sub>	Gate-emitter voltage	V <sub>CE</sub> = 0V, T <sub>J</sub> = 25°C	±20	V
I <sub>C</sub>	Collector current	DC, T <sub>C</sub> = 105°C	1800	A
I <sub>CRM</sub>		Pulse (Note 1)	3600	A
I <sub>E</sub>	Emitter current (Note 2)	DC, T <sub>C</sub> = 90°C	1800	A
I <sub>ERM</sub>		Pulse (Note 1)	3600	A
P <sub>tot</sub>	Maximum power dissipation (Note 3)	T <sub>C</sub> = 25°C, IGBT part	17800	W
V <sub>iso</sub>	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1min.	6000	V
V <sub>e</sub>	Partial discharge extinction voltage	RMS, sinusoidal, f = 60Hz, Q <sub>PD</sub> ≤ 10pC	2600	V
T <sub>J</sub>	Junction temperature		-50 ~ +150	°C
T <sub>Jop</sub>	Operating junction temperature		-50 ~ +150	°C
T <sub>stg</sub>	Storage temperature		-55 ~ +150	°C
t <sub>psc</sub>	Short circuit pulse width	V <sub>CC</sub> = 2500V, V <sub>CE</sub> ≤ V <sub>CES</sub> , V <sub>GE</sub> = 15V, T <sub>J</sub> = 150°C	10	µs

## ELECTRICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit	
			Min	Typ	Max		
I <sub>CES</sub>	Collector cutoff current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0V	T <sub>J</sub> = 25°C	—	—	6.0	mA
			T <sub>J</sub> = 125°C	—	6.0	—	
			T <sub>J</sub> = 150°C	—	36.0	—	
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	V <sub>CE</sub> = 10V, I <sub>C</sub> = 180mA, T <sub>J</sub> = 25°C	6.5	7.0	7.5	V	
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = V <sub>GES</sub> , V <sub>CE</sub> = 0V, T <sub>J</sub> = 25°C	-0.5	—	0.5	µA	
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 100kHz T <sub>J</sub> = 25°C	—	208	—	nF	
C <sub>oes</sub>	Output capacitance		—	14.0	—		
C <sub>res</sub>	Reverse transfer capacitance		—	1.9	—		
Q <sub>G</sub>	Total gate charge	V <sub>CC</sub> = 1800V, I <sub>C</sub> = 1800A, V <sub>GE</sub> = ±15V	—	13.5	—	µC	
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> = 1800A (Note 4) V <sub>GE</sub> = 15V	T <sub>J</sub> = 25°C	—	2.00	—	V
			T <sub>J</sub> = 125°C	—	2.50	—	
			T <sub>J</sub> = 150°C	—	2.60	3.10	
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> = 1800V I <sub>C</sub> = 1800A V <sub>GE</sub> = ±15V	T <sub>J</sub> = 25°C	—	0.45	—	µs
			T <sub>J</sub> = 125°C	—	0.45	—	
			T <sub>J</sub> = 150°C	—	0.45	0.90	
t <sub>r</sub>	Turn-on rise time	V <sub>CC</sub> = 1800V I <sub>C</sub> = 1800A V <sub>GE</sub> = ±15V	T <sub>J</sub> = 25°C	—	0.25	—	µs
			T <sub>J</sub> = 125°C	—	0.25	—	
			T <sub>J</sub> = 150°C	—	0.25	0.50	
E <sub>on(10%)</sub>	Turn-on switching energy (Note 7)	R <sub>G(on)</sub> = 1.5Ω L <sub>S</sub> = 100nH Inductive load	T <sub>J</sub> = 25°C	—	2.95	—	J
			T <sub>J</sub> = 125°C	—	3.25	—	
			T <sub>J</sub> = 150°C	—	3.40	—	
E <sub>on</sub>	Turn-on switching energy (Note 5)	R <sub>G(on)</sub> = 1.5Ω L <sub>S</sub> = 100nH Inductive load	T <sub>J</sub> = 25°C	—	3.00	—	J
			T <sub>J</sub> = 125°C	—	3.40	—	
			T <sub>J</sub> = 150°C	—	3.55	—	
t <sub>d(off)</sub>	Turn-off delay time	V <sub>CC</sub> = 1800V I <sub>C</sub> = 1800A V <sub>GE</sub> = ±15V	T <sub>J</sub> = 25°C	—	2.90	—	µs
			T <sub>J</sub> = 125°C	—	3.20	—	
			T <sub>J</sub> = 150°C	—	3.20	4.25	
t <sub>f</sub>	Turn-off fall time	V <sub>CC</sub> = 1800V I <sub>C</sub> = 1800A V <sub>GE</sub> = ±15V	T <sub>J</sub> = 25°C	—	0.40	—	µs
			T <sub>J</sub> = 125°C	—	0.45	—	
			T <sub>J</sub> = 150°C	—	0.50	1.00	
E <sub>off(10%)</sub>	Turn-off switching energy (Note 7)	R <sub>G(off)</sub> = 12Ω L <sub>S</sub> = 100nH Inductive load	T <sub>J</sub> = 25°C	—	2.30	—	J
			T <sub>J</sub> = 125°C	—	3.05	—	
			T <sub>J</sub> = 150°C	—	3.10	—	
E <sub>off</sub>	Turn-off switching energy (Note 5)	R <sub>G(off)</sub> = 12Ω L <sub>S</sub> = 100nH Inductive load	T <sub>J</sub> = 25°C	—	2.45	—	J
			T <sub>J</sub> = 125°C	—	3.10	—	
			T <sub>J</sub> = 150°C	—	3.35	—	

# CM1800HC-66X

HIGH POWER SWITCHING USE

INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## ELECTRICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit	
			Min	Typ	Max		
V <sub>EC</sub>	Emitter-collector voltage (Note 2)	I <sub>E</sub> = 1800A (Note 4) V <sub>GE</sub> = 0V	T <sub>j</sub> = 25°C	—	2.20	—	V
			T <sub>j</sub> = 125°C	—	2.40	—	
			T <sub>j</sub> = 150°C	—	2.50	3.00	
t <sub>rr</sub>	Reverse recovery time (Note 2)		T <sub>j</sub> = 25°C	—	0.95	—	μs
			T <sub>j</sub> = 125°C	—	1.10	—	
			T <sub>j</sub> = 150°C	—	1.15	—	
I <sub>rr</sub>	Reverse recovery current (Note 2)		T <sub>j</sub> = 25°C	—	—	—	A
			T <sub>j</sub> = 125°C	—	2350	—	
			T <sub>j</sub> = 150°C	—	2500	—	
Q <sub>rr(10%)</sub>	Reverse recovery charge (Note 2,6)	V <sub>CC</sub> = 1800V I <sub>E</sub> = 1800A V <sub>GE</sub> = ±15V	T <sub>j</sub> = 25°C	—	1600	—	μC
			T <sub>j</sub> = 125°C	—	2400	—	
			T <sub>j</sub> = 150°C	—	2500	—	
Q <sub>rr</sub>	Reverse recovery charge (Note 2,5)	R <sub>G(on)</sub> = 1.5Ω L <sub>S</sub> = 100nH Inductive load	T <sub>j</sub> = 25°C	—	1800	—	μC
			T <sub>j</sub> = 125°C	—	2600	—	
			T <sub>j</sub> = 150°C	—	2700	—	
E <sub>rec(10%)</sub>	Reverse recovery energy (Note 2,7)		T <sub>j</sub> = 25°C	—	1.70	—	J
			T <sub>j</sub> = 125°C	—	2.45	—	
			T <sub>j</sub> = 150°C	—	2.80	—	
E <sub>rec</sub>	Reverse recovery energy (Note 2,5)		T <sub>j</sub> = 25°C	—	1.85	—	J
			T <sub>j</sub> = 125°C	—	2.60	—	
			T <sub>j</sub> = 150°C	—	2.95	—	

## THERMAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to Case, IGBT part	—	—	7.0	K/kW
R <sub>th(j-c)D</sub>		Junction to Case, FWDi part	—	—	11.0	
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink λ <sub>grease</sub> = 1W/m·k, D <sub>(c-s)</sub> = 80μm	—	5.0	—	K/kW

## MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
M <sub>t</sub>	Mounting torque	M8 : Main terminals screw	7.0	—	19.0	N·m
M <sub>s</sub>		M6 : Mounting screw	3.0	—	6.0	N·m
M <sub>t</sub>		M4 : Auxiliary terminals screw	1.0	—	3.0	N·m
M	Mass		—	1.2	—	kg
CTI	Comparative tracking index		600	—	—	—
d <sub>a</sub>	Clearance		19.5	—	—	mm
d <sub>s</sub>	Creepage distance		32.0	—	—	mm
L <sub>P-CE</sub>	Parasitic stray inductance		—	8.0	—	nH
R <sub>CC+EE'</sub>	Internal lead resistance	T <sub>C</sub> = 25°C	—	0.09	—	mΩ

Note1. Pulse width and repetition rate should be such that junction temperature (T<sub>j</sub>) does not exceed T<sub>jopmax</sub> rating.

Note2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD).

Note3. Junction temperature (T<sub>j</sub>) should not exceed T<sub>jmax</sub> rating (150°C).

Note4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

Note5. Definition of all items is according to IEC 60747, unless otherwise specified.

Note6. The integration range of reverse recovery charge is from I<sub>E</sub> = 0A to 10%I<sub>E</sub>.

Note7. The integration range of switching energies is from 10%V<sub>CE</sub> to 10%I<sub>C</sub>(10%I<sub>E</sub>).

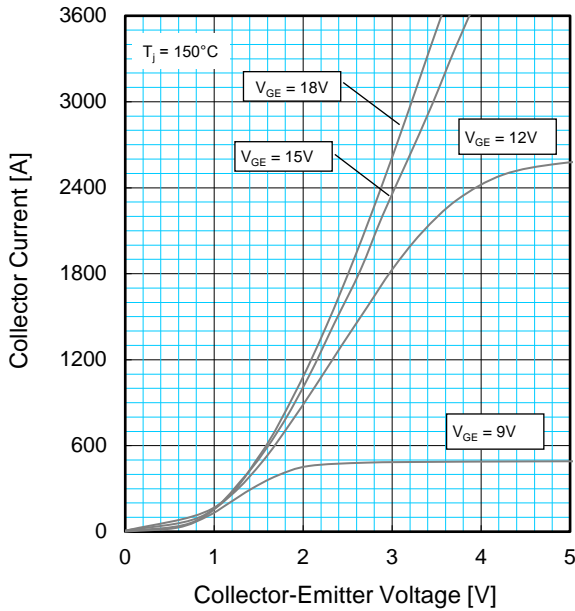
# CM1800HC-66X

HIGH POWER SWITCHING USE

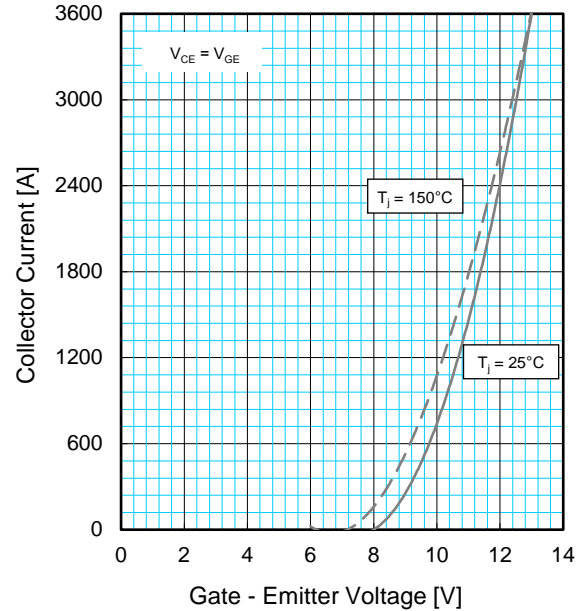
INSULATED TYPE

## PERFORMANCE CURVES

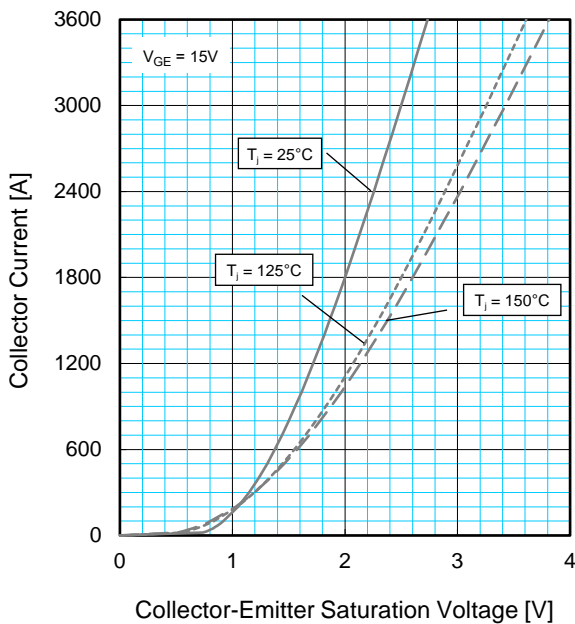
**OUTPUT CHARACTERISTICS (TYPICAL)**



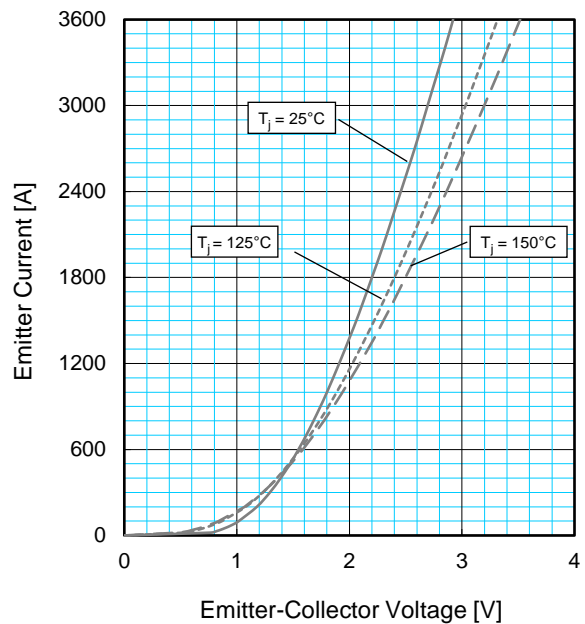
**TRANSFER CHARACTERISTICS (TYPICAL)**



**COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)**



**FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)**



# CM1800HC-66X

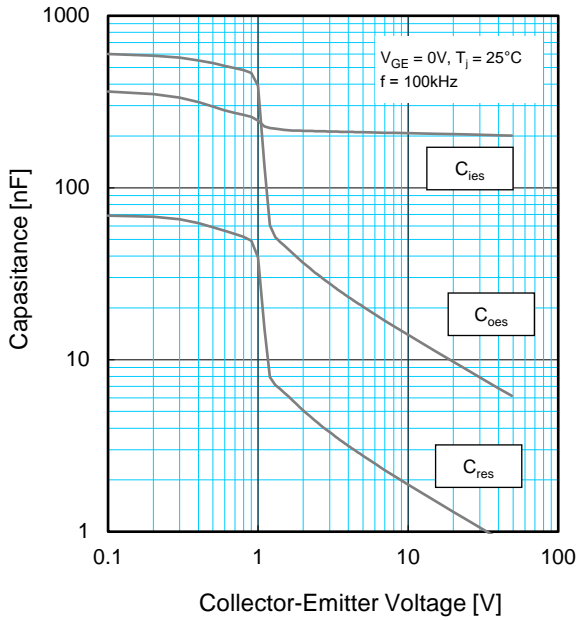
HIGH POWER SWITCHING USE

INSULATED TYPE

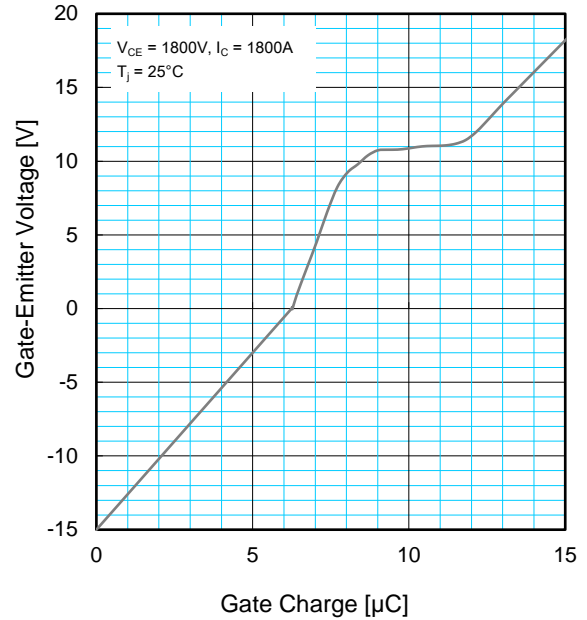
5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## PERFORMANCE CURVES

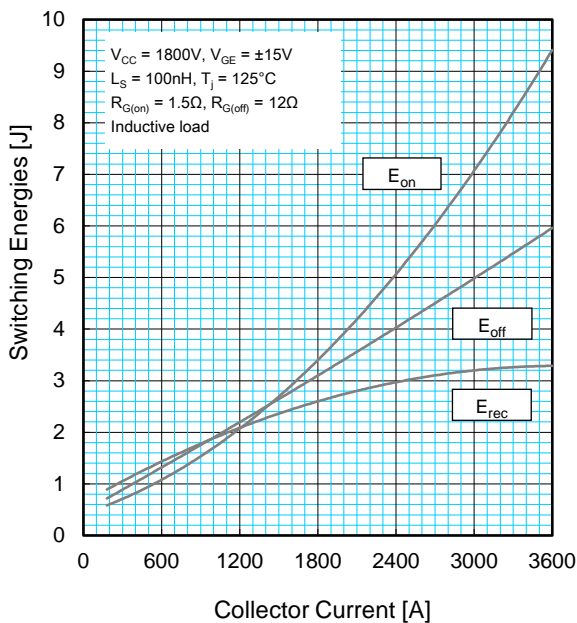
**CAPACITANCE CHARACTERISTICS (TYPICAL)**



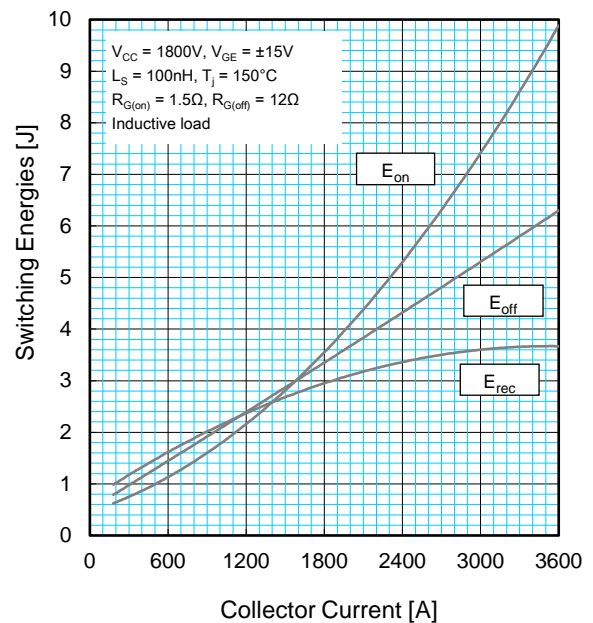
**GATE CHARGE CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



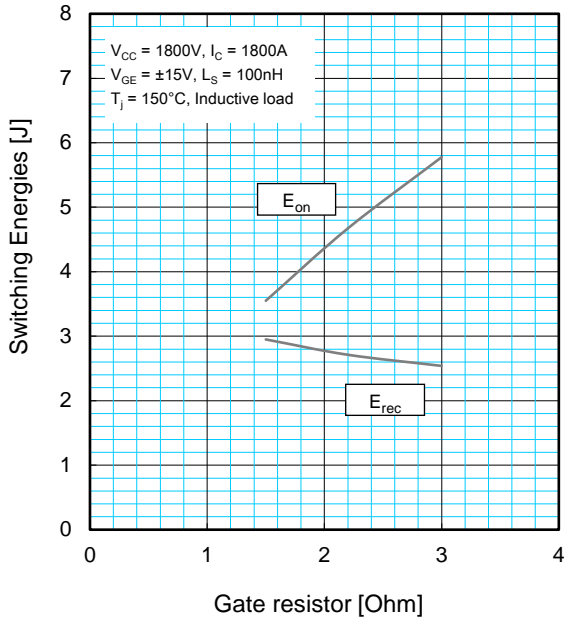
# CM1800HC-66X

HIGH POWER SWITCHING USE

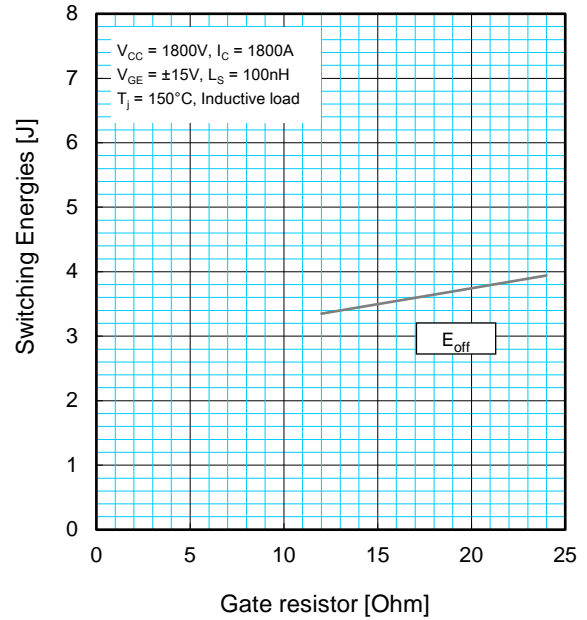
INSULATED TYPE

## PERFORMANCE CURVES

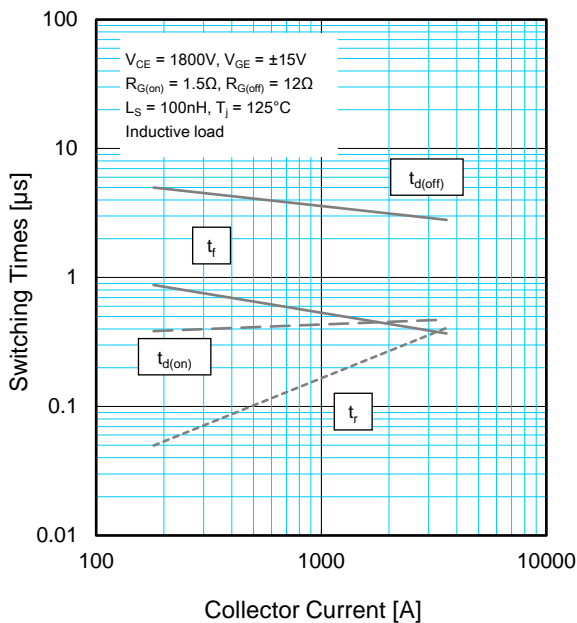
**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



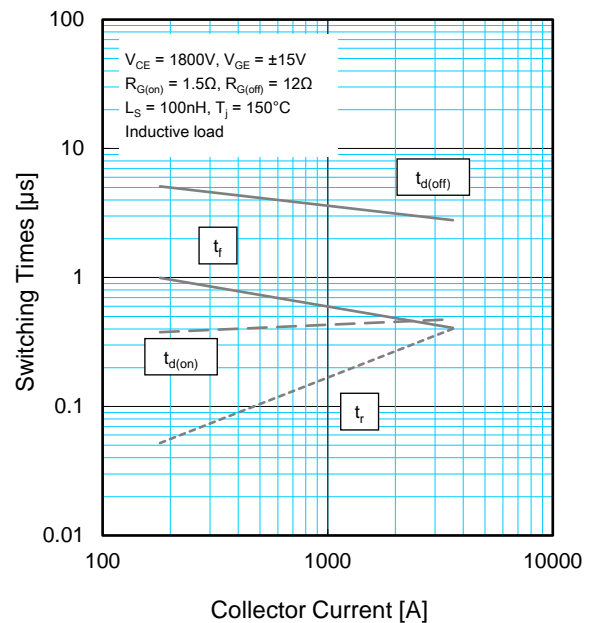
**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)**



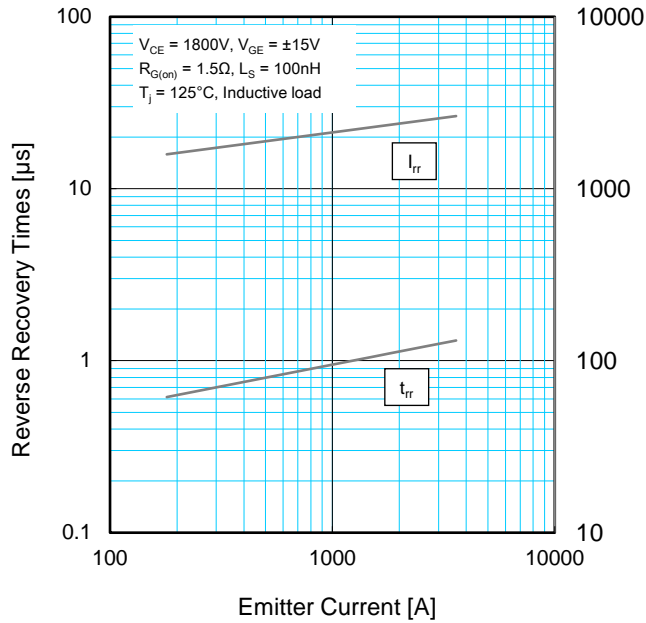
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HIGH POWER SWITCHING USE

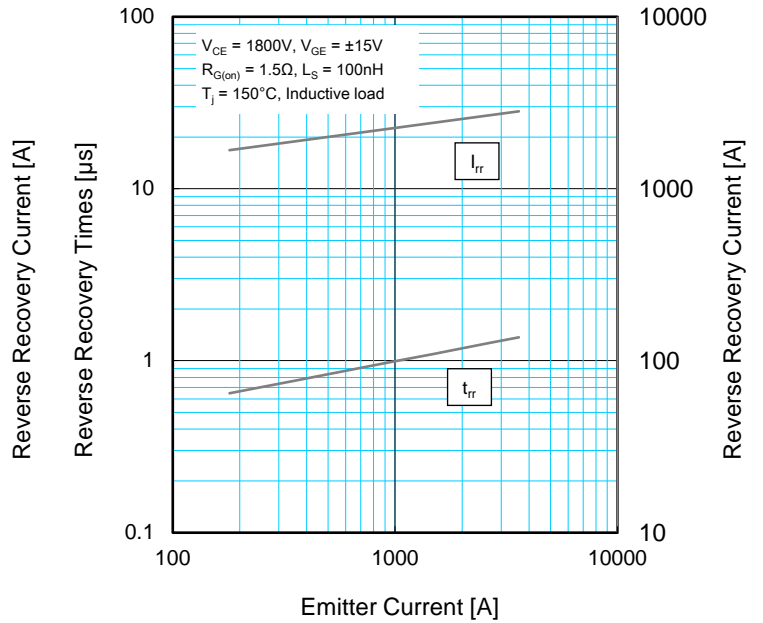
INSULATED TYPE

## PERFORMANCE CURVES

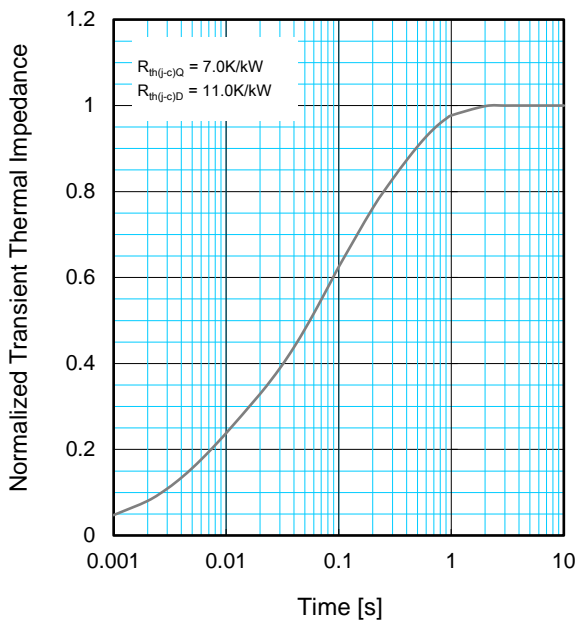
**FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)**



**FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)**



**TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS**



$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i \left\{ 1 - \exp\left(-\frac{t}{\tau_i}\right) \right\}$$

	1	2	3	4
$R_i / R_{th(j-c)}$	0.0096	0.1893	0.4044	0.3967
$\tau_i$ [sec]	0.0001	0.0058	0.0602	0.3512

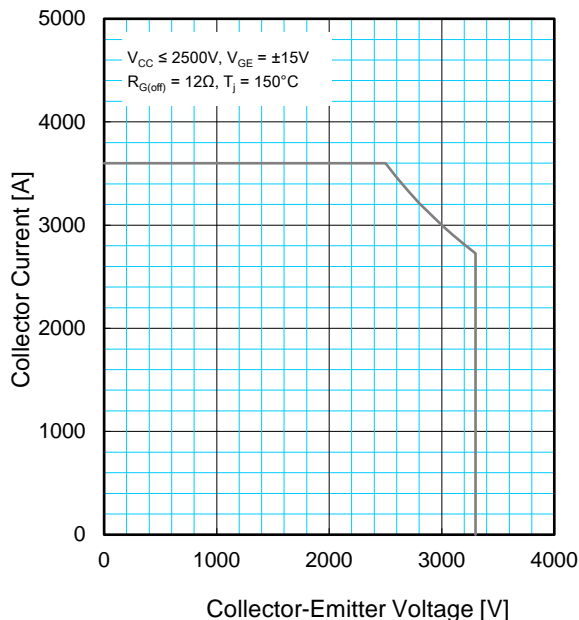
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HIGH POWER SWITCHING USE

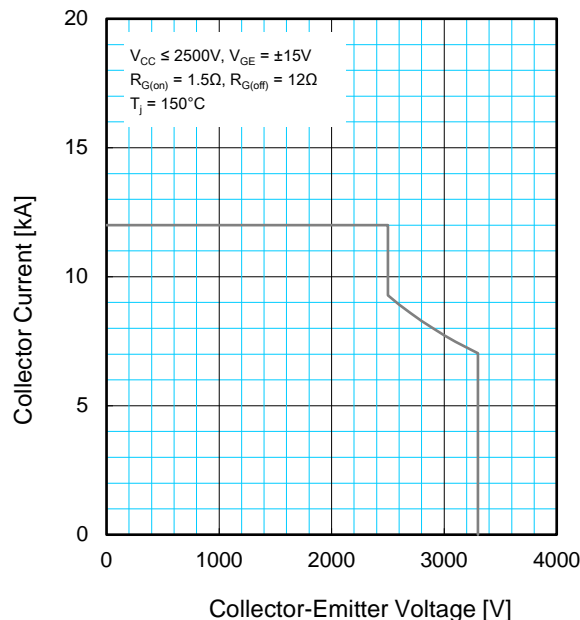
INSULATED TYPE

## PERFORMANCE CURVES

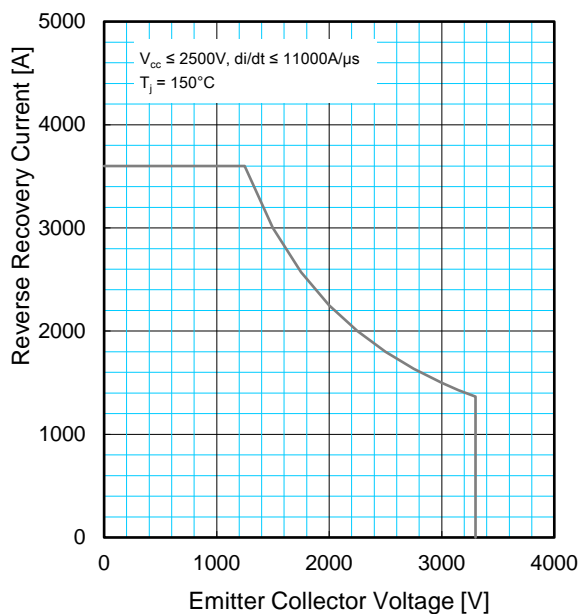
**REVERSE BIAS SAFE OPERATING AREA (RBSOA)**



**SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)**



**FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)**





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5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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