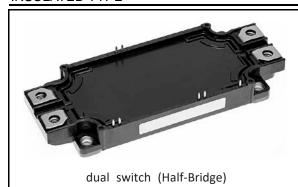


<IGBT Modules>

CM300DX-24S1

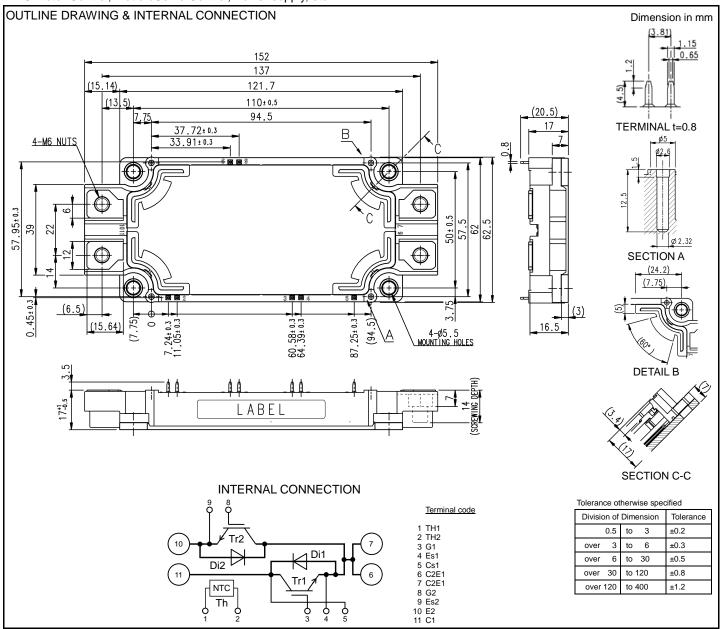
HIGH POWER SWITCHING USE INSULATED TYPE



- Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •UL Recognized under UL1557, File No. E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



1

HIGH POWER SWITCHING USE

INSULATED TYPE

MAXIMUM RATINGS (T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	ctor-emitter voltage G-E short-circuited		V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T _C =107 °C (Note2, 4)	300	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	600	A
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	1850	W
l _E (Note1)	Emitter current	DC (Note2)	300	۸
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	600	A

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	
T _{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stq}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	m Conditions			Limits		
Symbol	item			Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		ı	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		ı	-	0.5	μA
V _{GE(th)}	Gate-emitter threshold voltage	I _C =30 mA, V _{CE} =10 V		5.4	6.0	6.6	V
.,		I _C =300 A, V _{GE} =15 V,	T _j =25 °C	-	1.80	2.25	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _j =125 °C	-	2.00	-	V
(Terminal)	Collector-emitter saturation voltage	(Note5)	T _j =150 °C	-	2.05	-	
.,	Collector-entitler saturation voltage	I _C =300 A,	T _j =25 °C	-	1.70	2.15	
V _{CEsat} (Chip)		V _{GE} =15 V,	T _j =125 °C	-	1.90	-	V
		(Note5)	T _j =150 °C	-	1.95	-	
Cies	Input capacitance			-	-	30	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	6.0	nF
C _{res}	Reverse transfer capacitance	7		-	-	0.5	
Q _G	Gate charge	V _{CC} =600 V, I _C =300 A, V _{GE} =15 V		-	630	-	nC
t _{d(on)}	Turn-on delay time	Vcc=600 V, Ic=300 A, V _{GE} =±15 V,		-	-	800	
tr	Rise time			-	-	200	ns
t _{d(off)}	Turn-off delay time			-	-	600	
t _f	Fall time	$R_G=0 \Omega$, Inductive load		-	-	300	1
414		I _E =300 A, G-E short-circuited,	T _j =25 °C	-	2.60	3.40	
V _{EC} (Note1)		Refer to the figure of test circuit	T _j =125 °C	-	2.16	-	V
(Terminal)		(Note5)	T _j =150 °C	-	2.10	-	
	Emitter-collector voltage	I _E =300 A,	T _j =25 °C	-	2.50	3.30	
V _{EC} (Note1)		G-E short-circuited,	T _j =125 °C	-	2.06	-	V
(Chip)		(Note5)	T _j =150 °C	-	2.00	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =300 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	R _G =0 Ω, Inductive load		-	8.0	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =300 A,		-	26.7	-	
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=0 \Omega, T_{i}=150 \text{ °C},$		-	35.7	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	18.6	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, Tc=25 °C (Note2)		-	-	0.9	mΩ
r _g	Internal gate resistance	Per switch		-	6.5	-	Ω

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HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; T_i=25 °C, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Itom	Conditions	Limits			Unit
Syllibol	ltem ltem	Conditions	Min.	Тур.	Max.	Orill
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Itom	Conditions	Limits			Unit
Symbol Item		Conditions	Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	81	K/kW
$R_{th(j-c)D}$	Thermai resistance	Junction to case, per Inverter FWD (Note4)	-	-	130	N/KVV
R _{th(c-s)} Contact	Contact thermal resistance	Case to heat sink, per 1 module,		15		K/kW
	Contact thermal resistance	Thermal grease applied (Note4, 7)	-		-	r/KVV

MECHANICAL CHARACTERISTICS

Symbol	Itom	Conditions				Unit	
Symbol	Item	Conditions		Min.	Тур.	Max.	Utill
M_t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
m	mass	-		-	350	-	g
d	Creepage distance	Terminal to terminal		17	-	-	mm
d _s Cr		Terminal to base plate		18.5	-	-	
d _a Clearance	Clearance	Terminal to terminal		10	-	-	mm
	Clearance	Terminal to base plate		16.3	-	-	
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm

^{*.} This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

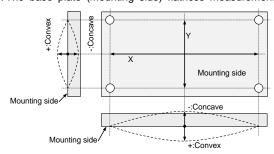
- 2. Junction temperature (T_j) should not increase beyond $T_{j\,m\,a\,x}$ rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_i) dose not exceed T_{imax} rating.
- 4. Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

6.
$$B(25/50) = In(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 R_{25} : resistance at absolute temperature T_{25} [K], T_{25} =25 [°C] +273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K], T_{50} =50 [°C] +273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



HIGH POWER SWITCHING USE

INSULATED TYPE

9. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness : t=1.6

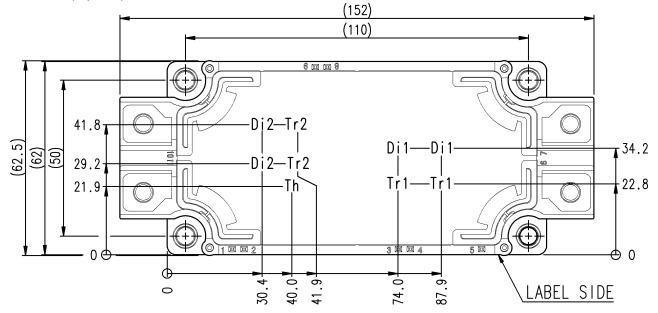
Туре	Manufacturer	Size	Tightening torque (N·m)	Recommended tightening method
(1) PT®	EJOT	K25×8	0.55 ± 0.055	
(2) PT®		K25×10	0.75 ± 0.075	by handwork (equivalent to 30 rpm
(3) DELTA PT®		25×8	0.55 ± 0.055	by mechanical screw driver)
(4) DELTA PT®		25×10	0.75 ± 0.075	~ 600 rpm (by mechanical screw driver)
(5) B1 tapping screw	-	φ2.6×10	0.75 ± 0.075	
		φ2.6×12		

RECOMMENDED OPERATING CONDITIONS

Symbol	Itom	Conditions	Limits			Unit
Symbol Item		Conditions	Min.	Тур.	Max.	Offic
Vcc	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	0	-	15	Ω

CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

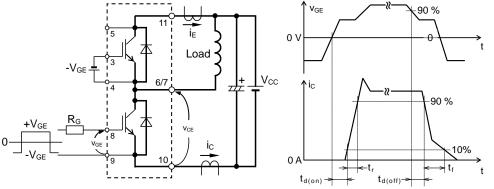


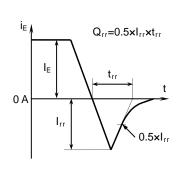
Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

HIGH POWER SWITCHING USE INSULATED TYPE

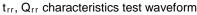
IIII

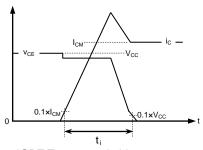


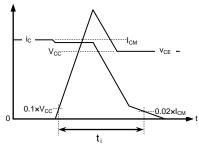


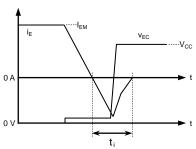


Switching characteristics test circuit and waveforms









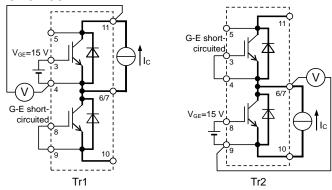
IGBT Turn-on switching energy

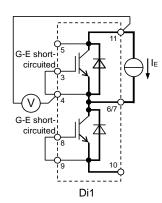
IGBT Turn-off switching energy

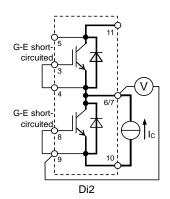
FWD Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT







V_{CEsat} characteristics test circuit

V_{EC} characteristics test circuit

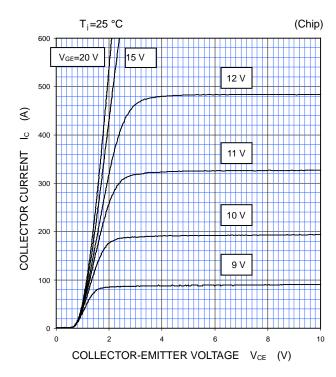
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

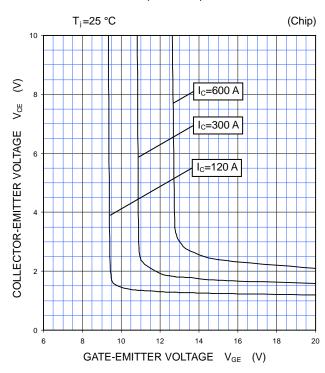
INVERTER PART

OUTPUT CHARACTERISTICS

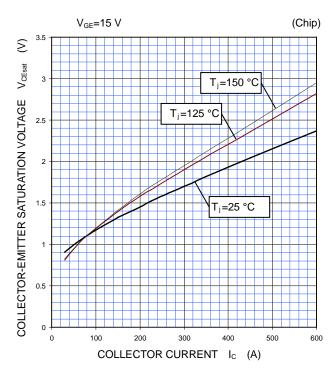
(TYPICAL)



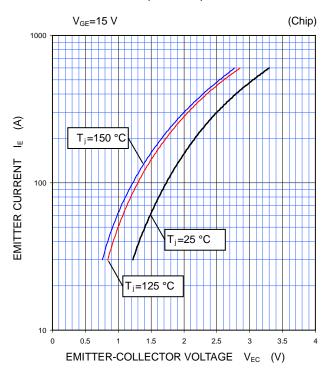
COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

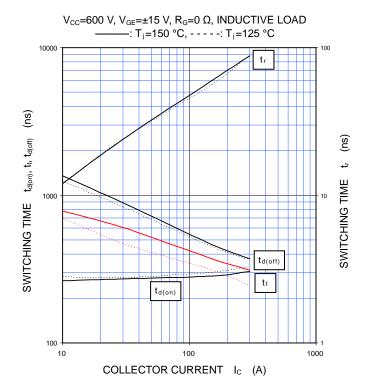


HIGH POWER SWITCHING USE INSULATED TYPE

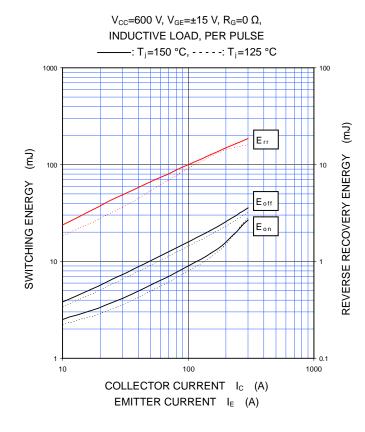
PERFORMANCE CURVES

INVERTER PART

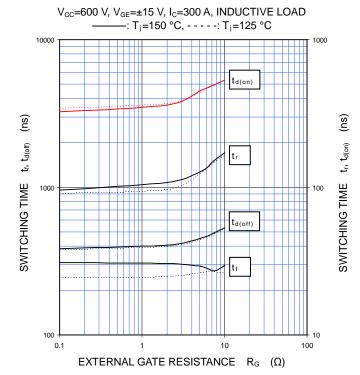
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



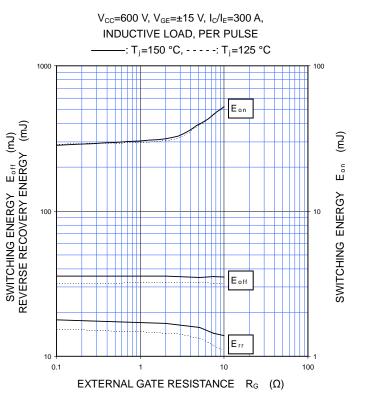
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



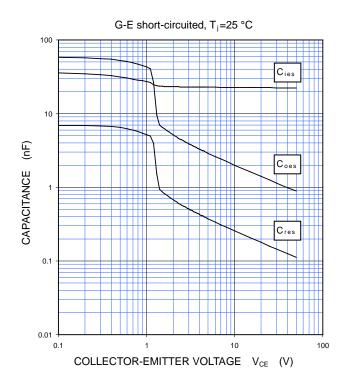
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

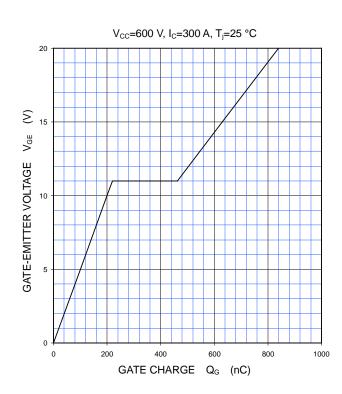
INVERTER PART

CAPACITANCE CHARACTERISTICS

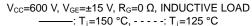
(TYPICAL)

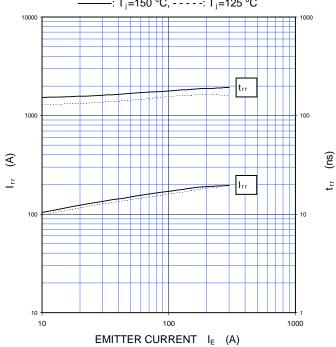


GATE CHARGE CHARACTERISTICS (TYPICAL)



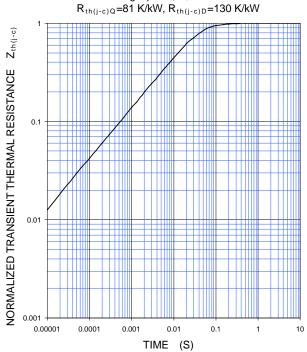
FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)





TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T_C=25 °C R_{th(i-c)D}=81 K/kW, R_{th(i-c)D}=130 K/kW



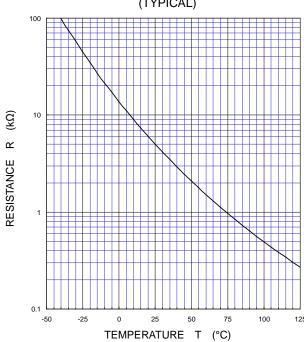
8

HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



9



<IGBT Modules>

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10

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