HIGH POWER SWITCHING USE INSULATED TYPE

CM400HA-24A

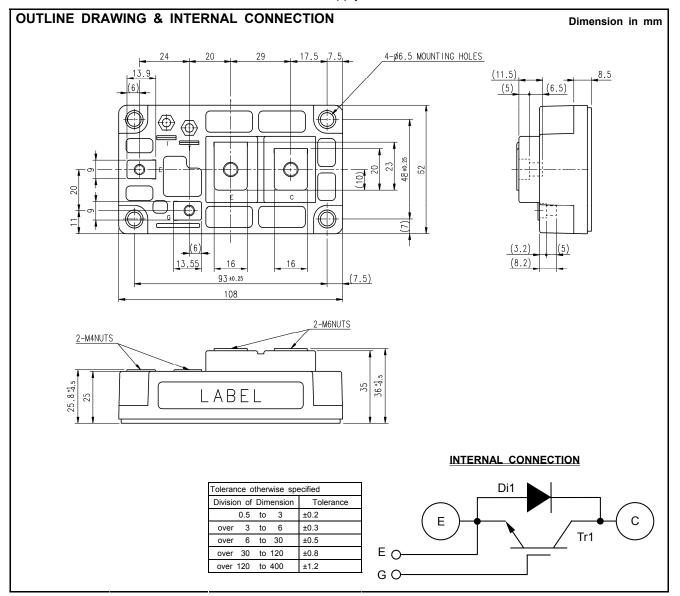


- Flat base Type
 Copper (non-plating) base plate
 No accessory (terminal screw) attach
- •RoHS Directive compliant

Single

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



HIGH POWER SWITCHING USE INSULATED TYPE

ABSOLUTE MAXIMUM RATINGS (T_j=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V	
V_{GES}	Gate-emitter voltage	C-E short-circuited	±20	V	
Ic	Collector current	DC, T _C =87 °C (Note.2)	400	Α	
I _{CRM}	Collector current	Pulse, Repetitive (Note.3)	800		
P _{tot}	Total power dissipation	T _C =25 °C (Note.2, 4)	2350	W	
I _E (Note.1)	Emitter current	T _C =25 °C (Note.2, 4)	400	Α	
I _{ERM} (Note.1)	(Free wheeling diode forward current)	Pulse, Repetitive (Note.3)	800] ^	
Tj	Junction temperature	-	-40 ~ +150	°C	
T _{stg}	Storage temperature	-	-40 ~ +125		
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V	

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
				Min.	Тур.	Max.	Offic
M_t		Main terminals	M 6 screw	1.96	2.45	2.94	
M _t	Mounting torque	Auxiliary terminals	M 4 screw	0.98	1.18	1.47	N·m
Ms		Mounting to heat sink	M 6 screw	1.96	2.45	2.94	
m	Weight	-		-	480	-	g
ec	Flatness of base plate	On the centerline X, Y	(Note.5)	±0	-	+100	μm

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

Symbol	Item	Conditions		Limits			Linit
Syllibol	item			Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1	mA
I _{GES}	Gate-emitter leakage current	±V _{GE} =V _{GES} , C-E short-circuited		-	-	1	μA
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	I _C =40 mA, V _{CE} =10 V		6	7	8	V
V _{CEsat}	Collector-emitter saturation voltage	I _C =400 A (Note.6),	T _j =25 °C	-	2.1	3.0	V
CESAL		V _{GE} =15 V	T _j =125 °C	-	2.4	-	
C_{ies}	Input capacitance	V _{CE} =10 V, G-E short-circuited		-	-	70	nF
Coes	Output capacitance			-	-	6.0	
C_{res}	Reverse transfer capacitance			-	-	1.4	
Q_{G}	Gate charge	V _{CC} =600 V, I _C =400 A, V _{GE} =15 V		-	2000	1	nC
$t_{d(on)}$	Turn-on delay time	V_{CC} =600 V, I _C =400 A, V_{GE} =±15 V, R_{G} =0.78 Ω, Inductive load		-	-	550	ns
t _r	Rise time			-	-	180	
t _{d(off)}	Turn-off delay time			-	-	600	
t _f	Fall time			-	-	350	
V _{EC} (Note.1)	Emitter-collector voltage	I _E =400 A (Note.6), G-E short-circuited		-	3.0	3.8	V
t _{rr} (Note.1)	Reverse recovery time	V _{CC} =600 V, I _E =400 A, V _{GE} =±15 V,		-	-	250	ns
Q _{rr} (Note.1)	Reverse recovery charge	R _G =0.78 Ω, Inductive load		-	14.7	-	μC
Eon	Turn-on switching energy per pulse	V_{CC} =600 V, I_{C} = I_{E} =400 A, V_{GE} =±15 V, R_{G} =0.78 Ω, T_{j} =125 °C, Inductive load		-	50.4	-	
E _{off}	Turn-off switching energy per pulse			-	41.8	-	mJ
E _{rr} (Note.1)	Reverse recovery energy per pulse			-	20	-	
r _g	Internal gate resistance	T _C =25 °C		-	1.5	-	Ω
R_G	External gate resistance	-		0.78	-	10	Ω

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
		Conditions	Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance (Note.2)	Junction to case, IGBT part	-	-	53	K/kW
$R_{th(j-c)D}$	Thematresistance	Junction to case, FWDi part	-	-	80	K/kW
$R_{th(c-s)}$	Contact thermal resistance (Note.2)	Case to heat sink, Thermal grease applied (Note.7)	ı	20	ı	K/kW

2



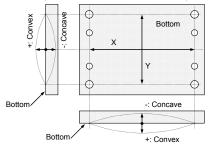
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- Note.1: Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).
- Note.2: Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface of base plate and heat sink just under the chips. (Refer to the figure of chip location)

The heat sink thermal resistance $\{R_{th(s\text{-}a)}\}$ should measure just under the chips.

- Note.3: Pulse width and repetition rate should be such that the device junction temperature (Tj) dose not exceed Tjmax rating.
- Note.4: Junction temperature (T_j) should not increase beyond $T_{j\,m\,a\,x}$ rating.
- Note.5: Base plate flatness measurement point is as in the following figure.



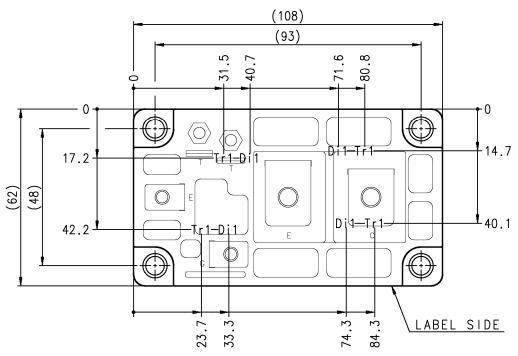
Note.6: Pulse width and repetition rate should be such as to cause negligible temperature rise.

(Refer to the figure of test circuit)

Note.7: Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K).

CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

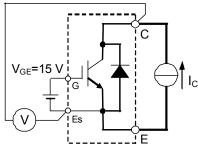


Tr1: IGBT, Di1: FWDi. Each mark points the center position of each chip.

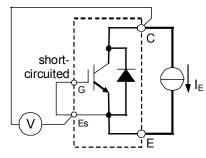


HIGH POWER SWITCHING USE INSULATED TYPE

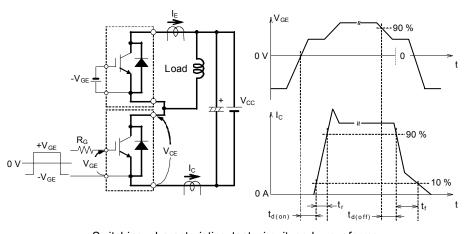
TEST CIRCUIT AND WAVEFORMS



V_{CEsat} test circuit



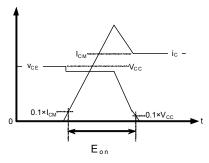
V_{EC} test circuit



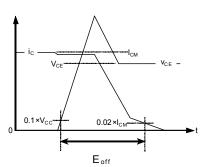
 i_{E} $Q_{rr}=0.5\times I_{rr}\times t_{rr}$ $0.5\times I_{rr}$

Switching characteristics test circuit and waveforms

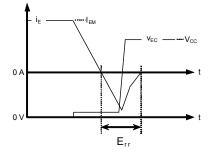
 t_{rr} , Q_{rr} test waveform



IGBT Turn-on switching energy



IGBT Turn-off switching energy



FWDi Reverse recovery energy

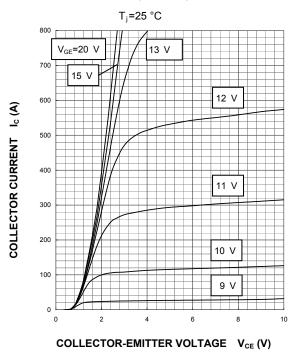
Turn-on, Turn-off switching and Reverse recovery energy test waveforms (integral range)



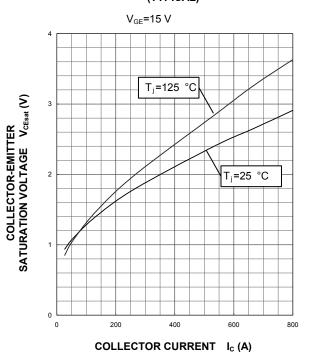
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

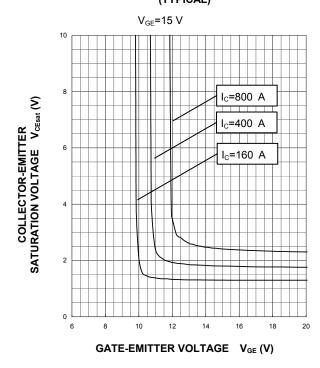
OUTPUT CHARACTERISTICS (TYPICAL)



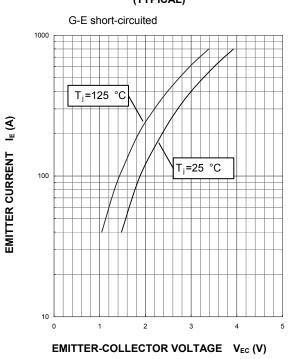
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

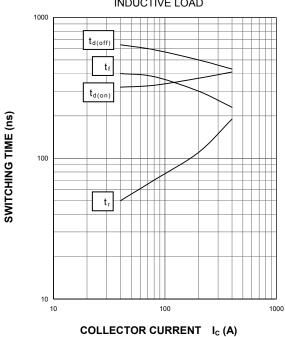




HIGH POWER SWITCHING USE INSULATED TYPE

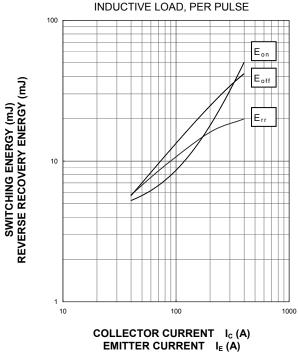
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_G =0.78 Ω , T_j =125 °C INDUCTIVE LOAD



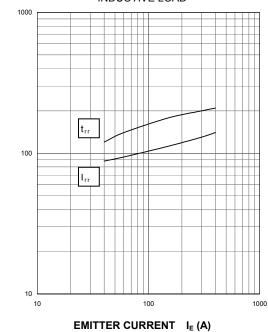
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $\label{eq:Vcc=600} V_{\text{CC}}\text{=}600~V,~V_{\text{GE}}\text{=}\pm15~V,~R_{\text{G}}\text{=}0.78~\Omega,~T_{j}\text{=}125~^{\circ}\text{C}\\ \text{INDUCTIVE LOAD, PER PULSE}$



FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_G =0.78 Ω , T_j =125 °C INDUCTIVE LOAD

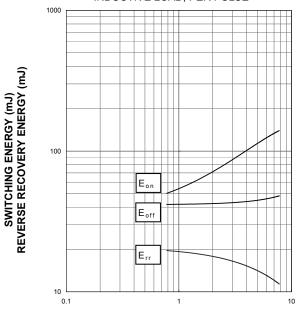


trr (ns), Irr (A)

HALF-BRIDGE

SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, I_C/I_E =400 A, V_{GE} =±15 V, T_j =125 °C INDUCTIVE LOAD, PER PULSE

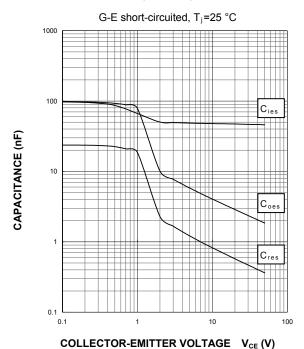


EXTERNAL GATE RESISTANCE $R_{G}(\Omega)$

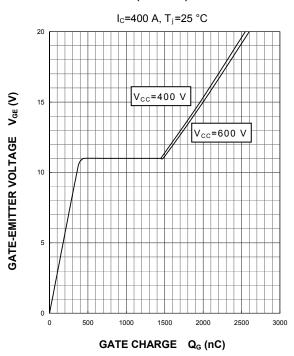


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CAPACITANCE CHARACTERISTICS (TYPICAL)

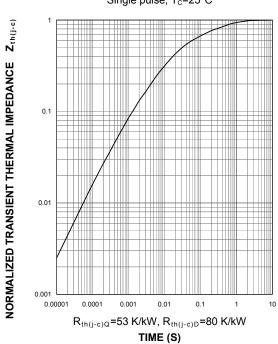


GATE CHARGE CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T_C =25°C



HIGH POWER SWITCHING USE INSULATED TYPE

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