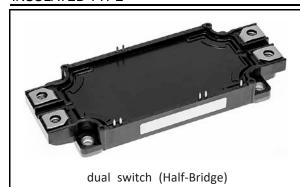


<IGBT Modules>

CM600DX-24S1

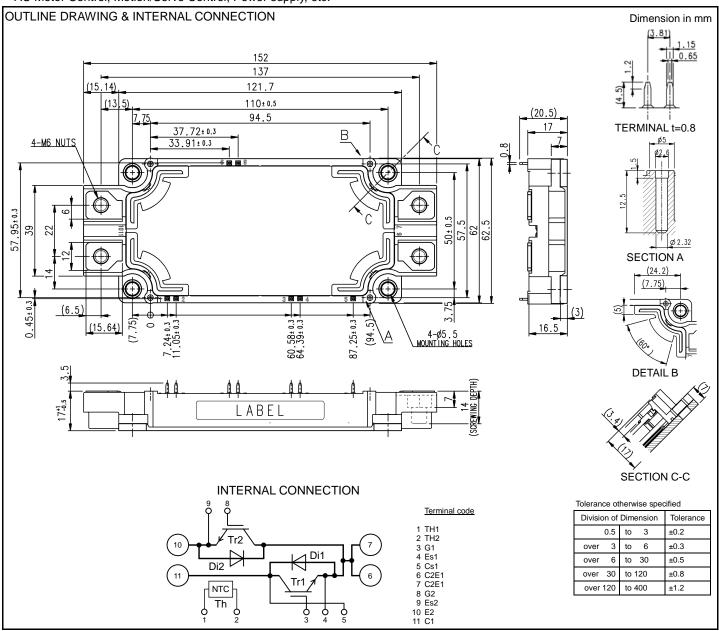
HIGH POWER SWITCHING USE INSULATED TYPE



- •Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •UL Recognized under UL1557, File No. E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



1

HIGH POWER SWITCHING USE

INSULATED TYPE

MAXIMUM RATINGS (T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	age G-E short-circuited		V	
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Collector ourrent	DC, T _C =94 °C (Note2, 4)	600	۸	
I _{CRM}	Collector current	Pulse, Repetitive, V _{GE} =15 V (Note3)	1200	A	
P _{tot}	Total power dissipation T _C =25 °C (Note2, 4)		3330	W	
l _E (Note1)	Emittor ourrent	DC (Note2)	600	۸	
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	1200	Α	

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	
T _{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stq}	Storage temperature	-	-40 ~ +125	10

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions		Limits			Unit
Symbol	item	Conditions		Min.	Тур.	Max.	Offic
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited	V _{CE} =V _{CES} , G-E short-circuited			1.0	mA
I_{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I_C =60 mA, V_{CE} =10 V		5.4	6.0	6.6	V
		I _C =600 A, V _{GE} =15 V,	T _j =25 °C	-	2.00	2.45	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _j =125 °C	-	2.30	-	V
(Terrillial)	Collector-emitter saturation voltage	(Note5)	T _j =150 °C	-	2.40	-	
	Collector-entitler saturation voltage	I _C =600 A,	T _j =25 °C	-	1.85	2.35	
V _{CEsat}		V _{GE} =15 V,	T _j =125 °C	-	2.10	-	V
(Chip)		(Note5)	T _j =150 °C	-	2.15	-	
Cies	Input capacitance			-	-	50	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	10	nF
Cres	Reverse transfer capacitance		-			0.83	1
Q _G	Gate charge	V _{CC} =600 V, I _C =600 A, V _{GE} =15 V		-	1050	-	nC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =600 A, V _{GE} =±15 V, R _G =0 Ω, Inductive load		-	-	800	
tr	Rise time			-	-	200	ns
t _{d(off)}	Turn-off delay time			-	-	600	
tf	Fall time			-	-	300	
41		I _E =600 A, G-E short-circuited,	T _j =25 °C	-	2.8	3.60	
V _{EC} (Note1)		Refer to the figure of test circuit (Note5)	T _j =125 °C	-	2.4	-	V
(Terminal)			T _j =150 °C	-	2.3	-	
	- Emitter-collector voltage	I _E =600 A,	T _j =25 °C	-	2.7	3.50	
V _{EC} (Note1)		G-E short-circuited,	T _j =125 °C	-	2.3	-	V
(Chip)		(Note5)	T _j =150 °C	-	2.2	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =600 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	R _G =0 Ω, Inductive load		-	16	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =600 A,		-	91.5	-	
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=0 \Omega, T_{i}=150 \text{ °C},$		-	63.1	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	36.1	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)	Main terminals-chip, per switch,			0.4	mΩ
r _g	Internal gate resistance	Per switch		-	5.0	_	Ω

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HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; T_i=25 °C, unless otherwise specified)

NTC THERMISTOR PART

Cumbal	lta.a.	Conditions	Limits			l lesis
Symbol	Item	Conditions	Min. Typ. Max. 4.85 5.00 5.15 kΩ -7.3 - +7.8 %			
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
Symbol		Conditions	Min.	Тур.	Max.	Offit
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	45	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)	-	-	72	r/kvv
R _{th(c-s)}	Contact thermal registeres	Case to heat sink, per 1 module,		15		K/kW
	Contact thermal resistance	Thermal grease applied (Note4, 7)	-	15	-	r/KVV

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits		Unit		
Symbol	item			Min.	Тур.	Max.	Offic	
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N⋅m	
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N⋅m	
m	mass	-		-	350	-	g	
d	Creepage distance	Terminal to terminal		17	-	-	mm	
d _s		Terminal to base plate		18.5	-	-		
۵	Clearance	Terminal to terminal		10	-	-		
d _a		Terminal to base plate		16.3	-	-	mm	
e _c	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm	

^{*.} This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

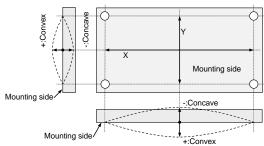
- 2. Junction temperature (T_j) should not increase beyond $T_{j\,m\,a\,x}$ rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_i) dose not exceed T_{imax} rating.
- 4. Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

6.
$$B(25/50) = In(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 R_{25} : resistance at absolute temperature T_{25} [K], T_{25} =25 [°C] +273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K], T_{50} =50 [°C] +273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



HIGH POWER SWITCHING USE

INSULATED TYPE

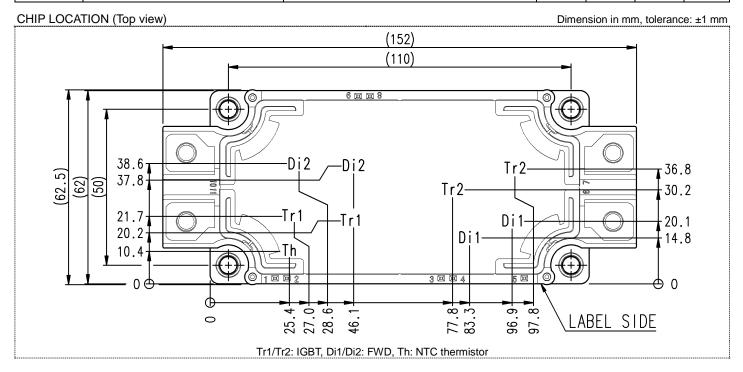
Note9 Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness: t=1.6

Туре	Manufacturer	Size	Tightening torque (N·m)	Recommended tightening method
(1) PT®	EJOT	K25×8	0.55 ± 0.055	
(2) PT®		K25×10	0.75 ± 0.075	by handwork (equivalent to 30 rpm
(3) DELTA PT®		25×8	0.55 ± 0.055	by mechanical screw driver)
(4) DELTA PT®		25×10	0.75 ± 0.075	~ 600 rpm (by mechanical screw driver)
(5) B1 tapping screw	-	φ2.6×10	0.75 ± 0.075	
		φ2.6×12		

RECOMMENDED OPERATING CONDITIONS

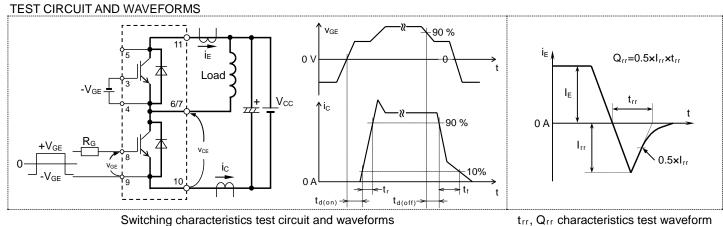
Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Orill
Vcc	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V _{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	14.0	15.0	16.5	V
R _G	External gate resistance	Per switch	0	-	6.8	Ω



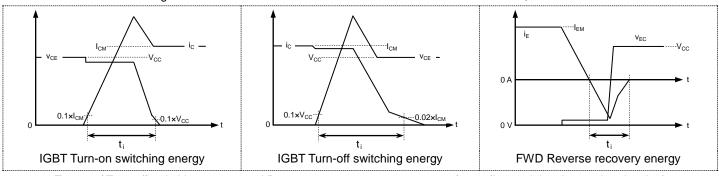
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HIGH POWER SWITCHING USE

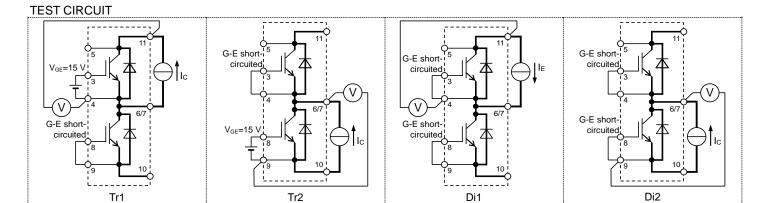
INSULATED TYPE



Switching characteristics test circuit and waveforms



Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)



V_{CEsat} characteristics test circuit

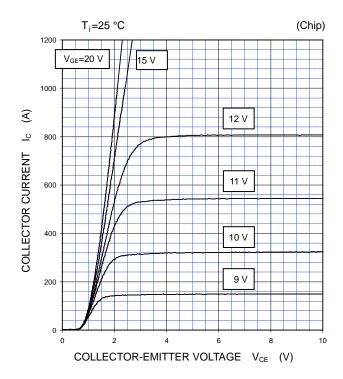
V_{EC} characteristics test circuit

HIGH POWER SWITCHING USE INSULATED TYPE

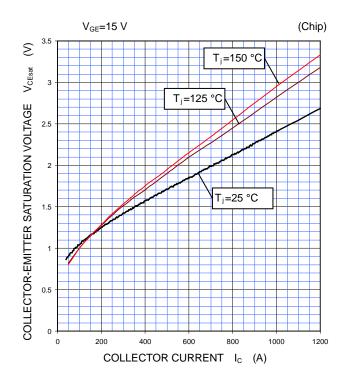
PERFORMANCE CURVES

INVERTER PART

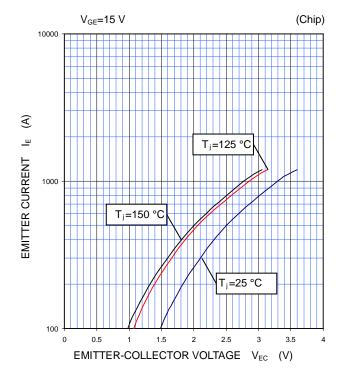
OUTPUT CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

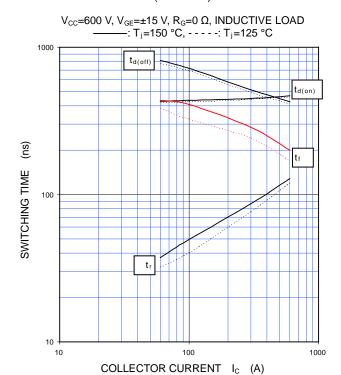


HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

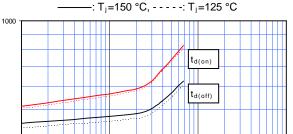
INVERTER PART

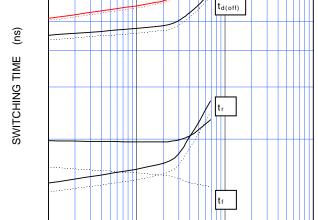
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, I_{C} =600 A, INDUCTIVE LOAD

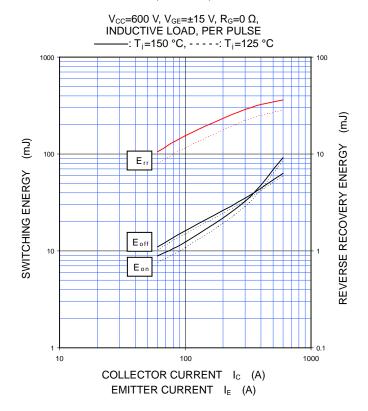




100

0.1

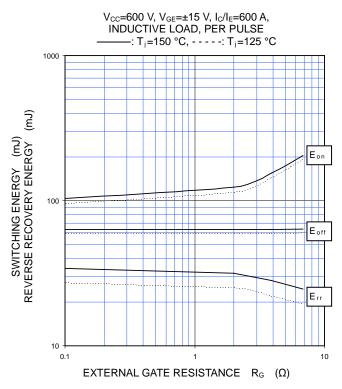
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

EXTERNAL GATE RESISTANCE R_G (Ω)

100

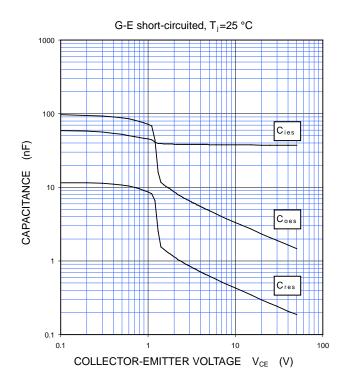


HIGH POWER SWITCHING USE INSULATED TYPE

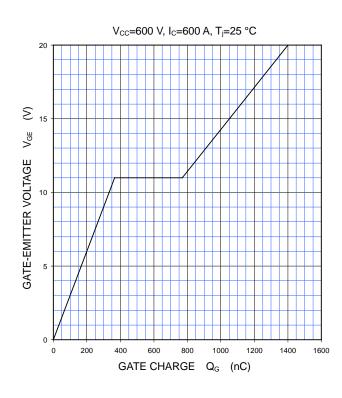
PERFORMANCE CURVES

INVERTER PART

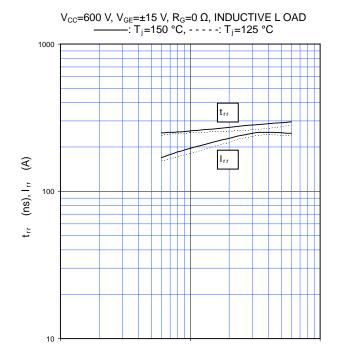
CAPACITANCE CHARACTERISTICS (TYPICAL)



GATE CHARGE CHARACTERISTICS (TYPICAL)

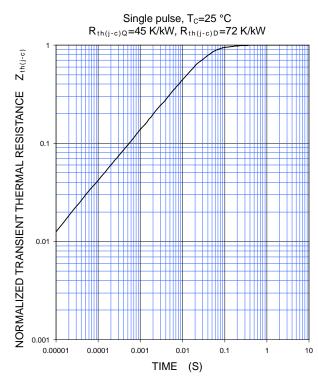


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

EMITTER CURRENT I_E (A)

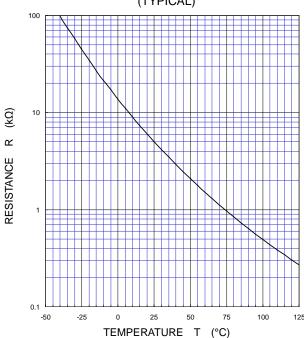


HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)





<IGBT Modules>

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