

<IGBT Modules>

CM600DX-24T/CM600DXP-24T

HIGH POWER SWITCHING USE **INSULATED TYPE**



Collector current Ic 600A Maximum junction temperature T_{vjmax} 175°C

- •Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- •Tin-plating pin terminals



Collector current Ic Maximum junction temperature T_{vjmax} 175°C

- Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- Tin-plating pressfit terminals
- •UL Recognized under UL1557, File No. E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.

dual switch (half-bridge)

OPTION (Below options are available.)

- •PC-TIM (Phase Change Thermal Interface Material) pre-apply
- V_{CEsat} selection for parallel connection

INTERNAL CONNECTION

TERMINAL CODE 1. TH1 6. C2E1 2. TH2 7. C2E1 3. G1 8. G2 4. Es1 9. Es2 Di2 5. Cs1 10. E2 11. C1 NTC

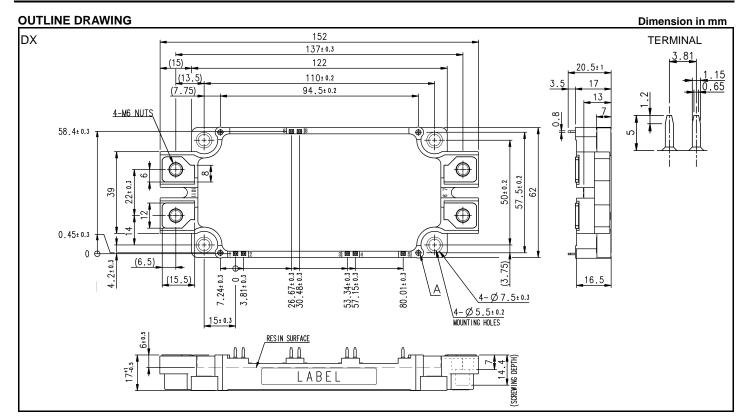
OUTLINE DRAWING Dimension in mm MOUNTING HOLES SECTION A COM. <u>(6.5)</u> Ø2.6 Ø2.32 28 働

1

Publication Date: September 2017 CMH-11084-B Ver.1.2

HIGH POWER SWITCHING USE

INSULATED TYPE

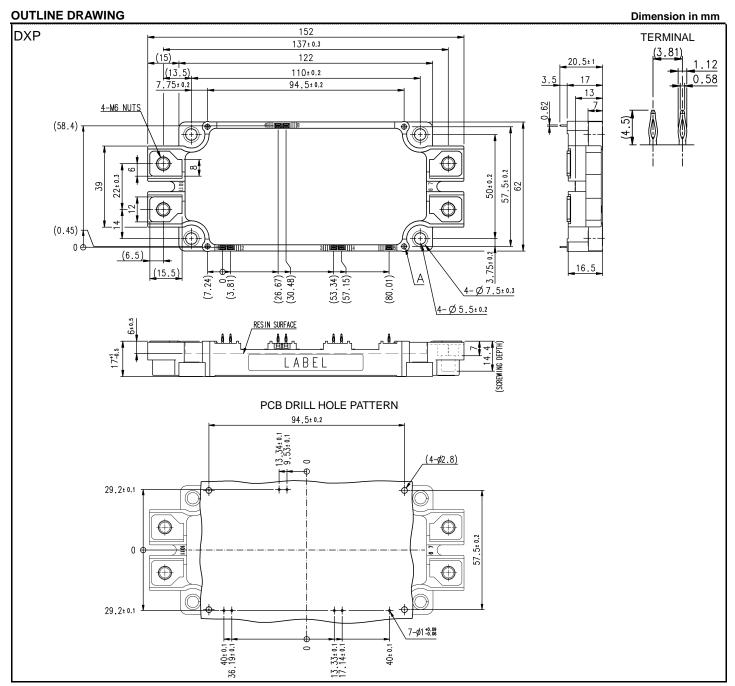


Tolerance otherwise specified

Divisio	n of	Tolerance		
0.5		to	3	±0.2
over	3	to	6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over 120		to 400		±1.2

HIGH POWER SWITCHING USE

INSULATED TYPE



Tolerance otherwise specified

Divisio	n of	Tolerance					
	0.5	to	3	±0.2			
over 3		to	6	±0.3			
over	6	to	30	±0.5			
over	30	to 120		±0.8			
over 120		to 400		±1.2			

HIGH POWER SWITCHING USE

INSULATED TYPE

MAXIMUM RATINGS (T_{vj} =25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V	
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Collector current	DC, T _C =114 °C (Note2, 4)	600	Λ	
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	1200	A	
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	3125	W	
I _E (Note1)	Emitter current	DC (Note2)	600	^	
I _{ERM} (Note1)	Emilier current	Pulse, Repetitive (Note3)	1200	Α	

MODULE

Symbol	Item Conditions		Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	
T _{vjop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ + 150	°C
T _{stq}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_{vj} =25 °C, unless otherwise specified) INVERTER PART IGBT/FWD

Symbol Item		Conditions		Limits			Linit
Symbol	item	Conditions			Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited	V _{CE} =V _{CES} , G-E short-circuited			1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	Ic=60 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =600 A, V _{GE} =15 V,	T _{vj} =25 °C	-	1.65	2.05	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _{vj} =125 °C	-	1.85	-	V
(Terminal)	Callantar are the restriction value of	(Note5)	T _{vj} =150 °C	-	1.90	-	
	Collector-emitter saturation voltage	Ic=600 A,	T _{vj} =25 °C	-	1.50	1.75	
V _{CEsat}		V _{GE} =15 V,	T _{vj} =125 °C	-	1.70	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	1.75	-	
Cies	Input capacitance			-	-	145.5	nF
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited	-	-	4.1		
Cres	Reverse transfer capacitance	7	-	-	1.8		
Q _G	Gate charge	V _{CC} =600 V, I _C =600 A, V _{GE} =15 V	-	4.5	-	μC	
t _{d(on)}	Turn-on delay time	V_{CC} =600 V, I _C =600 A, V_{GE} =±15 V, R_{G} =1.0 Ω, Inductive load		-	-	600	ns
tr	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time			-	-	800	
t _f	Fall time			-	-	400	
Nata1)		I _E =600 A, G-E short-circuited,	T _{vj} =25 °C	-	1.75	2.25	V
V _{EC} (Note1)		Refer to the figure of test circuit	T _{vj} =125 °C	-	1.90	-	
(Terminal)	Emitter collector voltage	(Note5)	T _{vj} =150 °C	-	1.95	-	
Nata1)	- Emitter-collector voltage	I _E =600 A,	T _{vj} =25 °C	-	1.60	1.95	
V _{EC} (Note1) (Chip)		G-E short-circuited,	T _{vj} =125 °C	-	1.60	-	
(Criip)		(Note5)	T _{vj} =150 °C	-	1.60	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =600 A, V _{GE} =±15 V,		-	-	400	ns
Q _{rr} (Note1)	Reverse recovery charge	R_G =1.0 Ω, Inductive load		-	46.8	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =600 A,	V _{CC} =600 V, I _C =I _E =600 A,		50.6	-	m l
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=1.0 \Omega, T_{vj}=150 \text{ °C},$		-	61.9	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	48.5	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =2	5 °C (Note4)	-	0.71	-	mΩ
r _g	Internal gate resistance	Per switch		-	0.67	-	Ω

4

Publication Date : September 2017 CMH-11084-B Ver.1.2

HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; Tvj=25 °C, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
	item	Conditions	Min.	Тур.	Max.	Offic
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions			Unit		
Symbol	item			Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)		-	-	48	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)		-	-	76	N/KVV
D	Contact thermal resistance	Case to heat sink,	Thermal grease applied (Note4, 7)	i	11.5	-	K/kW
$R_{th(c-s)}$		per 1 module,	PC-TIM applied (Note4, 8)	ı	3.1	i	r/KVV

MECHANICAL CHARACTERISTICS

Cumala al	lá a sa	000		1.1-20			
Symbol	Item	Con	Min.	Тур.	Max.	Unit	
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
		Caldennia tura (DV)	Terminal to terminal	17	-	-	mm
يا.	Creepage distance	Solder pin type (DX)	Terminal to base plate	16.4	-	-	
ds		Pressfit pin type (DXP)	Terminal to terminal	17	-	-	- mm
			Terminal to base plate	16.8	-	-	
		Solder pin type (DX)	Terminal to terminal	10	-	-	mm
يا.			Terminal to base plate	16.2	-	-	
d _a	Clearance	5 (1) (2)(2)	Terminal to terminal	10	-	-	
		Pressfit pin type (DXP) Terminal to base plate		16.2	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note9)		±0	-	+200	μm
m	mass	-		-	300	-	g

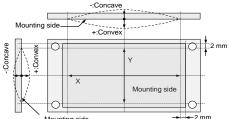
- *: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.
- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).
 - 2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
 - 3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed T_{vjmax} rating.
 - 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 R_{25} : resistance at absolute temperature T_{25} [K]; T_{25} =25 [°C]+273.15=298.15 [K]

 $R_{50}\!\!:$ resistance at absolute temperature T_{50} [K]; $T_{50}\!\!=\!\!50$ [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K)/D_(C-S)=50 μ m.
- 8. Typical value is measured by using PC-TIM of λ =3.4 W/(m·K)/D_(C·S)=50 μ m.
- 9. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



<IGBT Modules>

CM600DX-24T/CM600DXP-24T

HIGH POWER SWITCHING USE

INSULATED TYPE

Note10. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness : t=1.6

	Туре	Manufacturer	Size	Tightening torque (N•m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N·m	by handwork (equivalent to 30 rpm
(3)	DELTA PT®		25×8	0.55 ± 0.055 N·m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N·m	~ 600 rpm (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N·m	
	tapping screw		φ2.6×12	0.75 ± 0.075 N-III	

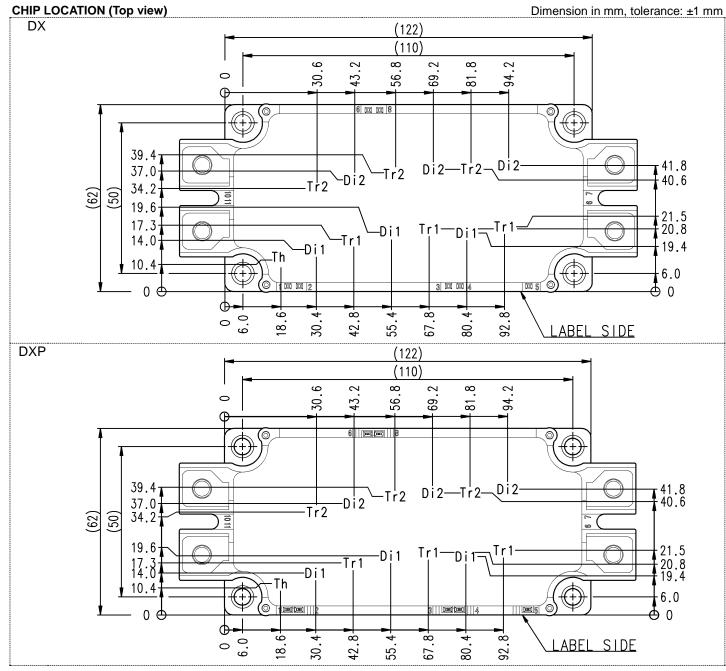
RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions		Unit		
Symbol	item	Conditions	Min.	Тур.	Max.	Onit
Vcc	(DC) Supply voltage	Applied across C1-E2 terminals	ı	600	850	٧
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-E1s/G2-E2s terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	1.0	-	6.8	Ω

Publication Date : September 2017 CMH-11084-B Ver.1.2

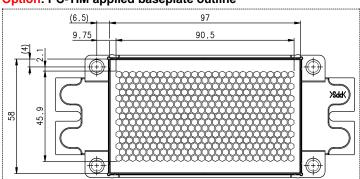
HIGH POWER SWITCHING USE

INSULATED TYPE



Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

Option: PC-TIM applied baseplate outline

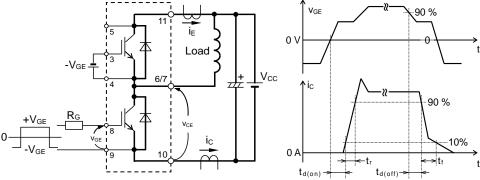


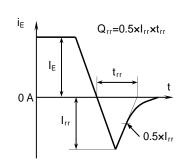
Ver.1.2

HIGH POWER SWITCHING USE

INSULATED TYPE

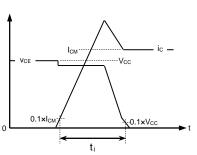
TEST CIRCUIT AND WAVEFORMS

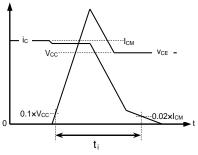


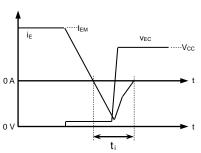


Switching characteristics test circuit and waveforms









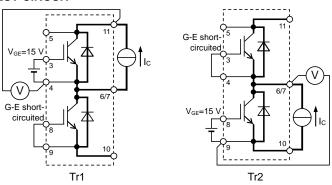
IGBT Turn-on switching energy

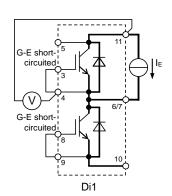
IGBT Turn-off switching energy

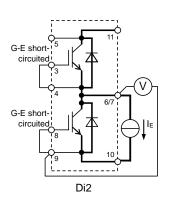
FWD Reverse recovery energy

Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT







V_{CEsat} characteristics test circuit

V_{EC} characteristics test circuit

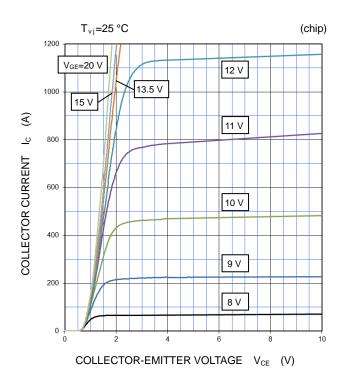
HIGH POWER SWITCHING USE

INSULATED TYPE

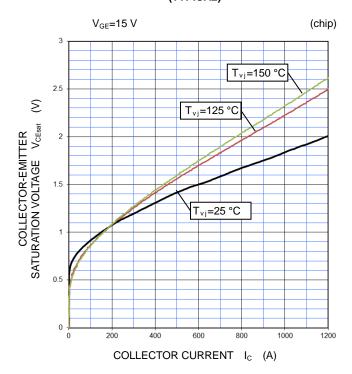
PERFORMANCE CURVES

INVERTER PART

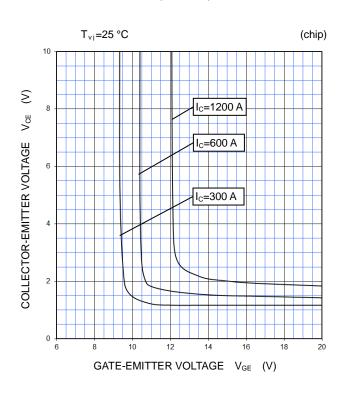
OUTPUT CHARACTERISTICS (TYPICAL)



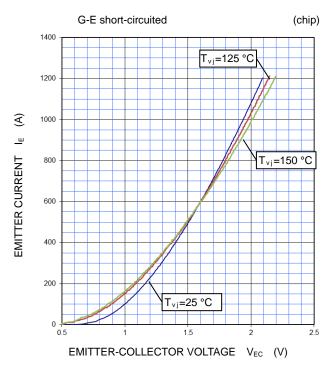
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



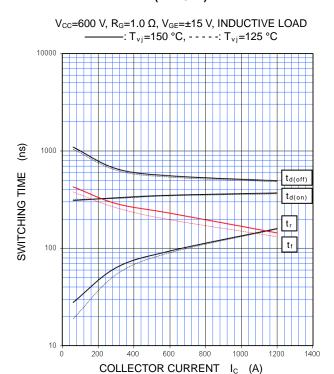
HIGH POWER SWITCHING USE

INSULATED TYPE

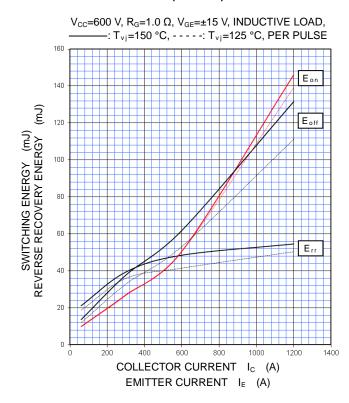
PERFORMANCE CURVES

INVERTER PART

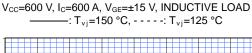
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

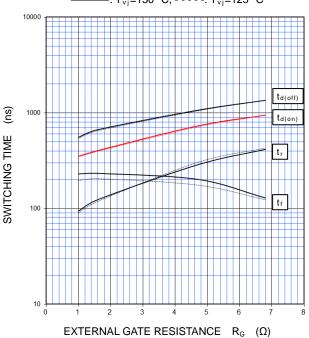


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



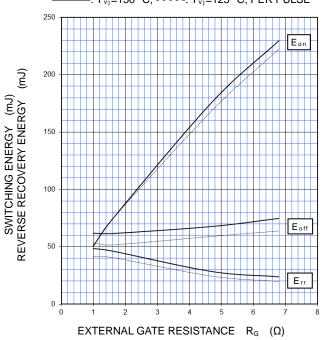
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)





HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, I_C/I_E =600 A, V_{GE} =±15 V, INDUCTIVE LOAD, -: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C, PER PULSE



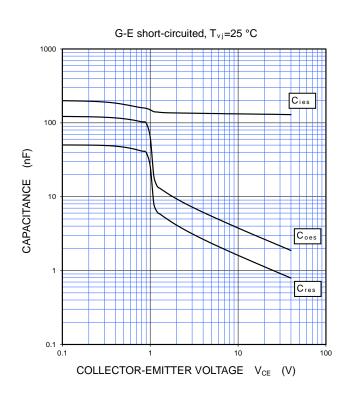
HIGH POWER SWITCHING USE

INSULATED TYPE

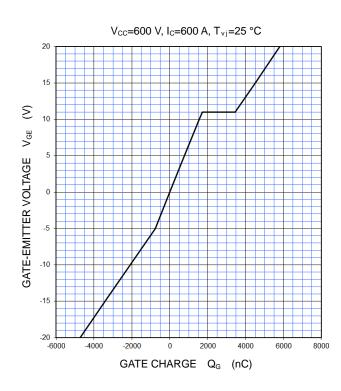
PERFORMANCE CURVES

INVERTER PART

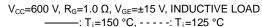
CAPACITANCE CHARACTERISTICS (TYPICAL)

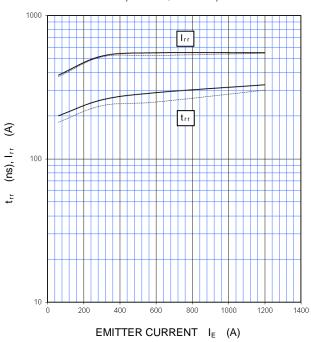


GATE CHARGE CHARACTERISTICS (TYPICAL)

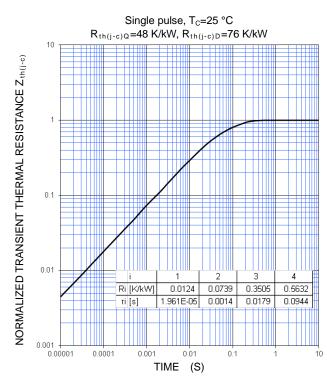


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)





TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



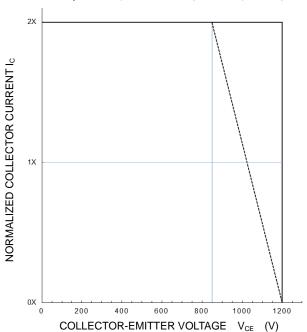
HIGH POWER SWITCHING USE

INSULATED TYPE

PERFORMANCE CURVES

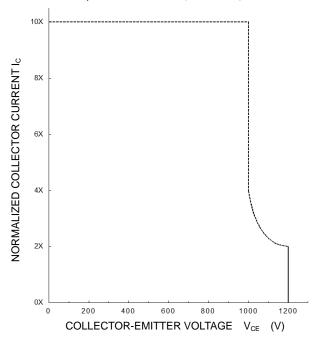
INVERTER PART

TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)



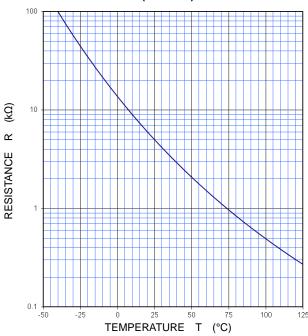
SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{CC} \le 800 \text{ V}$, $R_G = 1.0 \sim 6.8 \Omega$, $V_{GE} = \pm 15 \text{ V}$, $T_{vj} = 25 \sim 150 \text{ °C}$, $t_W \le 8 \mu \text{s}$, Non-Repetitive



NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

Keep safety first in your circuit designs!

This product is designed for industrial application purpose. The performance, the quality and support level of the product is guaranteed by "Customer's Std. Spec.".

Mitsubishi Electric Corporation puts its reasonable effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them by the reliability lifetime such as Power Cycle, Thermal Cycle or others, or to be used under special circumstances(e.g. high humidity, dusty, salty, highlands, environment with lots of organic matter / corrosive gas / explosive gas, or situation which terminal of semiconductor products is received strong mechanical stress). In the customer's research and development, please evaluate it not only with a single semiconductor product but also in the entire system, and judge whether it's applicable. Furthermore, trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits (e.g. appropriate fuse or circuit breaker between a power supply and semiconductor products), (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

- •These materials are intended as a reference to assist our customers in the selection of the Mitsubishi semiconductor product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Mitsubishi Electric Corporation or a third party.
- •Mitsubishi Electric Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, or circuit application examples contained in these materials.
- •All information contained in these materials, including product data, diagrams and charts represents information on products at the time of publication of these materials, and are subject to change by Mitsubishi Electric Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor for the latest product information before purchasing a product listed herein.

The information described here may contain technical inaccuracies or typographical errors. Mitsubishi Electric Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.

Please also pay attention to information published by Mitsubishi Electric Corporation by various means, including the Mitsubishi Semiconductor home page (www.MitsubishiElectric.com/semiconductors/).

- •When using any or all of the information contained in these materials, including product data, diagrams, and charts, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Mitsubishi Electric Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
- •Mitsubishi Electric Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Therefore, this product should not be used in such applications. Please contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
- •In the case of new requirement is available, this material will be revised upon consultation.
- •The prior written approval of Mitsubishi Electric Corporation is necessary to reprint or reproduce in whole or in part these materials.
- •If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
- Any diversion or re-export contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
- •Please contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor for further details on these materials or the products contained therein.

Generally the listed company name and the brand name are the trademarks or registered trademarks of the respective companies.

© 2017 MITSUBISHI ELECTRIC CORPORATION. ALL RIGHTS RESERVED.