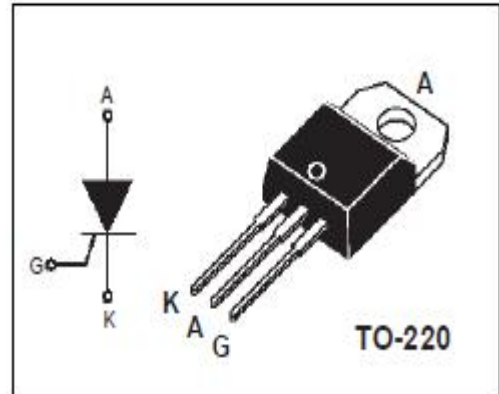


isc Thyristors**CMA30E1600PB****DESCRIPTION**

- With TO-220 packaging
- Long-term stability
- Thyristor for line frequency
- Planar passivated chip
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Switching applications
- Line rectifying 50/60 Hz

**ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)**

SYMBOL	PARAMETER	MIN	UNIT
V_{DRM}	Repetitive peak off-state voltage	1600	V
V_{RRM}	Repetitive peak reverse voltage	1600	V
$I_{\text{T(AV)}}$	Average forward current $T_c=40^{\circ}\text{C}$	30	A
$I_{\text{T(RMS)}}$	RMS on-state current	36	A
I_{TSM}	Surge non-repetitive on-state current (1/2 cycle,sine wave; $T_c=45^{\circ}\text{C}$)	260 280	A
$P_{\text{G(AV)}}$	Average gate power dissipation	0.5	W
T_j	Operating junction temperature	-40~125	$^{\circ}\text{C}$
T_{stg}	Storage temperature	-40~150	$^{\circ}\text{C}$

isc Thyristors

CMA30E1600PB

ELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
I _{RRM}	Repetitive peak reverse current	V _{RM} =V _{RRM} V _{DM} =V _{DRM}	T _j =25°C		0.01	mA
I _{DRM}	Repetitive peak off-state current		T _j =125°C		2	
V _{TM}	On-state voltage	I _{TM} = 30A I _{TM} = 60A			1.42 1.80	V
I _{GT}	Gate-trigger current	V _D = 6V; R _L =100 Ω			28	mA
V _{GT}	Gate-trigger voltage	V _D = 6V; R _L =100 Ω			1.3	V
R _{th(j-c)}	Thermal resistance	Junction to case			2.5	°C/W

NOTICE:

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