

#### **N-Channel Enhancement Mode Field Effect Transistor**

#### **General Description**

The 1404 is a N-channel Power MOSFET. It has specifically been designed to minimize input capacitance and gate charge. The device is therefore suitable in advanced high-efficiency switching applications.

#### **Features**

- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

## Absolute Maximum Ratings

# **Product Summery**

BVDSS	RDSON	ID
40V	5.5mΩ	140A

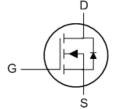
#### **Applications**

- LED power controller
- DC-DC & DC-AC converters
- High current, High speed switching
- Solenoid and relay drivers
- Motor control, Audio amplifiers

#### TO220 / TO263 Pin Configuration







Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current	140	Α
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current	99	Α
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	420	Α
EAS	Single Pulse Avalanche Energy(Thermally limited) <sup>2</sup>	240	mJ
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation	200	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	°C
$T_J$	Operating Junction Temperature Range -55 to 175		°C

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit	
$R_{ heta JA}$	Thermal Resistance Junction-ambient <sup>3</sup>		62	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction-case		1.05	°C/W	



#### **N-Channel Enhancement Mode Field Effect Transistor**

## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	40			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.032		V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =75A 4			5.5	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2		4	V
	Drain-Source Leakage Current	V <sub>DS</sub> =Max rating, V <sub>GS</sub> =0V			20	uA
I <sub>DSS</sub>		V <sub>DS</sub> =Max rating, V <sub>GS</sub> =0V@125℃			250	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm 20 V$ , $V_{DS}$ = $0 V$			±100	nA
Qg	Total Gate Charge	I <sub>D</sub> =75A		65		nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =32V		21		
Q <sub>gd</sub>	Gate-Drain Charge	V <sub>GS</sub> = 10 V 4		30		
T <sub>d(on)</sub>	Turn-On Delay Time	$\begin{array}{cccccccccccccccccccccccccccccccccccc$		18		
Tr	Rise Time			148		
T <sub>d(off)</sub>	Turn-Off Delay Time			42		ns
T <sub>f</sub>	Fall Time			74		
C <sub>iss</sub>	Input Capacitance			3200		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz		680		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			415		

## **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	-V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			75	Α
I <sub>SM</sub>	Pulsed Source Current <sup>1</sup>				420	Α
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =75 A , T <sub>J</sub> =25℃ 4			1.3	V

#### Note:

<sup>1.</sup>Repetitive rating; pulse width limited by max. junction temperature.

<sup>2.</sup>Limited by TJmax, starting TJ =  $25^{\circ}$ C, L = 0.04mH RG =  $25\Omega$ , IAS = 75A, VGS =10V. Part not recommended for use above this value.

<sup>3.</sup> This is only applied to TO-220AB pakcage.

<sup>4.</sup>Pulse width ≤ 1.0ms; duty cycle ≤ 2%.