





## **SOT-23 Formed SMD Package**

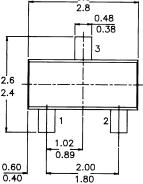
## **CMBT4125**

# GENERAL PURPOSE TRANSISTOR

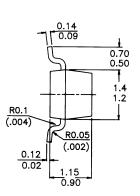
P-N-P transistor

Marking CMBT4125 = 5D PACKAGE OUTLINE DETAILS ALL DIMENSIONS IN mm





3.0



# Pin configuration

1 = BASE

2 = EMITTER

3 = COLLECTOR



#### ABSOLUTE MAXIMUM RATINGS

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	30	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	30	V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	4	V
Collector current (d.c.)	$-I_C$	max.	<i>200</i>	mA
Total power dissipation at $T_{amb} = 25^{\circ}C$	$P_{tot}$	max	<i>350</i>	mW
D.C. current gain				
I. 9 mA, V. 1 V	h	min.	<i>50</i>	
$-I_C = 2 \text{ mA}; -V_{CE} = 1 \text{ V}$	hFE	max.	150	

## **RATINGS** (at $T_A = 25^{\circ}C$ unless otherwise specified)

Limiting values

Collector-base voltage (open emitter)  $-V_{CBO}$ 30 V max. 30 V Collector-emitter voltage (open base)  $-V_{CEO}$ max.

# CMBT4125

Emitter-base voltage (open collector)  Collector current (d.c.)  Total power dissipation at T <sub>amb</sub> = 25°C  Storage temperature  Junction temperature  THERMAL CHARACTERISTICS	$-V_{EBO}$ $-I_{C}$ $P_{tot}$ $T_{stg}$ $Tj$	max. max. max -55 max.	4 200 350 to +150 150	V mA mW ° C ° C
$T_j = P (R_{th j-t} + R_{th s-a}) + T_{amb}$				
Thermal resistance from junction to ambient	$R_{th\ j-a}$		556	°C/mW
<b>CHARACTERISTICS</b> (at $T_A = 25^{\circ}C$ unless otherwise Collector-emitter breakdown voltage	e specified)			
$-I_C = 1 \text{ mA}$ ; $I_B = 0$ Collector-base breakdown voltage	-V <sub>(BR)</sub> CEO	min.	30	V
Conector-base breakdown voltage $-I_C = 10 \text{ mA}; I_E = 0$ Emitter-base breakdown voltage	-V <sub>(BR)</sub> CBO	min.	30	V
$-I_E = 10 \text{ mA}$ ; $I_C = 0$ Collector cut-off current	-V <sub>(BR)EBO</sub>	min.	4	V
$-V_{CB} = 20 \ V; \ I_E = 0 \ V$	$-I_{CBO}$	max.	50	nA
Emitter cut-off current $V_{BE} = 3 \ V; I_C = 0$	$I_{EBO}$	max.	50	nA
Output capacitance at $f = 100 \text{ kHz}$ $I_E = 0$ ; $-V_{CB} = 5 \text{ V}$	$C_{c}$	may	15	nΕ
IE = 0, $-vCB = 3vInput capacitance at f = 100 \text{ kHz}$	$c_c$	max.	4.5	pF
$I_C = 0$ ; $-V_{BE} = 0.5 V$	$C_e$	max.	10	pF
Saturation voltages				
$-I_C = 50 \text{ mA}; -I_B = 5 \text{ mA}$	-V <sub>CEsat</sub>	max.	0.4	V
$-I_C = 50 \text{ mA}; -I_B = 5 \text{ mA}$	-V <sub>BEsat</sub>	max.	0.95	V
D.C. current gain	7	min.	50	
$-I_C = 2 mA; -V_{CE} = 1 V$	$h_{FE}$	max.	150	
$-I_C = 50 \text{ mA}; -V_{CE} = 1 \text{ V}$ Noise figure at $R_S = 1 \text{ kW}$	$h_{FE}$	min.	25	
$-I_C = 100 \text{ mA}; -V_{CE} = 5 \text{ V}$				
f = 10  Hz to  15.7  kHz	NF	max.	5	dΒ
Small signal current gain				
$-V_{CE} = 1V$ ; $-I_{C} = 2 \text{ mA}$ ; $f = 1 \text{ KHz}$	$h_{fe}$	min. max.	50 150	
Transition frequency				
$-V_{CE} = 20V; -I_{C} = 10 \text{ mA}; f = 100 \text{ MHz}$	$f_T$	min.	200	MHz

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