

General Description

The 50N03 is N-ch MOSFETs with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

Features

- Simple Drive Requirement
- Fast Switching
- Low On-Resistance

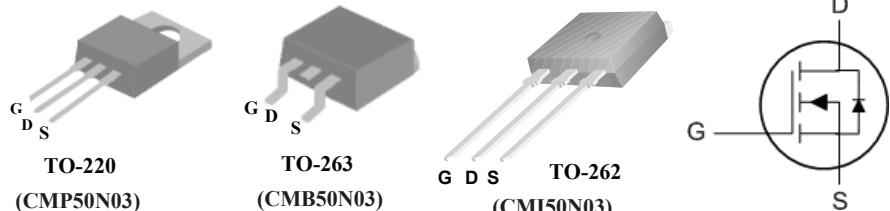
Product Summery

BVDSS	RDSON	ID
30V	12mΩ	50A

Applications

- LED POWER CONTROLLER
- DC-DC & DC-AC CONVERTERS
- HIGH CURRENT, HIGH SPEED SWITCHING
- SOLENOID AND RELAY DRIVERS
- MOTOR CONTROL, AUDIO AMPLIFIERS

TO220 / TO263/TO262 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current ¹	50	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current ¹	40	A
I_{DM}	Pulsed Drain Current ²	150	A
EAS	Single Pulse Avalanche Energy ³	45	mJ
I_{AS}	Avalanche Current	50	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	64	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	---	1.5	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	30	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.025	---	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=10\text{V}$, $I_D=25\text{A}$	---	10.5	12	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=25\text{A}$	---	16	20	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1	---	3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=24\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	μA
		$V_{\text{DS}}=24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_C=125^\circ\text{C}$	---	---	25	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_D=25\text{A}$	---	50	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1.3	---	Ω
Q_g	Total Gate Charge	$I_D=25\text{A}$	---	23	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=15\text{V}$	---	8.6	---	
Q_{gd}	Gate-Drain Charge	$V_{\text{GS}}=4.5\text{V}$	---	7.3	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DS}}=15\text{V}$	---	12.2	---	ns
T_r	Rise Time	$I_D=1\text{A}$	---	10	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$R_G=6\Omega$	---	45	---	
T_f	Fall Time	$V_{\text{GS}}=10\text{V}$	---	15.5	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2050	---	pF
C_{oss}	Output Capacitance		---	389	---	
C_{rss}	Reverse Transfer Capacitance		---	153	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ¹	$V_G=V_D=0\text{V}$, Force Current	---	---	50	A
I_{SM}	Pulsed Source Current ²		---	---	150	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=25\text{ A}$, $T_J=25^\circ\text{C}$	---	---	1.3	V

Note :

1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$ 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.1\text{mH}$, $I_D=12\text{A}$