

General Description

The CMN2305 is P-channel enhancement mode Power MOSFET, designed in serried ranks. With fast switching speed, low on-resistance, favorable stabilization.

Used in commercial and industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

Features

- RDS(ON)<65mΩ @ VGS=-4.5V
- RDS(ON)<80mΩ @ VGS=-2.5V
- Simple drive requirement
- Surface mount package

Absolute Maximum Ratings

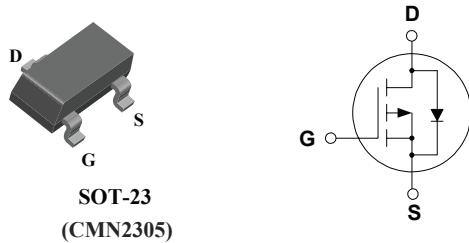
Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	± 10	V
$I_D@T_C=25^\circ C$	Continuous Drain Current	-3.9	A
I_{DM}	Pulsed Drain Current	-15	A
$P_D@T_C=25^\circ C$	Total Power Dissipation	1.2	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Product Summery

BVDSS	RDS(on)	ID
-20V	65mΩ	-3.9A

Applications

- PWM applications
- Load switch
- Power management
- PA Switch

SOT-23 Pin Configuration**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	90	°C/W

P-Channel Enhancement Mode Field Effect Transistor

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-20	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-4.5\text{V}$, $I_D=-3.9\text{A}$	---	---	65	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}$, $I_D=-2\text{A}$	---	---	80	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=-250\mu\text{A}$	-0.5	---	-1.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-16\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 8\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_D=-4\text{A}$	---	9	---	S
Q_g	Total Gate Charge	$I_D=-3.5\text{A}$	---	7.5	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=-4\text{V}$	---	1.4	---	
Q_{gd}	Gate-Drain Charge	$V_{\text{GS}}=-4.5\text{V}$	---	1.2	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DS}}=-10\text{V}$	---	8	---	ns
T_r	Rise Time	$R_G=6\Omega$	---	21	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$I_D=-2.8\text{A}$	---	60	---	
T_f	Fall Time		---	18	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-10\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	600	---	pF
C_{oss}	Output Capacitance		---	120	---	
C_{rss}	Reverse Transfer Capacitance		---	94	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_S=-0.75\text{A}$	---	---	-1.5	V