

General Description

The CMN2308 is the N-Channel enhancement mode power field effect transistors using advanced trench technology to provide excellent RDS(ON). These devices are particularly suited for low voltage application , such as high side power loss of mobile phone and notebook computer power management and other battery power supply circuit of the switch and the low line, need to be in a very small outline surface mount package.

Features

- RDS(ON)<105mΩ @ VGS=10V
- RDS(ON)<125mΩ @ VGS=4.5V
- SOT-23 Package

Absolute Maximum Ratings

| Symbol | Parameter | Rating | Units |
|--------------------------------------|--------------------------------------|------------|-------|
| V _{DS} | Drain-Source Voltage | 60 | V |
| V _{GS} | Gate-Source Voltage | ±20 | V |
| I _D @T _C =25°C | Continuous Drain Current | 3 | A |
| I _{DM} | Pulsed Drain Current | 5 | A |
| P _D @T _C =25°C | Total Power Dissipation | 1 | W |
| T _{STG} | Storage Temperature Range | -55 to 150 | °C |
| T _J | Operating Junction Temperature Range | -55 to 150 | °C |

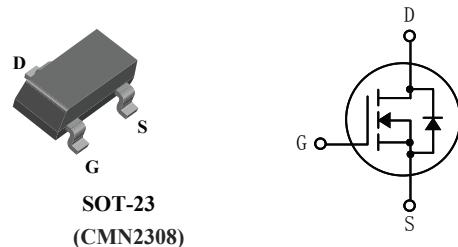
Product Summary

| BVDSS | RDSON | ID |
|-------|-------|----|
| 60V | 105mΩ | 3A |

Applications

- DC-DC converters
- Portable Equipment
- LCD Display inverter
- Load Switch

SOT-23 Pin Configuration



Thermal Data

| Symbol | Parameter | Rating | Unit |
|------------------|-------------------------------------|--------|------|
| R _{θJA} | Thermal Resistance Junction-ambient | 150 | °C/W |

N-Channel Enhancement Mode Field Effect Transistor

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|--------------------------|-----------------------------------|--|------|------|-----------|------------------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$ | 60 | --- | --- | V |
| $R_{\text{DS(ON)}}$ | Static Drain-Source On-Resistance | $V_{\text{GS}}=10\text{V}$, $I_D=3\text{A}$ | --- | --- | 105 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=4.5\text{V}$, $I_D=3\text{A}$ | --- | --- | 125 | |
| $V_{\text{GS(th)}}$ | Gate Threshold Voltage | $V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$ | 0.8 | --- | 1.4 | V |
| I_{DSS} | Drain-Source Leakage Current | $V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$ | --- | --- | 1 | μA |
| I_{GSS} | Gate-Source Leakage Current | $V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$ | --- | --- | ± 100 | nA |
| Q_g | Total Gate Charge | $I_D=3\text{A}$ | --- | 6.8 | --- | nC |
| Q_{gs} | Gate-Source Charge | $V_{\text{DS}}=30\text{V}$ | --- | 2.5 | --- | |
| Q_{gd} | Gate-Drain Charge | $V_{\text{GS}}=4.5\text{V}$ | --- | 3.0 | --- | |
| $T_{\text{d(on)}}$ | Turn-On Delay Time | $V_{\text{DS}}=30\text{V}$, $I_D=0.2\text{A}$, $R_G=25\Omega$ | --- | --- | 40 | ns |
| $T_{\text{d(off)}}$ | Turn-Off Delay Time | | --- | --- | 80 | |
| C_{iss} | Input Capacitance | $V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$ | --- | --- | 550 | pF |
| C_{oss} | Output Capacitance | | --- | --- | 125 | |
| C_{rss} | Reverse Transfer Capacitance | | --- | --- | 45 | |

Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-----------------|-----------------------|---|------|------|------|------|
| V_{SD} | Diode Forward Voltage | $V_{\text{GS}}=0\text{V}$, $I_S=1\text{A}$ | --- | --- | 1.5 | V |